

PWM Control 2A Step-Down Converter

❖ GENERAL DESCRIPTION

AX3105 consists of step-down switching regulator with PWM control. These device include a reference voltage source, oscillation circuit, error amplifier, internal PMOS and etc.

AX3105 provides low-ripple power, high efficiency, and excellent transient characteristics. The PWM control circuit is able to vary the duty ratio linearly from 0 up to 100%. This converter also contains an error amplifier circuit as well as a soft-start circuit that prevents overshoot at startup. An enable function, an over current protect function and short circuit protect function are built inside, and when OCP or SCP happens, the operation frequency will be reduced. Also, an internal compensation block is built in to minimum external component count.

With the addition of an internal P-channel Power MOS, a coil, capacitors, and a diode connected externally, these ICs can function as step-down switching regulators. They serve as ideal power supply units for portable devices when coupled with the SOP-8L package, providing such outstanding features as low current consumption. Since this converter can accommodate an input voltage up to 23V, it is also suitable for the operation via an AC adapter.

❖ FEATURES

- Input voltage : 3.6V to 23V
- Output voltage : 0.8V to Vcc
- Duty ratio : 0% to 100% PWM control
- Oscillation frequency : 330KHz typ.
- Soft-start(SS), Current Limit(CL), Enable function.
- Thermal Shutdown function.
- Short Circuit Protect (SCP).
- Built-in internal SW P-channel MOS.
- Low ESR output capacitor (Multi-layer chip capacitor (MLCC)) application.
- SOP-8L Pb-Free package.

❖ Absolute Maximum Ratings (at Ta=25°C)

Characteristics	Symbol	Rating	Unit
VCC Pin Voltage	V _{CC}	V _{SS} - 0.3 to V _{SS} + 25	V
Feedback Pin Voltage	V _{FB}	V _{SS} - 0.3 to V _{CC}	V
ON/OFF Pin Voltage	V _{EN}	V _{SS} - 0.3 to V _{CC} + 0.3	V
Switch Pin Voltage	V _{SW}	V _{SS} - 0.3 to V _{CC} + 0.3	V
Power Dissipation	PD	Internally limited	mW
Storage Temperature Range	T _{ST}	-40 to +150	°C
Operating Temperature Range	T _{OP}	-20 to +125	°C
Operating Supply Voltage	V _{OP}	+3.6 to +23	V
Thermal Resistance from Junction to case	θ _{JC}	25	°C/W
Thermal Resistance from Junction to ambient	θ _{JA}	70	°C/W

Note : θ_{JA} is measured with the PCB copper area(need connect to SW pins) of approximately 1 in²(Multi-layer).

❖ Electrical Characteristics (VIN = 12V, Ta=25°C, unless otherwise specified)

Characteristics	Symbol	Conditions	Min	Typ	Max	Units
Feedback Voltage	V _{FB}	I _{OUT} =0.1A	0.784	0.8	0.816	V
Quiescent Current	I _{CCQ}	V _{FB} =1.2V force driver off		3	5	mA
Feedback Bias Current	I _{FB}	I _{OUT} =0.1A	-	0.1	0.5	uA
Shutdown Supply Current	I _{SD}	V _{EN} =0V	-	2	10	uA
Current Limit	I _{CL}		3.0	-	-	A
Line Regulation	ΔV _{OUT} /V _{OUT}	V _{CC} = 5V~23V, I _{OUT} =0.2A	-	0.6	1.2	%
Load Regulation	ΔV _{OUT} /V _{OUT}	I _{OUT} = 0.1 to 2A	-	0.15	0.3	%
Oscillation Frequency	F _{OSC}	SW pin	260	330	400	KHz
EN Pin Logic input threshold voltage	V _{SH}	High (regulator ON)	2.0	-	-	V
	V _{SL}	Low (regulator OFF)	-	-	0.8	
EN Pin Input Current	I _{SH}	V _{EN} =2.5V (ON)	-	20	-	uA
	I _{SL}	V _{EN} =0.3V (OFF)	-	-10	-	uA
Soft-Start Time	T _{SS}		0.3	4	8	ms
Internal MOSFET R _{DS(ON)}	R _{DS(ON)}	V _{CC} =5V, V _{FB} =0V	-	100	150	mΩ
		V _{CC} =12V, V _{FB} =0V	-	70	100	
Efficiency	EFFI	V _{OUT} = 5V	I _{OUT} = 1A	-	92	%
			I _{OUT} = 2A	-	92	
Thermal shutdown Temp	TSD			125		°C

❖ Function Descriptions

PWM Control

The AX3105 consists of DC/DC converters that employ a pulse-width modulation (PWM) system. In converters of the AX3105, the pulse width varies in a range from 0 to 100%, according to the load current. The ripple voltage produced by the switching can easily be removed through a filter because the switching frequency remains constant. Therefore, these converters provide a low-ripple power over broad ranges of input voltage and load current.

RDS(ON) Current Limiting

The current limit threshold is setting by the internal circuit.

Setting the Output Voltage

Application circuit item shows the basic application circuit with AX3105 adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 0.8V \times \left(1 + \frac{R1}{R2}\right)$$

Table 1 Resistor select for output voltage setting

V _{OUT}	R2	R1
5V	1.3K	6.8K
	5.6K	30K
3.3V	1.5K	4.7K
	5.6K	18K
2.5V	2.2K	4.7K
	5.6K	12K
1.8V	2K	2.5K
1.5V	2.2K	2.0K
1.2V	3K	1.5K

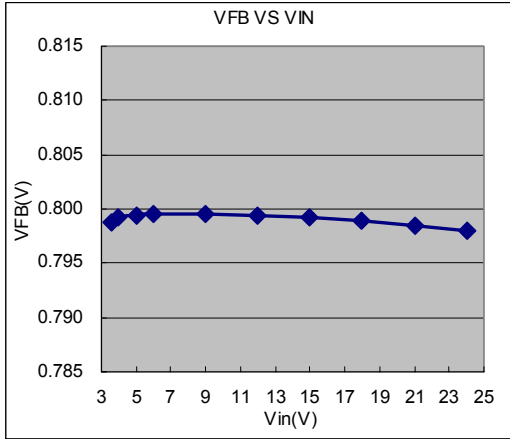
PCB Layout Guide

If you need low T_c & T_j or large PD(Power Dissipation), The dual SW pins(5&6) on the SOP-8L package are internally connected to die pad, The PCB layout should allow for maximum possible copper area at the SW pins.

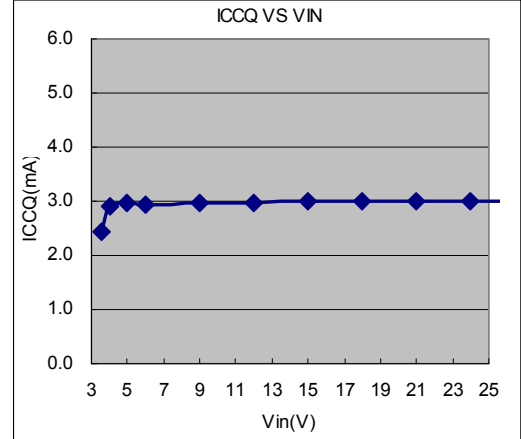
1. Connect C3 to V_{CC} pin as closely as possible to get good power filter effect.
2. Connect R3 to V_{CC} pin as closely as possible.
3. Connect ground side of the C2 & D1 as closely as possible.

❖ Typical Characteristics

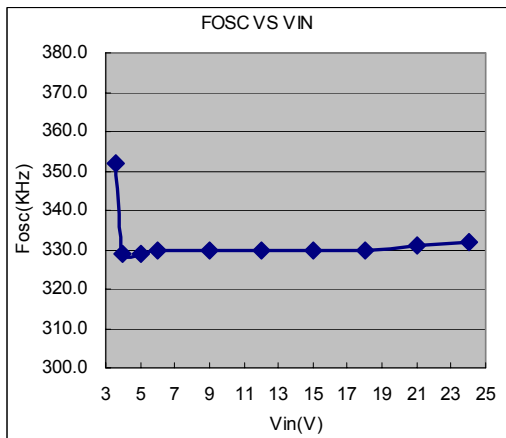
VFB VS VIN



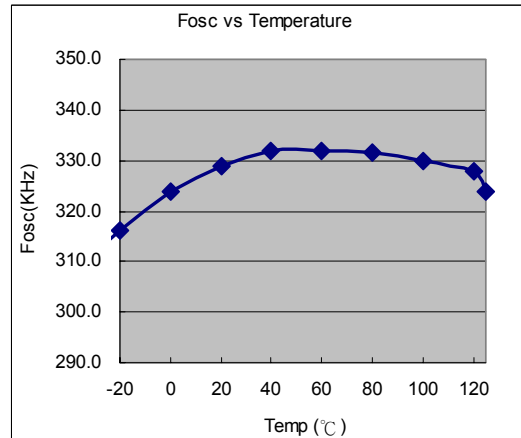
ICCQ VS VIN



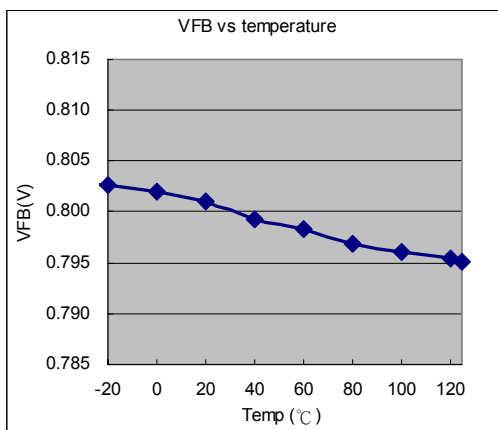
FOSC VS VIN



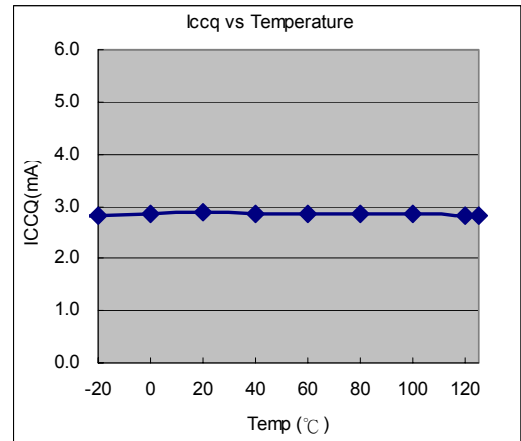
FOSC VS TEMPERATURE



VFB VS TEMPERATURE



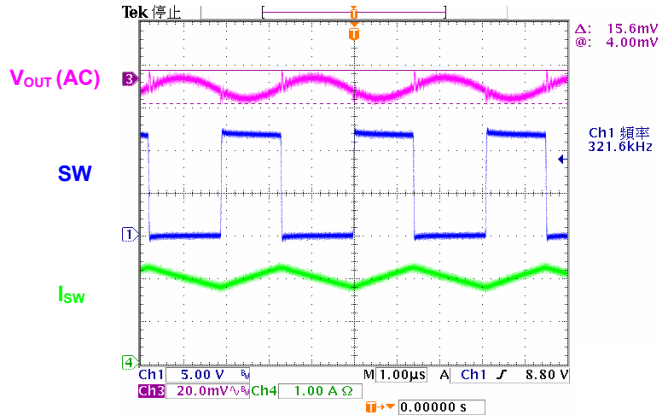
ICCQ VS TEMPERATURE



❖ Typical Characteristics (MLCC)

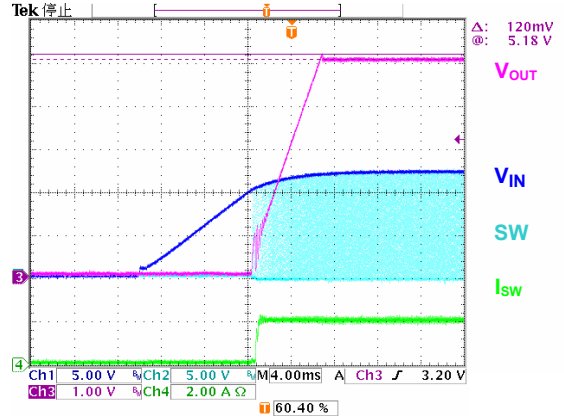
Output Ripple

($V_{IN}=12V, V_{OUT}=5V, I_{OUT}=2A$)



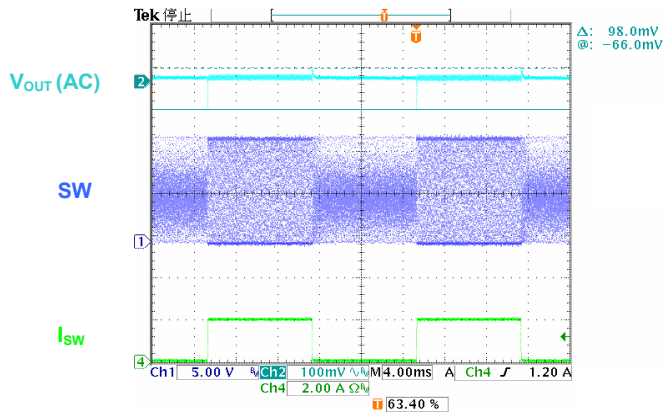
Power on test wave

($V_{IN}=12V, V_{OUT}=5V, I_{OUT}=2A$)



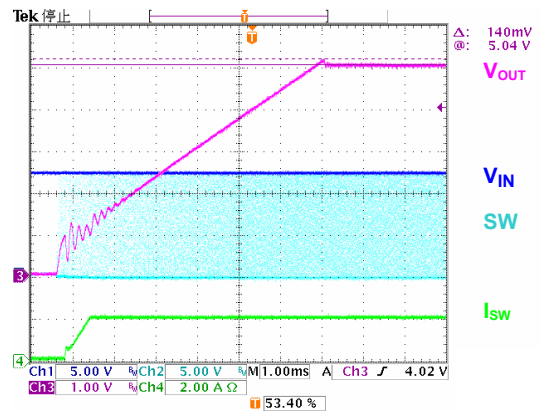
Load Transient Response

($V_{IN}=12V, V_{OUT}=5V, I_{OUT}=0.1\sim 2A$)



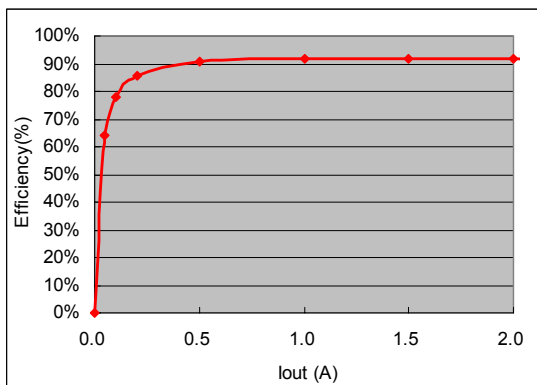
EN on test wave

($V_{IN}=12V, V_{OUT}=5V, I_{OUT}=2A$)



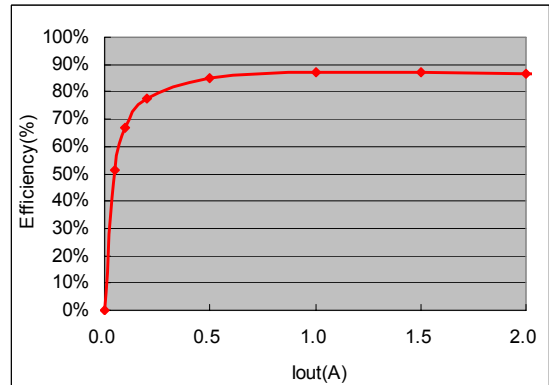
Efficiency

($V_{IN}=12V, V_{OUT}=5V$)

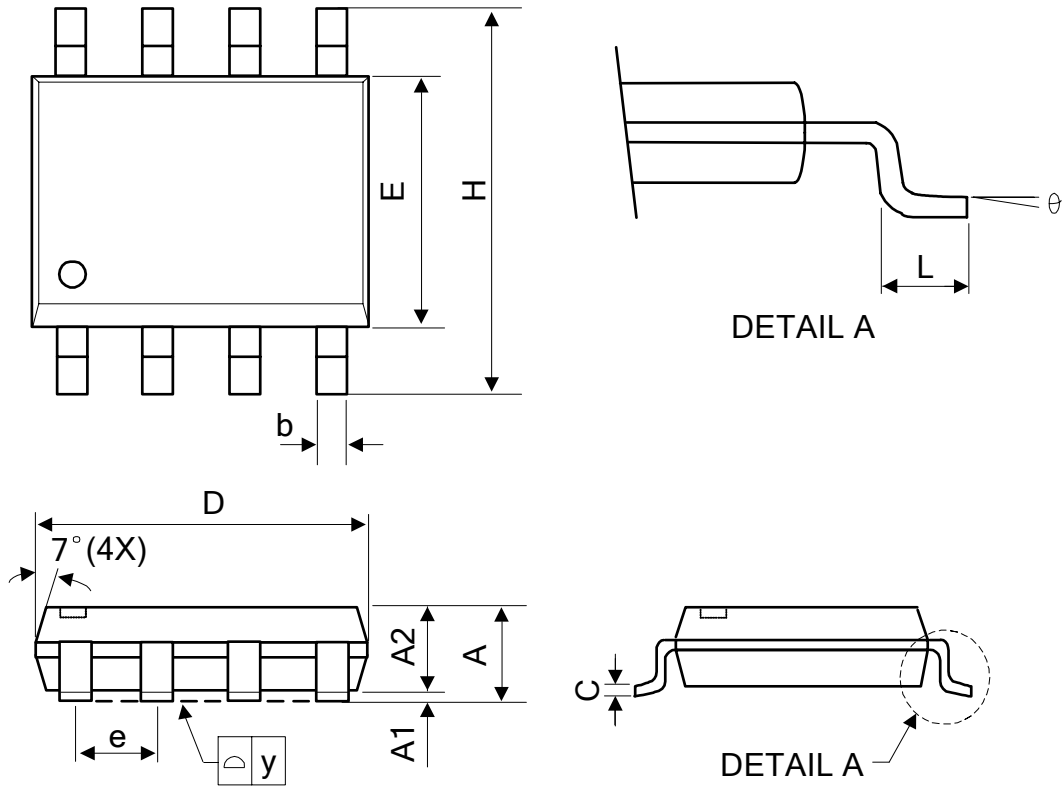


Efficiency

($V_{IN}=12V, V_{OUT}=3.3V$)



❖ Package Outlines



Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	1.40	1.60	1.75	0.055	0.063	0.069
A1	0.10	-	0.25	0.040	-	0.100
A2	1.30	1.45	1.50	0.051	0.057	0.059
C	0.19	0.20	0.25	0.0075	0.008	0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E	3.80	3.90	4.00	0.150	0.154	0.157
H	5.79	5.99	6.20	0.228	0.236	0.244
L	0.38	0.71	1.27	0.015	0.028	0.050
b	0.33	0.41	0.51	0.013	0.016	0.020
e	1.27 TYP			0.050 TYP		
y	-	-	0.10	-	-	0.004
θ	0°	-	8°	0°	-	8°