PWM Control 2A Step-Down Converter

❖ GENERAL DESCRIPTION

AX3106A consists of step-down switching regulator with PWM control. These devise include a reference voltage source, oscillation circuit, error amplifier, internal PMOS and etc.

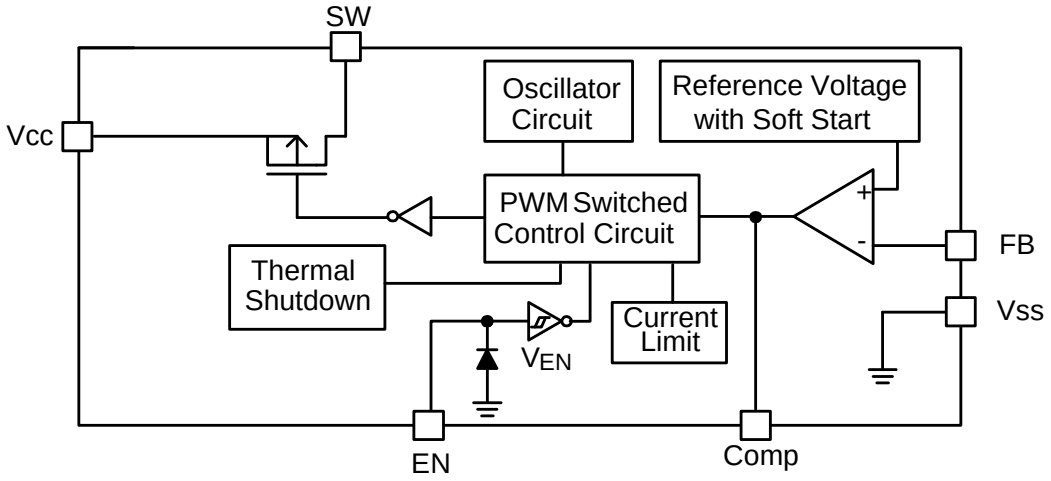
AX3106A provides low-ripple power, high efficiency, and excellent transient characteristics. The PWM control circuit is able to the duty ratio linearly form 0 up to 100%. This converter also contains an error amplifier circuit as well as a soft-start circuit that prevents overshoot at startup. An enable function, an over current protect function and short circuit protect function are built inside, and when OCP or SCP happens, the operation frequency will be reduced. Also, an internal compensation block is built in to minimum external component count.

With the addition of an internal P-channel Power MOS, a coil, capacitors, and a diode connected externally, these ICs can function as step-down switching regulators. They serve as ideal power supply units for portable devices when coupled with the SOP-8L package, providing such outstanding features as low current consumption. Since this converter can accommodate an input voltage up to 23V, it is also suitable for the operation via an AC adapter.

❖ FEATURES

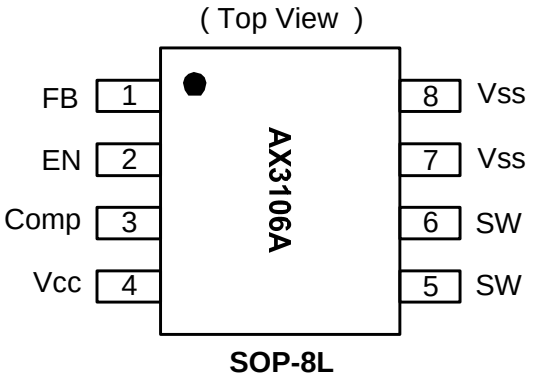
- Input voltage：3.6V to 23V
- Output voltage：0.8V to V_{CC}
- Duty ratio：0% to 100% PWM control
- Oscillation frequency：500KHz typ.
- Soft-start (SS), Current Limit (CL), Enable function.
- Thermal Shutdown function.
- Short Circuit Protect (SCP).
- Built-in internal SW P-channel MOS.
- Low ESR output capacitor (Multi-layer chip capacitor (MLCC)) application.
- SOP-8L Pb-Free package.

❖ BLOCK DIAGRAM



❖ PIN ASSIGNMET

The package of AX3106A is SOP-8L; the pin assignment is given by:



SOP-8L

Name	Description
FB	Feedback pin
EN	Power-off pin H：normal operation(Step-down) L：Step-down operation stopped (All circuits deactivated)
Comp	Compensation pin
V_{CC}	IC power supply pin
SW	Switch pin. Connect external inductor/diode here.
V_{SS}	GND pin

❖ ORDER/MARKING INFORMATION

Order Information	Top Marking
AX3106 X X X Frequency: A : 500Khz Package Type: S: SOP-8L Packing: Blank : Tube, A : Taping	Logo ← **AX** 3 1 0 6 → Part number A Y Y W W X → ID code:internal WW: 01~52 Year: 10=2010, 11=2011 AX3106A

❖ ABSOLUTE MAXIMUM RATINGS (at T_A=25°C)

Characteristics	Symbol	Rating	Unit
VCC Pin Voltage	V_{CC}	V_{SS} - 0.3 to V_{SS} + 23	V
Feedback Pin Voltage	V_{FB}	V_{SS} - 0.3 to V_{CC}	V
ON/OFF Pin Voltage	V_{EN}	V_{SS} - 0.3 to V_{CC} + 0.3	V
Switch Pin Voltage	V_{SW}	V_{SS} - 0.3 to V_{CC} + 0.3	V
Power Dissipation	PD	Internally limited	mW
Storage Temperature Range	T_{ST}	-40 to +150	°C
Operating Temperature Range	T_{OP}	-20 to +125	°C
Operating Supply Voltage	V_{OP}	+3.6 to +23	V
Thermal Resistance from Junction to case	θ_{JC}	25	°C/W
Thermal Resistance from Junction to ambient	θ_{JA}	70	°C/W

Note : θ_{JA} is measured with the PCB copper area(need connect to SW pins) of approximately 1 in^2(Multi-layer).

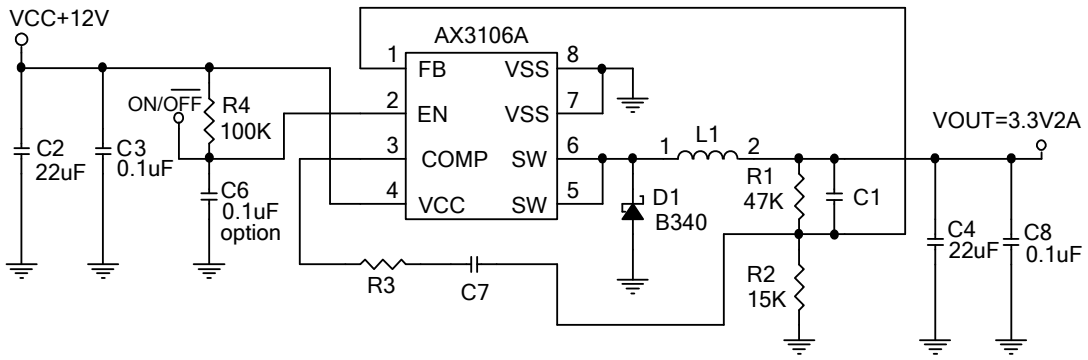
❖ ELECTRICAL CHARACTERISTICS

(V_{IN} = 12V, T_A=25°C, unless otherwise specified)

Characteristics	Symbol	Conditions		Min	Typ	Max	Units
Feedback Voltage	V_{FB}	I_{OUT}=0.1A		0.784	0.8	0.816	V
Quiescent Current	I_{CCQ}	V_{FB}=1.2V force driver off			3	5	mA
Feedback Bias Current	I_{FB}	I_{OUT}=0.1A		-	0.1	0.5	uA
Shutdown Supply Current	I_{SD}	V_{EN} =0V		-	2	10	uA
Switch Current	I_{SW}			3.0	-	-	A
Line Regulation	$\triangle V_{OUT}/V_{OUT}$	V_{CC}=4V~23V, I_{OUT}=0.2A		-	0.6	1.2	%
Load Regulation	$\triangle V_{OUT}/V_{OUT}$	I_{OUT} = 0.1 to 2A		-	0.15	0.3	%
Oscillation Frequency	F_{OSC}	SW pin		400	500	600	KHz
EN Pin Logic input threshold voltage	V_{SH}	High (regulator ON)		2.0	-	-	V
	V_{SL}	Low (regulator OFF)		-	-	0.8	
EN Pin Input Current	I_{SH}	V_{EN}=2.5V (ON)		-	20	-	uA
	I_{SL}	V_{EN}=0.3V (OFF)		-	-10	-	uA
Soft-Start Time	T_{SS}			-	20	-	ms
Internal MOSFET R_{DSON}	R_{DSON}	V_{CC}=5V, V_{FB}=0V		-	100	150	mΩ
		V_{CC}=12V, V_{FB}=0V		-	70	100	
Efficiency	EFFI	V_{OUT} = 5V	I_{OUT} = 1A	-	91	-	%
			I_{OUT} = 2A	-	91	-	
Thermal shutdown Temp	T_{SD}				125		°C

❖ APPLICATION CIRCUIT

MLCC

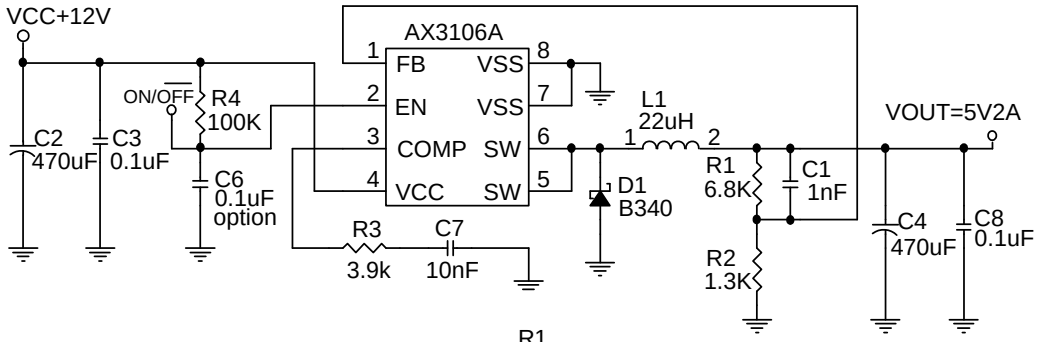


$$VOUT = VFB \times \left(1 + \frac{R1}{R2}\right)$$

VFB = 0.8 V ; R2 suggest 0.8K ~ 3.0K

Compensation Capacitor Selection(MLCC)					
VIN	V_{OUT}	L1	R3	C7	C1
5~20V	5.0/3.3/2.5V	22uH	3.9K	470pF	820pF

EL CAP



$$VOUT = VFB \times \left(1 + \frac{R1}{R2}\right)$$

VFB = 0.8 V ; R2 suggest 0.8K ~ 6.0K

Compensation Capacitor Selection(EL CAP)					
VIN	V_{OUT}	R3	C7	C1	L
7-20V	5.0/3.3/2.5V	3.9K	10nF	1nF	Coil
7-20V	1.8/1.5V	2K	10nF	1nF	Coil
5-7V	3.3/2.5/1.8/1.5/1.2V	2K	10nF	1nF	Coil
5-20V	3.3/2.5/1.8/1.5/1.2V	0.82K	10nF	1nF	SMD

❖ FUNCTION DESCRIPTIONS

PWM Control

The AX3106A consists of DC/DC converters that employ a pulse-width modulation (PWM) system. In converters of the AX3106A, the pulse width varies in a range from 0 to 100%, according to the load current. The ripple voltage produced by the switching can easily be removed through a filter because the switching frequency remains constant. Therefore, these converters provide a low-ripple power over broad ranges of input voltage and load current.

Setting the Output Voltage

Application circuit item shows the basic application circuit with adjustable output version. The external resistor sets the output voltage according to the following equation:

$$V_{OUT} = 0.8V \times \left(1 + \frac{R1}{R2}\right)$$

Table 1 Resistor select for output voltage setting

V_{OUT}	C_{OUT}	R2	R1
5V	EL	1.3K	6.8K
	MLCC	7.5K	39K
3.3V	EL	1.5K	4.7K
	MLCC	15K	47K
2.5V	EL	2.2K	4.7K
	MLCC	22K	47K
1.8V	EL	2K	2.5K
1.5V	EL	2.2K	2.0K
1.2V	EL	3K	1.5K

Inductor Selection

For most designs, the operates with inductors of 15μH to 22μH. The inductor value can be derived from the following equation:

$$L = \frac{V_{OUT} \times (V_{IN} - V_{OUT})}{V_{IN} \times \Delta I_L \times f_{OSC}}$$

L1 recommend value (V_{IN}=12V ,I_{OUT}=2A,)				
V_{OUT}	1.8 V	2.5V	3.3V	5V
L1	15~22uH	15~22uH	15~22uH	15~22uH

Where is inductor Ripple Current. Large value inductors lower ripple current and small value inductors result in high ripple currents. Choose inductor ripple current approximately 15% of the maximum load current 2A, ΔI_L=0.3A. The DC current rating of the inductor should be at least equal to the maximum load current plus half the ripple current to prevent core saturation (2A+0.15A).

Input Capacitor Selection

(EL CAP)

This capacitor should be located close to the IC using short leads and the voltage rating should be approximately 1.5 times the maximum input voltage. The RMS current rating requirement for the input capacitor of a buck regulator is approximately 1⁄2 the DC load current. A low ESR input capacitor sized for maximum RMS current must be used. A 220µF low ESR capacitor for most applications is sufficient.

(MLCC CAP)

A 22µF MLCC capacitor for most applications is sufficient.

Output Capacitor Selection

(EL CAP)

The output capacitor is required to filter the output and provide regulator loop stability. The important capacitor parameters are; the 100KHz Equivalent Series Resistance (ESR), the RMS ripples current rating, voltage rating, and capacitance value. For the output capacitor, the ESR value is the most important parameter. The ESR can be calculated from the following formula.

$$V_{RIPPLE} = \Delta I_L \times ESR$$ = 0.3A x 130mΩ= 40Mv

An aluminum electrolytic capacitor's ESR value is related to the capacitance and its voltage rating. In most case, higher voltage electrolytic capacitors have lower ESR values. Most of the time, capacitors with much higher voltage ratings may be needed to provide the low ESR values required for low output ripple voltage. It is recommended to replace this low ESR capacitor by using a 330µF low ESR values < 130mΩ.

(MLCC CAP)

A 22μF MLCC capacitor for most applications is sufficient.

Note: MLCC do not support when V_{OUT} is smaller than 2.5V.

RDS (ON) Current Limiting

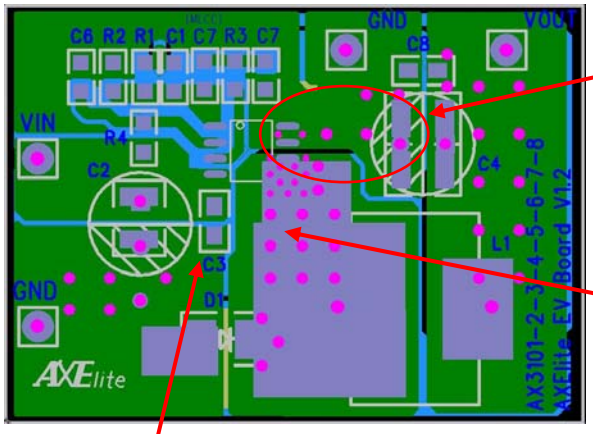
The current limit threshold is setting by the internal circuit.

V_{IN}	4V~4.6V	4.6V~8V	8V~20V
I_{CL}(MIN)	2.5A	2.8A	3.0A
I_{OUT}(MAX)	1.8A	2A	2A

PCB Layout Guide

If you need low Tc and Tj or large PD(Power Dissipation), The dual SW pins(5 and 6) and V_{SS} pins(7 and 8)on the SOP-8L package are internally connected to die pad, The PCB layout should allow for maximum possible copper area at the SW pins.

1. Connect C3 to V_{CC} and V_{SS} pin as closely as possible to get good power filter effect.
2. Connect ground side of the C2 and D1 as closely as possible.
3. D1'ground must be closed to input capacitor's ground (C2).



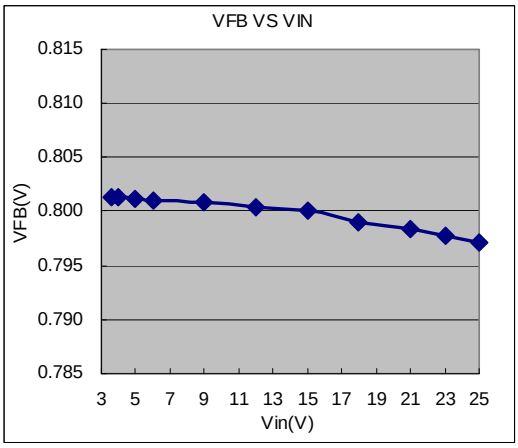
IC's ground is connect to output capacitor's ground, and use through hole to conduct the heat into the backside of PCB layer.

The heat sink copper of PCB area should be solder-painted without masked.

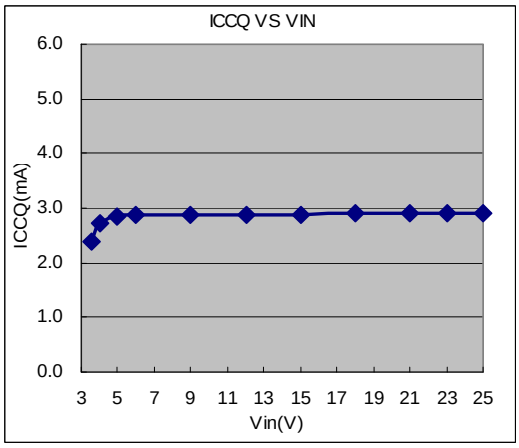
Bypass C3 need closely from IC's Vcc to Vss pins.

❖ TYPICAL CHARACTERISTICS

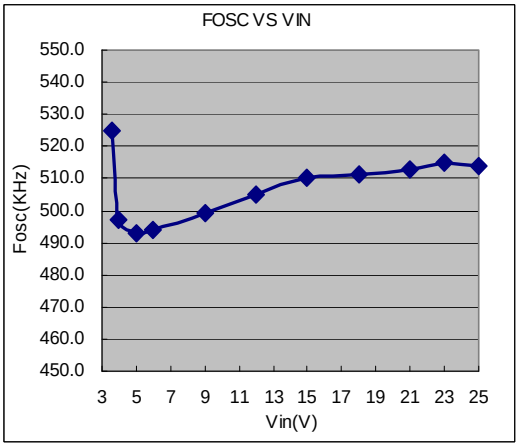
VFB VS VIN



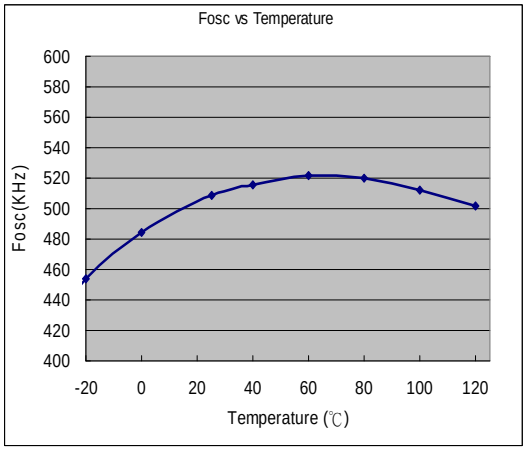
ICCQ VS VIN



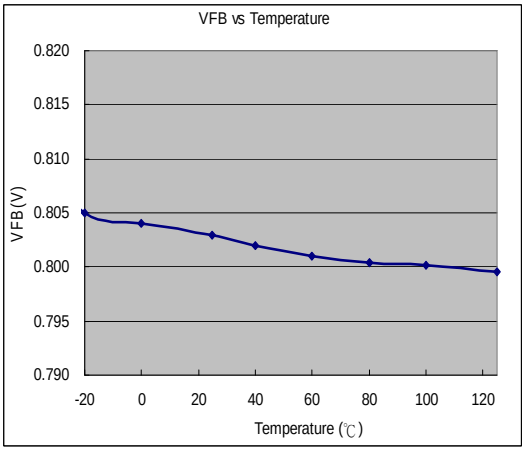
FOSC VS VIN



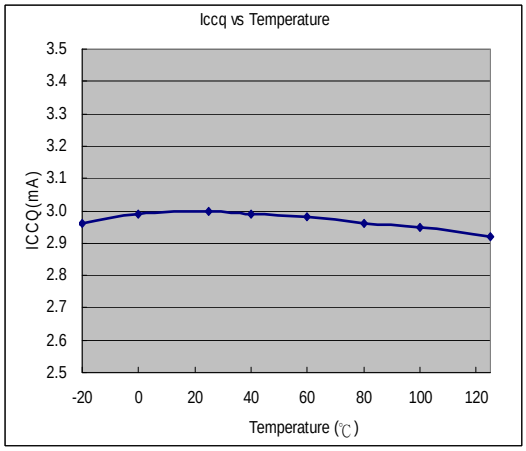
FOSC VS TEMPERATURE



VFB VS TEMPERATURE

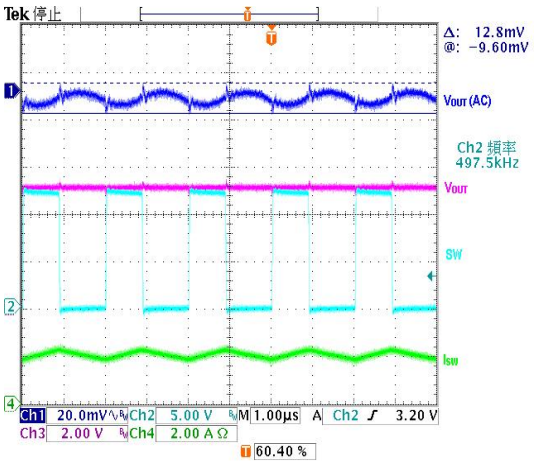


ICCQ VS TEMPERATURE

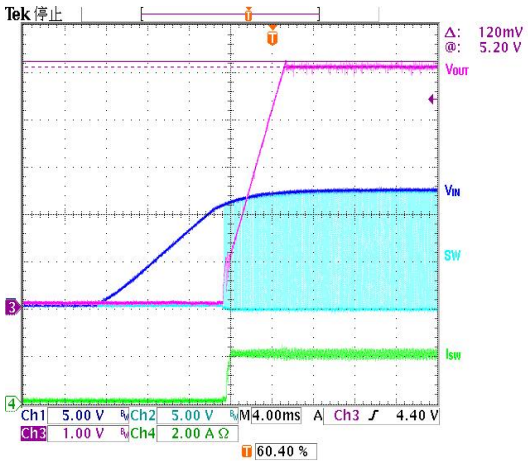


❖ TYPICAL CHARACTERISTICS (MLCC)

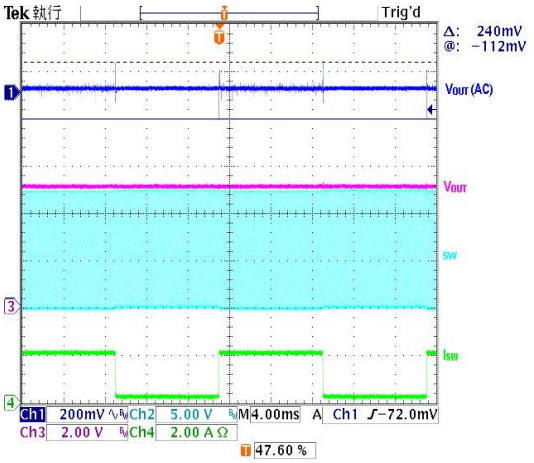
Output Ripple
(V_{IN}=12V, V_{OUT}=5V, I_{OUT}=2A)



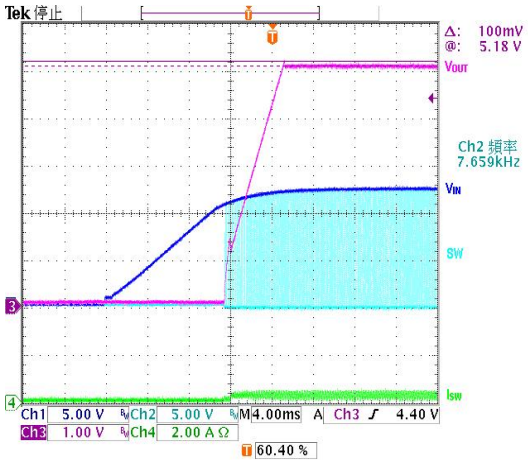
Power on test wave
(V_{IN}=12V, V_{OUT}=5V, I_{OUT}=2A)



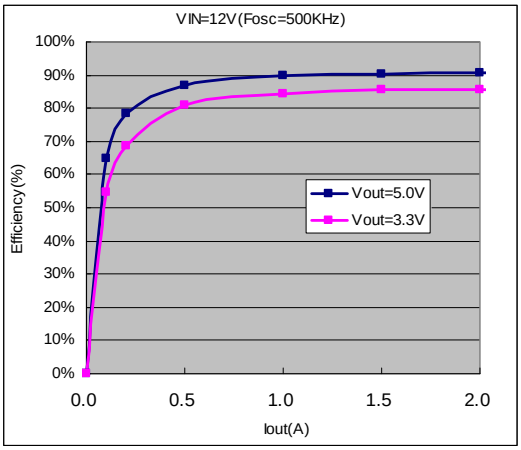
Load Transient Response
(V_{IN}=12V, V_{OUT}=5V, I_{OUT}=0.1~2A)



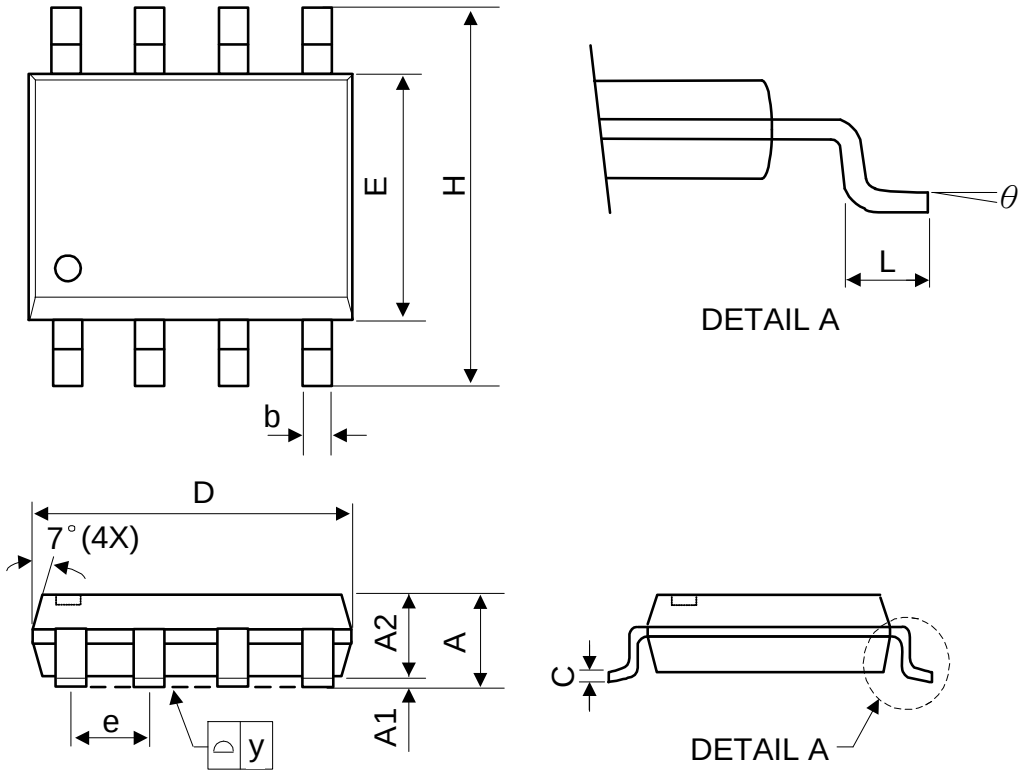
Power on test wave
(V_{IN}=12V, V_{OUT}=5V, I_{OUT}=0.2A)



Efficiency
(V_{IN}=12V)



❖ PACKAGE OUTLINES



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	-	-	1.75	-	-	0.069
A1	0.1	-	0.25	0.04	-	0.1
A2	1.25	-	-	0.049	-	-
C	0.1	0.2	0.25	0.0075	0.008	0.01
D	4.7	4.9	5.1	0.185	0.193	0.2
E	3.7	3.9	4.1	0.146	0.154	0.161
H	5.8	6	6.2	0.228	0.236	0.244
L	0.4	-	1.27	0.015	-	0.05
b	0.31	0.41	0.51	0.012	0.016	0.02
e	1.27 BSC			0.050 BSC		
y	-	-	0.1	-	-	0.004
θ	0°	-	8°	0°	-	8°

Mold flash shall not exceed 0.25mm per side

JEDEC outline: MS-012 AA