

MICROPOWER VFM STEP-UP DC/DC Converter

❖ GENERAL DESCRIPTION

The AX5502 is a high efficiency VFM Step-up DC/DC converter for small, low input voltage or battery powered systems with ultra-low quiescent supply current. The AX5502 accept a positive input voltage from start-up voltage to VOUT and convert it to a higher output voltage in the 2.2V to 5V range.

The AX5502 combine ultra low quiescent supply current and high efficiency to give maximum battery life. The high switching frequency and the internally limited peak inductor current permits the use of small, low cost inductors. Only three external components are needed an inductor a diode and an output capacitor.

The AX5502 is suitable to be used in battery powered equipment where low noise, low ripple and ultra low supply current are required. The AX5502 is available in very small package: SOT-23-3L, SOT-23-5L and SOT-89-3L.

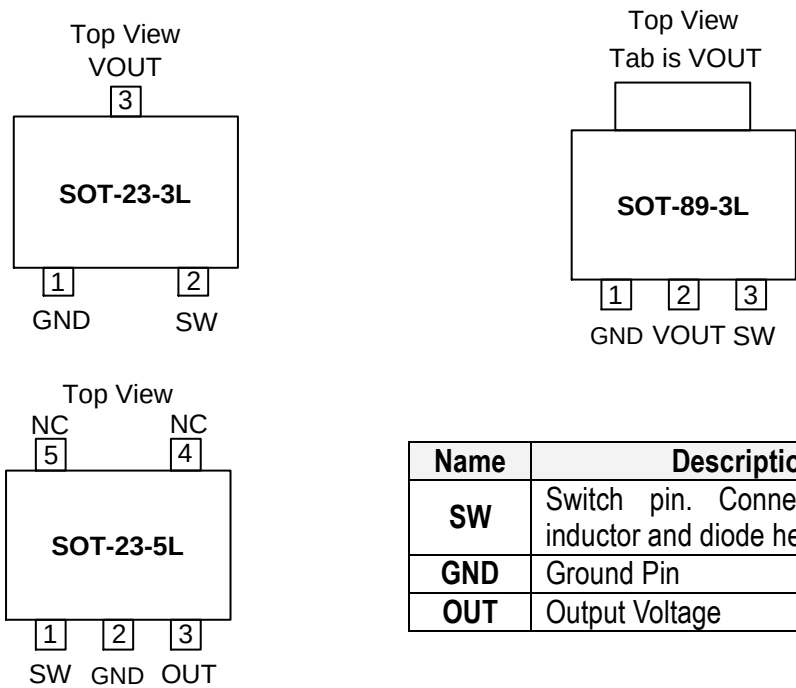
Typical applications are pagers, cameras and video camera, cellular telephones, wireless telephones, palmtop computer, battery backup supplies, battery powered equipment.

❖ FEATURES

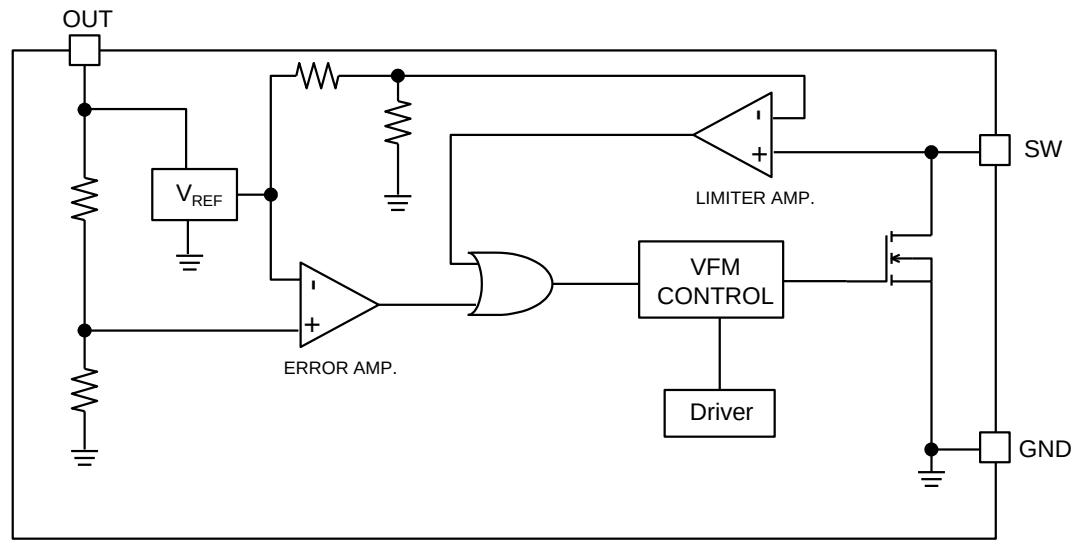
- Very Low Supply Current
- Regulated Output Voltage
- Wide range of output voltage is available form 2.2V to 5.0V by 0.1V steps
- Output Voltage Accuracy $\pm 5\%$
- Output Current up to 100mA
- Low ripple and low noise
- Very low start-up voltage
- High efficiency ($V_{OUT} = 5V$ TYP. 87%)
- Few external components
- Low profile: SOT-23-3L, SOT-23-5L and SOT-89-3L Pb-Free

❖ PIN ASSIGNMENT

The packages type: SOT-23-3L, SOT-23-5L and SOT-89-3L; the pin assignment is given by:



❖ BLOCK DIAGRAM



❖ ORDER/MARKING INFORMATION

Order Information	
AX5502-XX XX Vout Package Type Packing 22 : 2.2V R: SOT-23-3L A : Taping 23 : 2.3V B: SOT-23-5L 24 : 2.4V F: SOT-89-3L : : 50 : 5.0V	
Top Marking (SOT-23-3L/SOT-23-5L)	Top Marking (SOT-89-3L)
LLYWX → ID code:internal WW:01~26 (A~Z) 27~52 (a~z) Year: A=2010 1=2011 Identification code	5 5 0 2 LYWX → ID Code: internal WW: 01~26(A~Z) 27~52(a~z) Year: A=2010 1=2011 A: 2.2V F: 2.7V K: 3.2V P: 3.7V U: 4.2V Z: 4.7V B: 2.3V G: 2.8V L: 3.3V Q: 3.8V V: 4.3V 1: 4.8V C: 2.4V H: 2.9V M: 3.4V R: 3.9V W: 4.4V 2: 4.9V D: 2.5V I: 3.0V N: 3.5V S: 4.0V X: 4.5V 3: 5.0V E: 2.6V J: 3.1V O: 3.6V T: 4.1V Y: 4.6V

Appendix

Part Number	Identification Code	Part Number	Identification Code
AX5502-2.2V	Ta	AX5502-3.7V	TM
AX5502-2.3V	Tb	AX5502-3.8V	TN
AX5502-2.4V	Td	AX5502-3.9V	TO
AX5502-2.5V	TA	AX5502-4.0V	TP
AX5502-2.6V	TB	AX5502-4.1V	TQ
AX5502-2.7V	TC	AX5502-4.2V	TR
AX5502-2.8V	TD	AX5502-4.3V	TS
AX5502-2.9V	TE	AX5502-4.4V	TT
AX5502-3.0V	TF	AX5502-4.5V	TU
AX5502-3.1V	TG	AX5502-4.6V	TV
AX5502-3.2V	TH	AX5502-4.7V	TW
AX5502-3.3V	TI	AX5502-4.8V	TX
AX5502-3.4V	TJ	AX5502-4.9V	TY
AX5502-3.5V	TK	AX5502-5.0V	TZ
AX5502-3.6V	TL		

❖ ABSOLUTE MAXIMUM RATINGS (at $T_A = 25^\circ\text{C}$)

Characteristics		Symbol	Rating	Unit
V_{IN} Pin Voltage		V_{IN}	5.5	V
SW Voltage		V_{SW}	5.5	V
OUT Pin Voltage		V_{OUT}	5.5	V
Storage Temperature Range		T_{STG}	-40 to +150	$^\circ\text{C}$
Operating Junction Temperature Range		T_J	-20 to +100	$^\circ\text{C}$
Power Dissipation		PD	$(T_J - T_A) / \theta_{JA}$	mW
Thermal Resistance from Junction to case	SOT-23-3L	θ_{JC}	180	$^\circ\text{C/W}$
	SOT-23-5L			
	SOT-89-3L		100	
Thermal Resistance from Junction to ambient	SOT-23-3L	θ_{JA}	250	$^\circ\text{C/W}$
	SOT-23-5L			
	SOT-89-3L		160	

Note: θ_{JA} is measured with the PCB copper area of approximately 1 in² (Multi-layer) that need connect to GND pin of the AX5502.

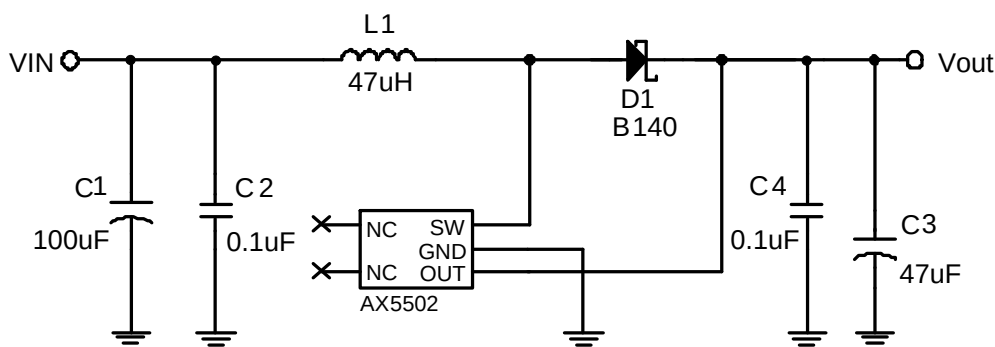
❖ ELECTRICAL CHARACTERISTICS

($V_{IN} = 1.8\text{V}$, $V_{OUT} = 3\text{V}$, $I_{OUT} = 10\text{mA}$, $T_A = 25^\circ\text{C}$, unless otherwise specified)

Characteristics	Symbol	Conditions	Min	Typ	Max	Units
Output Voltage Accuracy	ΔV_{OUT}		-5	-	+5	%
Start-up Voltage($V_{IN} - V_F$) (Note)	$V_{START-UP}$	$I_{OUT} = 1\text{mA}$, V_{IN} =rising from 0 to 2V	-	0.65	0.9	V
Hold-on Voltage	V_{HOLD}	$I_{OUT} = 1\text{mA}$, V_{IN} =falling from 2 to 0V	0.6	-	-	V
Supply Current	I_{SUPPLY}	No Load	-	22	-	μA
Internal Switch $R_{DS(ON)}$	$R_{LX(DSON)}$	$I_{LX} = 150\text{mA}$	-	850	-	$\text{m}\Omega$
Internal Leakage Current	$I_{LX(leak)}$	$V_{LX} = 4\text{V}$, Forced $V_{OUT} = 3.8\text{V}$	-	-	0.5	μA
Maximum Oscillator Frequency	F_{OSC}		-	150	-	KHz
Oscillator Duty On	D_{ON}	To be measure on SW pin	-	77	-	%
Efficiency	η	$V_{OUT} = 2.5\text{V} \sim 3.0\text{V}$, $I_{OUT} = 50\text{mA}$	-	82	-	%
		$V_{OUT} = 3.1\text{V} \sim 4.0\text{V}$, $I_{OUT} = 50\text{mA}$	-	83	-	%
		$V_{OUT} = 4.1\text{V} \sim 5.0\text{V}$, $I_{OUT} = 50\text{mA}$	-	87	-	%

Note: The minimum input voltage for the IC start-up is strictly a function of the VF catch diode.

❖ APPLICATION CIRCUIT



❖ OPERATION

The AX5502 architecture is built around a VFM CONTROL logic core, switching frequency is set through a built in oscillator. T_{ON} time is fixed (Typ. 5uS) while T_{OFF} time is determined by the error amplifier output, a logic signal coming from the comparison made by the Error Amplifier Stage between the signal coming from the output voltage divider network and the internal Band-Gap voltage reference (V_{ref}). T_{OFF} reaches a minimum (Typ. 1.7uS) when heavy load conditions are met (Clock frequency 150 KHz). An over current conditions, through the internal power switch, causes a voltage drop $V_{LX} = R_{DS(on)} \times I_{SW}$ and the V_{LX} limiter block forces the internal switch to be off, so narrowing T_{ON} time and limiting internal power dissipation. In this case the switching frequency may be higher than the 150 KHz set by the internal clock generator.

VFM control ensures very low quiescent current and high conversion efficiency even with very light loads. Since the Output Voltage pin is also used as the device Supply Voltage, the versions with higher output voltage present an higher internal supply voltage that results in lower power switch $R_{DS(on)}$, slightly greater output power and higher efficiency. Moreover, bootstrapping allows the input voltage to sag to 0.6V (at $I_{OUT}=1mA$) once the system is started. If the input voltage exceeds the output voltage, the output will follow the input, however, the input or output voltage must not be forced above 5.5V.

❖ APPLICATION INFORMATION

Input/output Capacitor Selection

The Output Ripple Voltage, as well as the Efficiency, is strictly related to the behavior of these elements. The output ripple voltage is the product of the peak inductor current and the output capacitor Equivalent Series Resistance (ESR). Best performances are obtained with good high frequency characteristics capacitors and low ESR. The best compromise for the value of the Output Capacitance is 47 μ F Tantalum Capacitor; Lower values may cause higher Output Ripple Voltage and lower Efficiency without compromising the functionality of the device.

An Input Capacitor is required to compensate, if present, the series impedance between the Supply Voltage Source and the Input Voltage of the Application.

Inductor Selection

A 47 μ H inductor is recommended for most AX5502 applications. However, the inductance value is not critical, and the AX5502 will work with inductors in the 33 μ H to 120 μ H.

Diode Selection

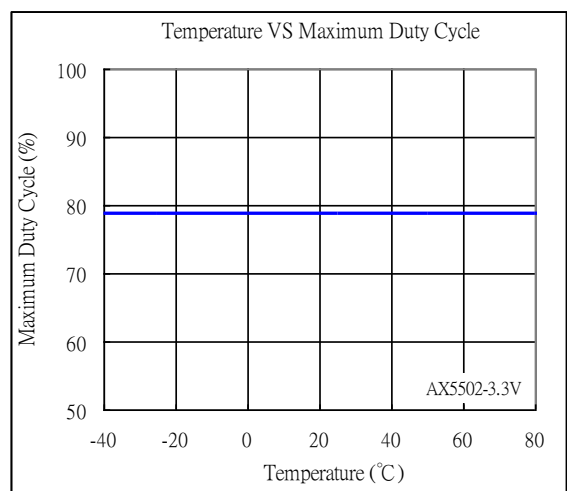
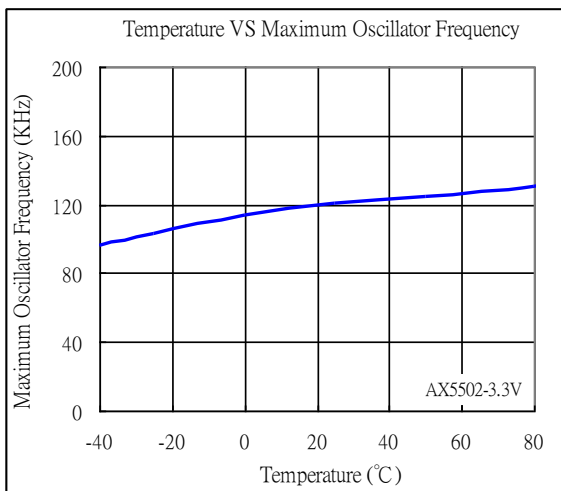
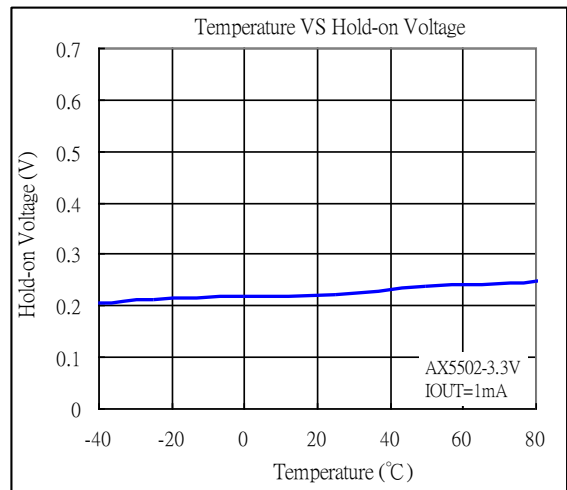
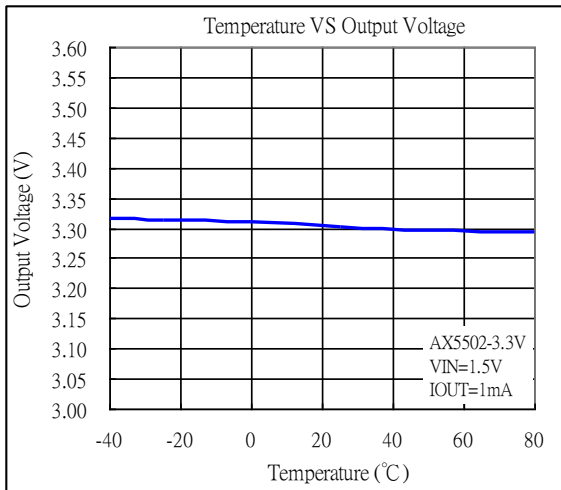
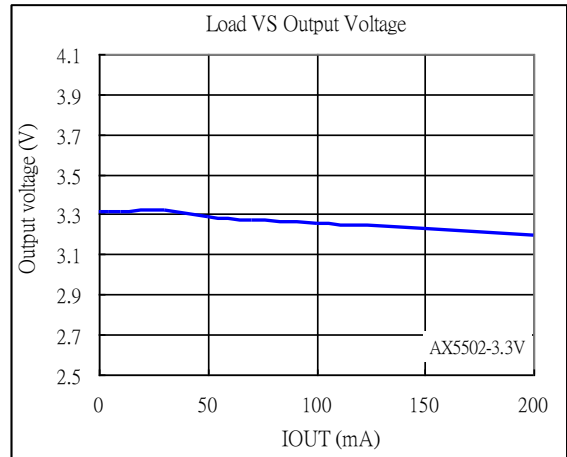
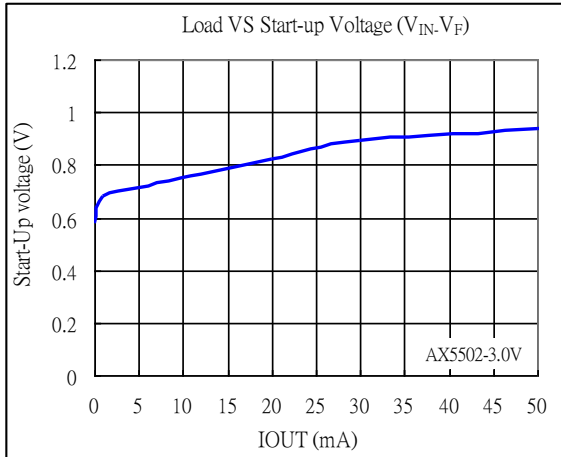
Schottky diodes with higher current ratings usually have lower forward voltage drop, larger diode capacitance and fast reverse recovery, it is the ideal choices for AX5502 applications. The forward voltage drop of a Schottky diode represents the conduction losses in the system, while the diode capacitance (C_T or C_D) represents the switching losses. For diode selection, both forward voltage drop and diode capacitance need to be considered.

PCB layout guide

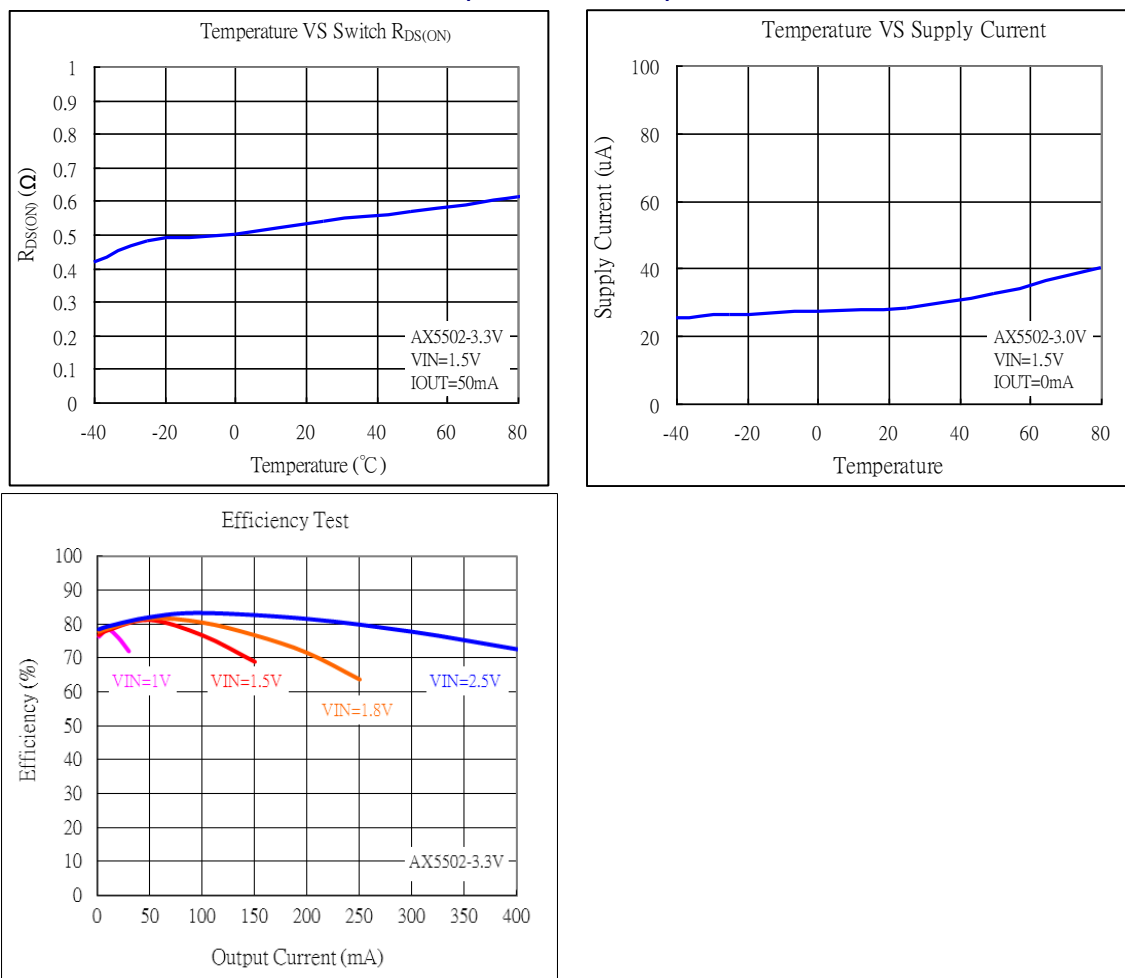
When laying out the PC board, the following suggestions should be taken to ensure proper operation of the AX5502. These items are also illustrated graphically in below.

1. The power traces, including the GND trace, the SW trace and the V_{CC} trace should be kept short, direct and wide to allow large current flow. Put enough multiply-layer pads when they need to change the trace layer.
2. Do not trace signal line under inductor.

❖ TYPICAL CHARACTERISTICS

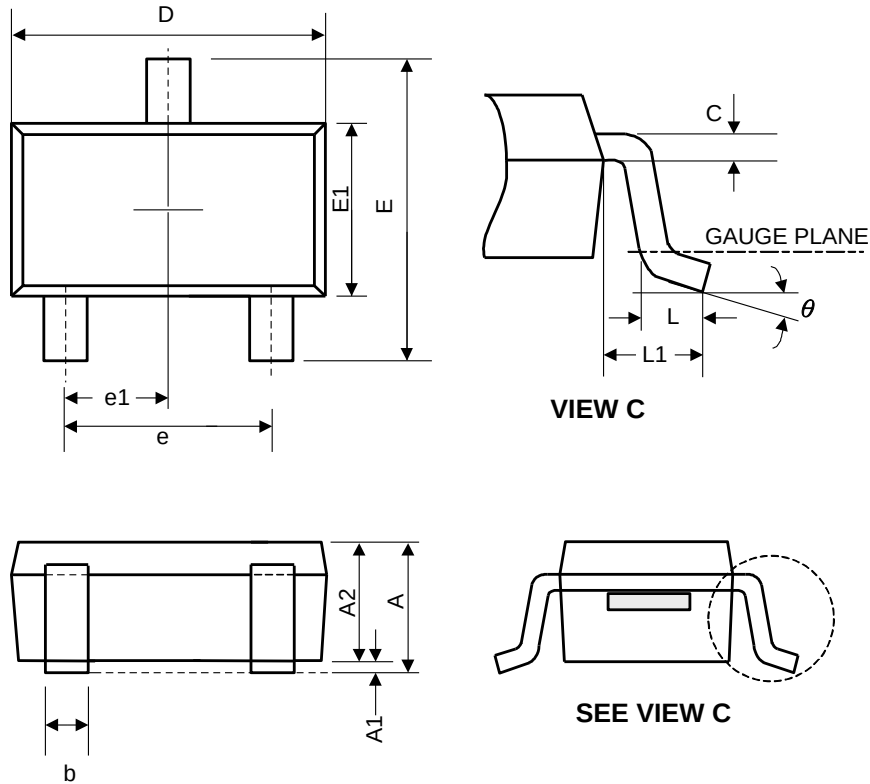


❖ TYPICAL CHARACTERISTICS (CONTINUOUS)



❖ **PACKAGE OUTLINES**

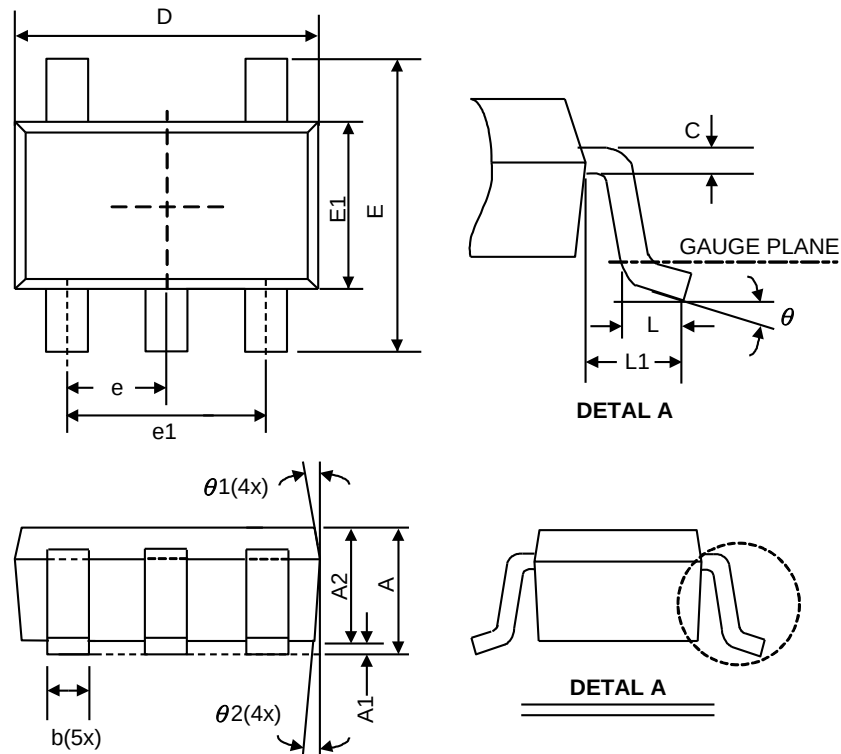
(1) SOT-23-3L



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	-	-	1.45	-	-	0.057
A1	0.00	0.08	0.15	-	-	0.006
A2	0.90	1.10	1.30	0.035	0.043	0.051
b	0.30	0.40	0.50	0.012	0.016	0.020
C	0.08	0.15	0.22	0.003	0.006	0.009
D	2.70	2.90	3.10	0.106	0.114	0.122
E	2.60	2.80	3.00	0.102	0.110	0.118
E1	1.40	1.60	1.80	0.055	0.063	0.071
L	0.30	0.45	0.60	0.012	0.018	0.024
L1	0.50	0.60	0.70	0.020	0.024	0.028
e	1.9 BSC			0.075 BSC		
e1	0.95 BSC			0.037 BSC		
θ	0°	4°	8°	0°	4°	8°

JEDEC outline: NA

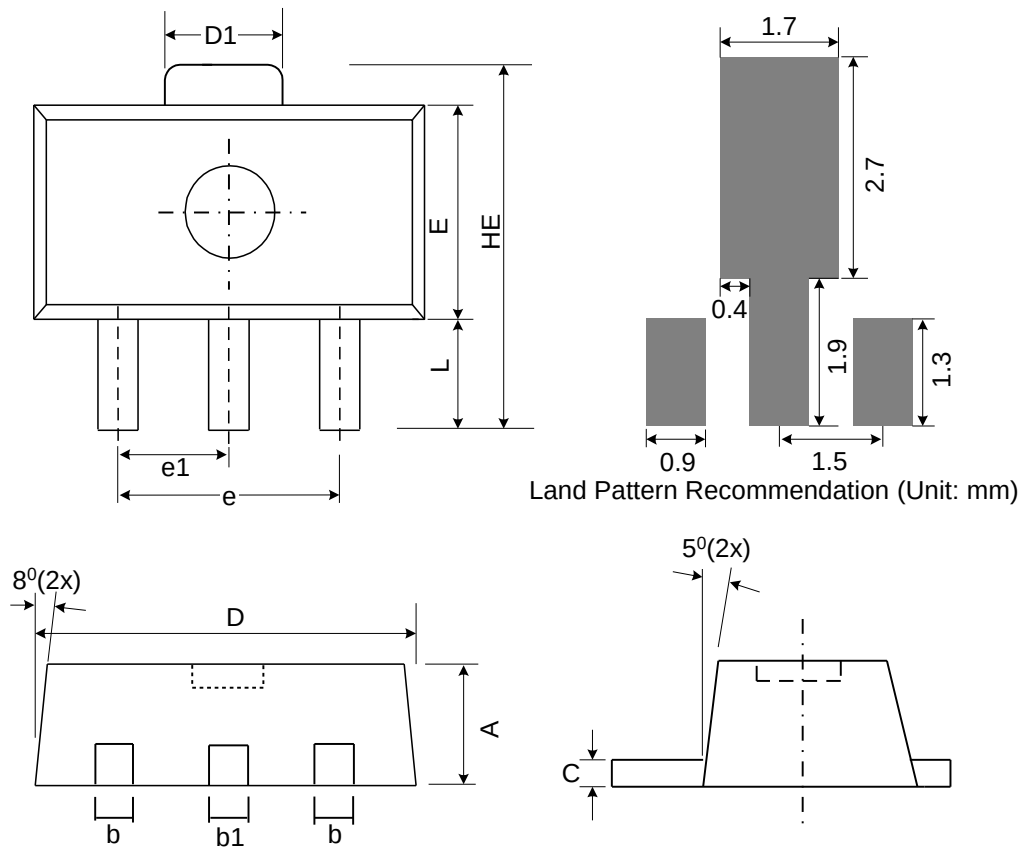
(2) SOT-23-5L



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	-	-	1.45	-	-	0.057
A1	0.00	0.08	0.15	0	0.003	0.006
A2	0.90	1.10	1.30	0.035	0.043	0.051
b	0.30	0.40	0.50	0.012	0.016	0.020
C	0.08	0.15	0.22	0.003	0.006	0.009
D	2.70	2.90	3.10	0.106	0.114	0.122
E1	1.40	1.60	1.80	0.055	0.063	0.071
E	2.60	2.80	3.00	0.102	0.110	0.118
L	0.30	0.45	0.60	0.012	0.018	0.024
L1	0.50	0.60	0.70	0.020	0.024	0.028
e1	1.9 BSC			0.075 BSC		
e	0.95 BSC			0.037 BSC		
θ	0°	4°	8°	0°	4°	8°
$\theta 1$	5°	10°	15°	5°	10°	15°
$\theta 2$	5°	10°	15°	5°	10°	15°

JEDEC outline: MO-178 AA

(3) SOT-89-3L



Symbol	Dimensions in Millimeters			Dimensions in Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	1.40	1.50	1.60	0.055	0.059	0.063
b	0.36	0.42	0.48	0.014	0.017	0.019
b1	0.44	0.50	0.56	0.017	0.02	0.022
C	0.35	0.40	0.44	0.014	0.016	0.017
D	4.40	4.50	4.60	0.173	0.177	0.181
D1	1.35	1.59	1.83	0.053	0.063	0.072
e	3.0 BSC			0.118 BSC		
e1	1.5 BSC			0.059 BSC		
E	2.29	2.45	2.60	0.09	0.097	0.102
HE	3.94	4.10	4.25	0.155	0.161	0.167
L	0.80	1.00	1.20	0.031	0.04	0.047

JEDEC outline: TO-243 AB