

GENERAL DESCRIPTION

The PT5107 is a low-dropout voltage regulator designed for portable and wireless applications that require high PSRR, low quiescent current and excellent line and load transient response. The PT5107 is designed to work with small 1 μ F input and output ceramic capacitors. The quiescent current is as low as 28 μ A.

With its better than 63dB PSRR at 10kHz, the PT5107 is ideal for battery powered systems for delivering low dropout voltage and low quiescent current.

The device can be used for mobile phones and similar battery powered wireless applications. It provides up to 200mA, from a 2.5V to 5.5V input. The PT5107 consumes less than 0.1 μ A in shutdown mode. The PT5107 is available in 5 pin SOT23/SC70 packages. The output standards of 1.5V, 1.8V, 2.5V, 2.8V, 3.0V, 3.3V are available.

FEATURES

- 2.5V to 5.5V input range
- 200mA guaranteed output current
- 65dB PSRR @1KHz, $V_{IN} = V_{OUT} + 1V$
64dB PSRR @10KHz, $V_{IN} = V_{OUT} + 1V$
- Low quiescent current: 28 μ A (Typ.)
- 180mV maximum dropout voltage with 100mA load
- < 1 μ A quiescent current at shutdown mode
- Fast turn on time: 72 μ s (typical)
- Thermal shutdown and short-circuit current limit
- 1.5V, 1.8V, 2.5V, 2.8V, 3.0V, 3.3V typical output standard
- Miniature SOT-23-5 package

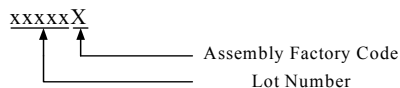
APPLICATION

- CDMA/GSM mobile phone
- Handless telephone
- WLAN and Bluetooth appliances
- PDAS/MP3 handsets
- Battery powered portable devices

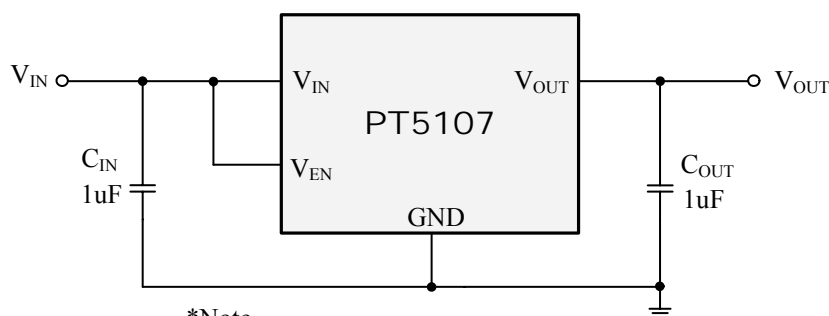
ORDERING INFORMATION

PACKAGE	TEMPERATURE RANGE	TRANSPORT MEDIA	OUTPUT VOLTAGE	ORDERING PART NUMBER	MARK
SOT-23-5	-40°C to 85°C	Tape and Reel 3000 units	1.5V	PT5107E23E-15	5107o
			1.8V	PT5107E23E-18	5107I
			2.5V	PT5107E23E-25	5107e
			2.8V	PT5107E23E-28	5107b
			3.0V	PT5107E23E-30	5107A
			3.3V	PT5107E23E-33	5107D

Note:



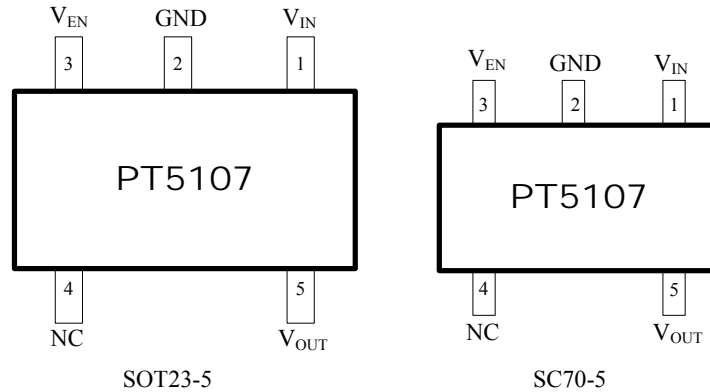
TYPICAL APPLICATION CIRCUIT



*Note

C_{OUT}: Recommended ceramic capacitor

PIN ASSIGNMENT



PIN DESCRIPTIONS

SOT23-5/SC70-5 PIN No.	PIN NAMES	DESCRIPTION
1	V _{IN}	Input of LDO
2	GND	Ground
3	V _{EN}	Enable Input Logic, Enable High
4	NC	
5	V _{OUT}	Output of LDO

ABSOLUTE MAXIMUM RATINGS (Note 1)

SYMBOL	ITEMS	VALUE	UNIT
V _{IN}	V _{IN} Range	-0.3~6.0V	V
V _{OUT}	V _{OUT} Range	-0.3~(V _{IN} +0.3)< 6.0V	V
V _{EN}	V _{EN} Range	-0.3~6.0V	V
PD _{MAX}	Maximum Power Dissipation (Note 1)	364	mW
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-65~150	°C
T _{SOLDER}	Lead Temperature (Note 2)	260°C, 2s	
	ESD Rating, HBM	2	KV

RECOMMENDED OPERATING RANGE

SYMBOL	ITEMS	VALUE	UNIT
V _{IN}	V _{IN} Range	-0.3~5.5V	V
V _{EN}	V _{EN} Range	-0.3~(V _{IN} +0.3)<5.5V	V
P _{TR}	Thermal Resistance, θ _{JA} (SOT23-5)	220	°C/W
PD _{MAX}	Maximum Power Dissipation (Note 3)	250	mW
T _{OPT}	Operation Temperature	-40~85	°C

Note 1: The Absolute Maximum power dissipation depends on the ambient temperature and can be calculated using the formula: $P_D = (T_J - T_A) / \theta_{JA}$, where T_J is the junction temperature, T_A is the ambient temperature, and θ_{JA} is the junction-to-ambient thermal resistance. The 364mW rating for SOT23-5 appearing under Absolute Maximum Ratings results from substituting the Absolute Maximum junction temperature, 150°C, for T_J , 70°C for T_A , and 220°C/W for θ_{JA} . More power can be dissipated safely at ambient temperatures below 70°C. Less power can be dissipated safely at ambient temperatures above 70°C. The Absolute Maximum power dissipation can be increased by 4.5mW for each degree below 70°C, and it must be derated by 4.5mW for each degree above 70°C.

Note 2: Additional information on lead temperature and pad temperature may be obtained by contact CR PowTech

Note 3: Like the Absolute Maximum power dissipation, the maximum power dissipation for operation depends on the ambient temperature. The 250mW rating for SOT23-5 appearing under Operating Ratings results from substituting the maximum junction temperature for operation, 125°C, for T_J , 70°C for T_A , and 220°C/W for θ_{JA} above. More power can be dissipated at ambient temperatures below 70°C. Less power can be dissipated at ambient temperatures above 70°C. The maximum power dissipation for operation can be increased by 4.5mW for each degree below 70°C, and it must be derated by 4.5mW for each degree above 70°C.

ELECTRICAL CHARACTERISTICS (Note 4, 5)

Unless otherwise specified: V_{IN} = V_{OUT(nom)} + 1.0V, V_{EN} = V_{IN}, C_{IN} = 1uF, I_{OUT} = 30mA, C_{OUT} = 1uF, T_A = +25°C.

SYMBOL	PARAMETER	CONDITIONS	TYP	MIN	MAX	UNIT
ΔV _{OUT}	Output Voltage Error	I _{OUT} = 30mA		-2	2	%V _{OUT}
	Line Regulation Error	I _{OUT} = 30mA, V _{IN} =(V _{OUT(nom)} + 0.5V) to 5.5V	0.01	-0.1	0.1	%/V
	Load Regulation Error (Note 6)	I _{OUT} = 1mA to 150mA	15		50	mV
I _{OUTMAX}	Maximum Output Current			200		mA
PSRR	Power Supply Rejection Ratio (Note 7)	V _{IN} = V _{OUT(nom)} + 1.0V, f=1kHz I _{OUT} = 50mA	65			dB
		V _{IN} = V _{OUT(nom)} + 1.0V, f=1kHz I _{OUT} = 100mA	63			
		V _{IN} = V _{OUT(nom)} + 1.0V, f=10kHz I _{OUT} = 50mA	64			
		V _{IN} = V _{OUT(nom)} + 1.0V, f=10kHz I _{OUT} = 100mA	63			

ELECTRICAL CHARACTERISTICS (continued) (Note 4, 5)

SYMBOL	PARAMETER	CONDITIONS	TYP	MIN	MAX	UNIT
I _Q	Quiescent Current	I _{OUT} = 0mA, V _{OUT} = 2.5V	28		50	uA
		V _{EN} = 0V	0.1		1.0	
V _{DIFF}	Dropout Voltage (Note 8)	I _{OUT} = 50mA	52		90	mV
		I _{OUT} = 100mA	106		180	
I _{SC}	Output Short Current Limit	Output Grounded	330			mA
T _{ON}	Turn-On Time (Note 9)		70			us
V _{IL}	Maximum Low Input Level at V _{EN}	V _{IN} = 2.5 to 5.5			0.4	V
V _{IH}	Minimum High Input Level at V _{EN}	V _{IN} = 2.5 to 5.5		1.4		V
TSD	Thermal shutdown Temperature		160			°C
	Thermal shutdown Hysteresis		20			°C
ΔV _{OUT} /V _{OUT}	V _{OUT} Temperature Characteristics	Temperature = -40 to 125°C	100			ppm/°C

Note 4: The target output voltage, which is labeled V_{OUT(nom)}, is the desired voltage option.

Note 5: All limits are guaranteed. All electrical characteristics having room-temperature limits are tested during production with T_J = 25°C or correlated using Statistical Quality Control (SQC) methods. All hot and cold limits are guaranteed by correlating the electrical characteristics to process and temperature variations and applying statistical process control.

Note 6: An increase in the load current results in a slight decrease in the output voltage and vice versa.

Note 7: The PRSS is measured by applying a 200mV_{P-P} sine wave on V_{IN} and measure the ripple at V_{OUT} (see Figure 2). Due to the very high PSRR performance of the PT5107, it is strongly recommended to use a spectrum analyzer rather than an oscilloscope to watch the PSRR performance.

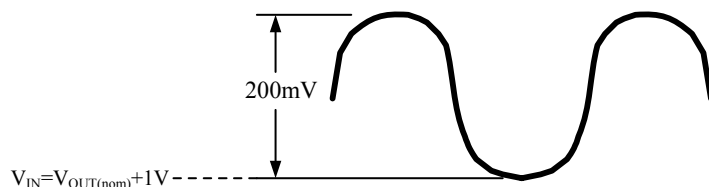


Figure 2.
Graph show the sine waveform added on V_{IN} to measure PSRR

Note 8: Dropout voltage is the input-to-output voltage difference at which the output voltage is 100mV below its nominal value. This specification does not apply for input voltages below 2.5V.

Note 9: Turn-on time is time measured between the enable input just exceeding V_{IH} and the output voltage just reaching 95% of its nominal value.

SIMPLIFIED BLOCK DIAGRAM

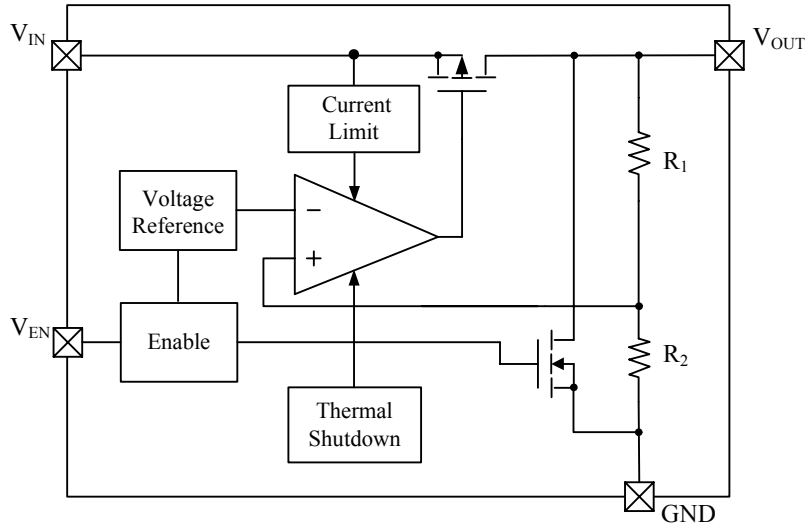


Figure 1. Graph the PT5107 major functional blocks

In Figure 1, the block of Voltage Reference provides the reference voltage of the LDO.

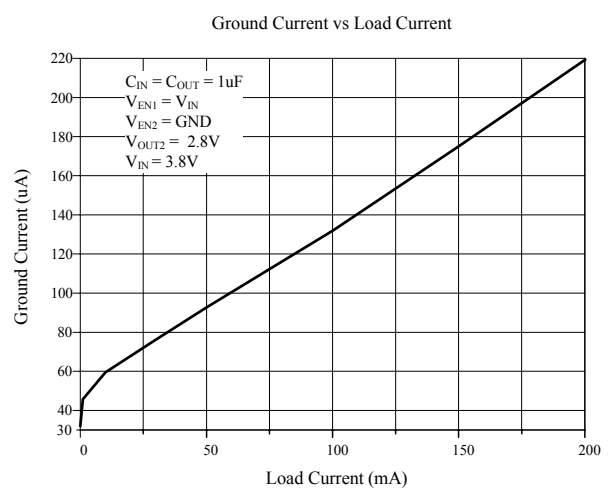
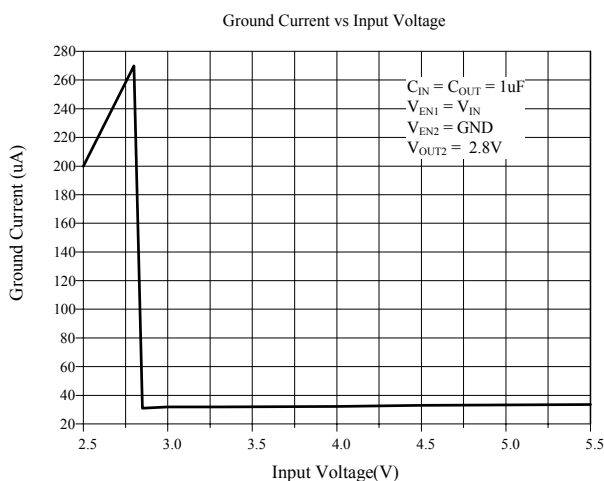
The op-amp block is used as the error amplifier of the LDO by comparing the reference with the output feedback voltages. Its output controls the gate of a large PMOS pass element and hereby adjusts the output voltage.

The resistor R_1 and R_2 form a voltage divider to provide the feedback voltage.

The Current Limit block senses the LDO output current and limits the output current from being too high. This is mostly a short circuit protection feature.

TYPICAL PERFORMANCE CHARACTERISTICS

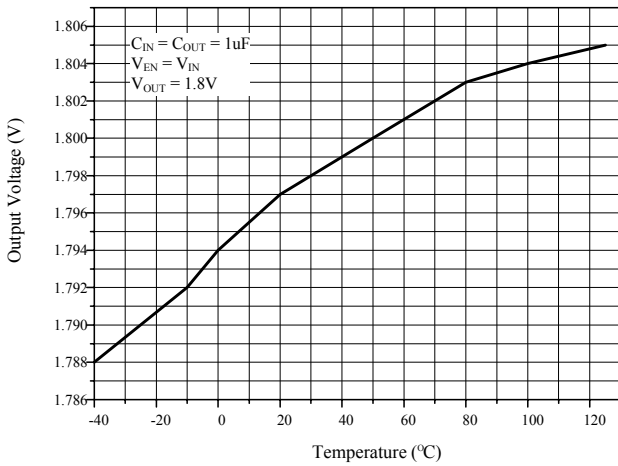
Unless otherwise specified: $V_{IN} = V_{OUT(nom)} + 1V$, $V_{EN1} = V_{EN2} = V_{IN}$, $C_{IN} = C_{OUT1} = C_{OUT2} = 1 \mu F$ (Ceramic Capacitor), $T_A = +25^\circ C$.



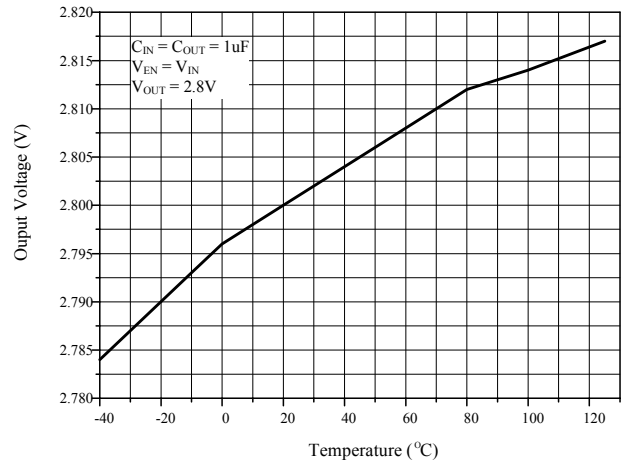
TYPICAL PERFORMANCE CHARACTERISTICS

Unless otherwise specified: $V_{IN} = V_{OUT(nom)} + 1V$, $V_{EN1} = V_{EN2} = V_{IN}$, $C_{IN} = C_{OUT1} = C_{OUT2} = 1 \mu F$ (Ceramic Capacitor), $T_A = +25^\circ C$.

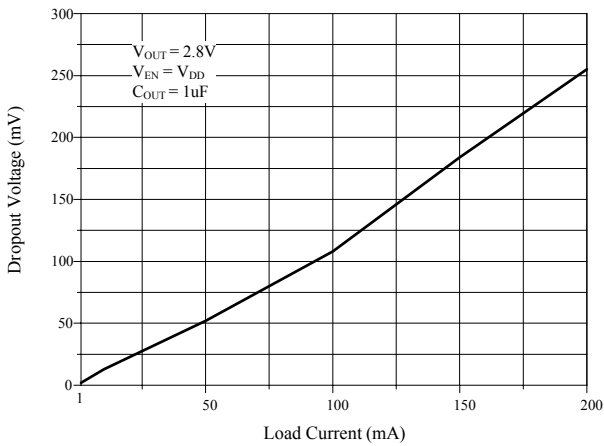
Output Voltage Temperature Characteristics



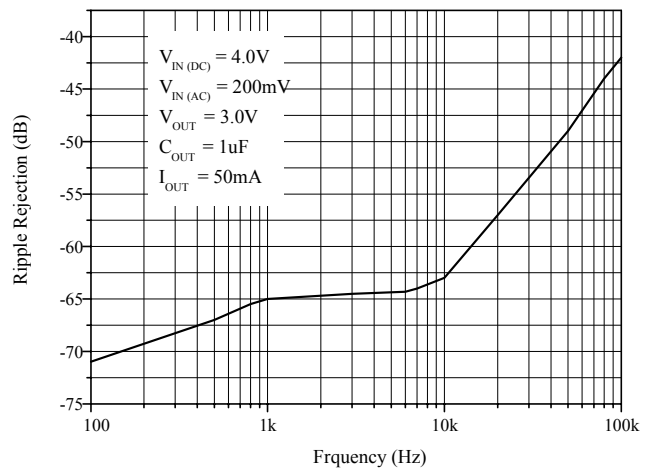
Output Voltage Temperature Characteristics



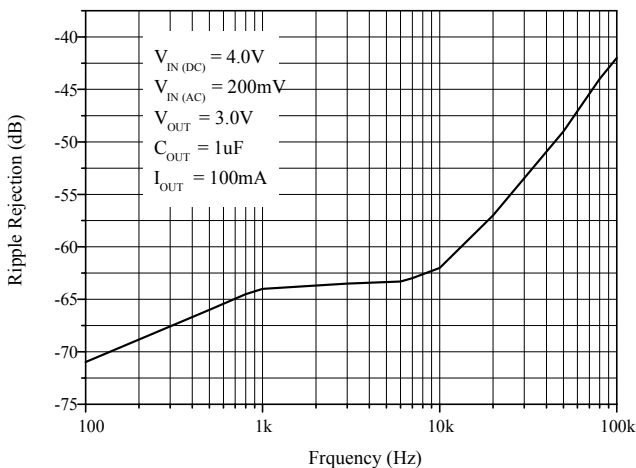
Dropout Voltage vs Load Current



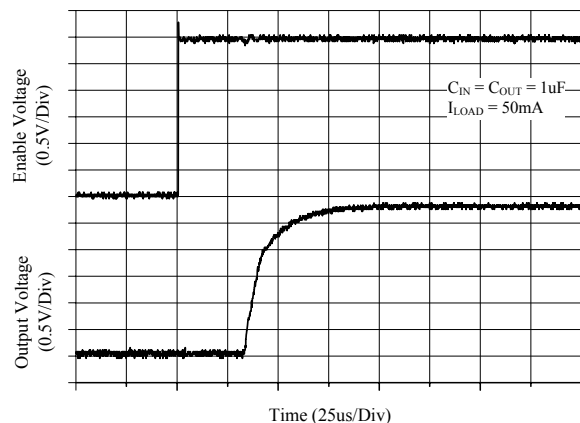
Ripple Rejection vs. Frequency

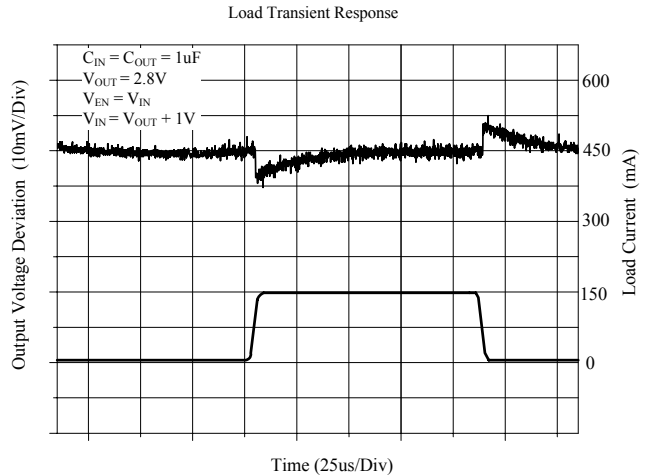
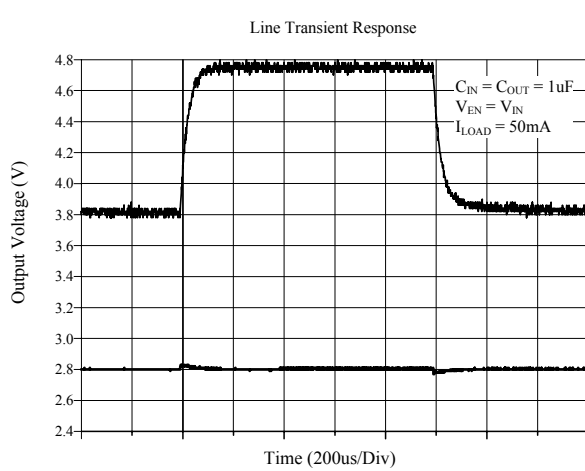


Ripple Rejection vs. Frequency



Enable Start-up Time





APPLICATION INFORMATION

INPUT CAPACITOR

An input capacitor of $\geq 1.0\mu\text{F}$ is required between the PT5107 V_{IN} and GND pin. This capacitor must be located within 1cm distance from V_{IN} pin and connected to a clear ground. A ceramic capacitor is recommended although a good quality tantalum or film may be used at the input. However, a tantalum capacitor can suffer catastrophic failures due to surge current when connected to a low impedance power supply (such as a battery or a very large capacitor).

There is no requirement for the ESR on the input capacitor, but the tolerance and temperature coefficient must be considered in order to ensure the capacitor work within the operation range over the full range of temperature and operating conditions.

OUTPUT CAPACITOR

In applications, it is important to select the output capacitor to keep the PT5107 in stable operation. The output capacitor must meet all the requirements specified in the following recommended capacitor table over all conditions in applications. The minimum capacitance for stability and correct operation is $0.6\mu\text{F}$. The capacitance tolerance should be $\pm 30\%$ or better over the operation temperature range. The recommended capacitor type is X7R to meet the full device temperature specification.

Recommended Output Capacitor (C_{OUT})

	TYP	MIN	MAX	Unit
Capacitance	1.0	0.6	10	μF
ESR		0	400	$\text{m}\Omega$

The capacitor application conditions also include

DC-bias, frequency and temperature. Unstable operation will result if the capacitance drops below minimum specified value (see the next section Capacitor Characteristics).

The PT5107 is designed to work with very small ceramic output capacitors. A $1.0\mu\text{F}$ capacitor (X7R type) with ESR type between 0 and $400\text{m}\Omega$ is suitable in the PT5107 applications. X5R capacitors may be used but have a narrow temperature range. With these and other capacitor types (Y5V, Z6U) that may be used, selection relies on the range of operating conditions and temperature range for a specified application.

It may also be possible to use tantalum or film capacitors at the output, but these are not as good for reasons of size and cost.

It is also recommended that the output capacitor be located within 1cm from the output pin and return to a clean ground wire.

CAPACITOR CHARACTERISTICS

The PT5107 is designed to work with ceramic capacitor on the output to take advantage of the benefit they offer: for capacitor values from $1.0\mu\text{F}$ to $4.7\mu\text{F}$ range, ceramic capacitors are the smallest, least expensive and have the lowest ESR values (which is good for eliminating high frequency noise). The ESR of a typical $1\mu\text{F}$ ceramic capacitor is in the range of $20\text{m}\Omega$ to $40\text{m}\Omega$ that easily satisfies the ESR requirement for stability by the PT5107.

For both input and output capacitors careful understanding the capacitor specifications is required to

ensure correct device operation. The capacitor value can change greatly because of the operating condition and capacitor type. In particular the output capacitor selection should take account of all the capacitor parameters to ensure that the specification is satisfied for the application. Capacitor values can vary with DC bias conditions, temperature, and frequency of operation. Capacitor values will also demonstrate some decrease over time due to aging. The capacitor parameters are also dependant on the particular case size with smaller size giving poorer performance figures on general.

As an example, the following figure shows a typical graph showing a comparison of capacitor case sizes in Capacitance vs. DC Bias plot. As shown in the graph, as a result of the DC bias condition the capacitance value may drop below the minimum capacitance value given in the recommended capacitor table. It is also recommended that the capacitor manufacture's specification for the normal value capacitor are consulted for all conditions as some capacitor sizes may not be suitable in the actual application.

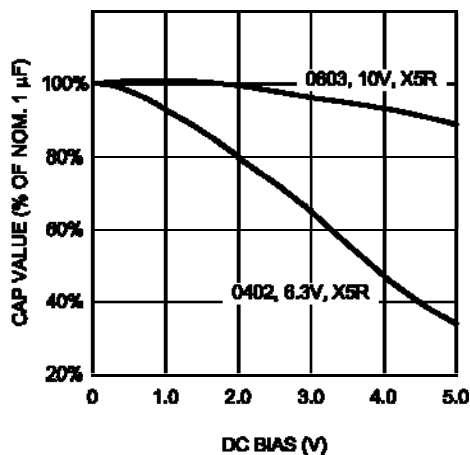


Figure 3. Graph showing a typical variation in capacitance vs. DC bias

The ceramic capacitor's capacitance can vary with temperature. The capacitor type X7R, which operates over a temperature range of -55°C to $+125^{\circ}\text{C}$, will only vary the capacitance to within $\pm 15\%$. The capacitor type X5R has a similar tolerance over a reduced temperature range of -55°C to $+85^{\circ}\text{C}$. Most large value ceramic capacitors ($2.2\mu\text{F}$) are manufactured with Z5U or Y5V temperature characteristics. Their capacitance can drop by more than 50% as the temperature goes from 25°C to 85°C . Therefore X7R is recommended over Z5U and Y5V in applications where the ambient temperature will change significantly above or below 25°C .

Tantalum capacitors are less desirable than ceramic for use as output capacitors because they are more expensive when comparing equivalent capacitance and voltage ratings in the $1\mu\text{F}$ to $4.7\mu\text{F}$ range.

Another important consideration is that tantalum capacitors have higher ESR values than equivalent size ceramics. This means that while it may be possible to find a tantalum capacitor with an ESR value within the stable range, it would have to be larger in capacitance (which means bigger and more costly) than a ceramic capacitor with the same ESR value. It should also be noted that the ESR of a typical tantalum will increase about 2:1 as the temperature goes from 25°C down to -40°C , so some guard band must be allowed.

NO-LOAD STABILITY

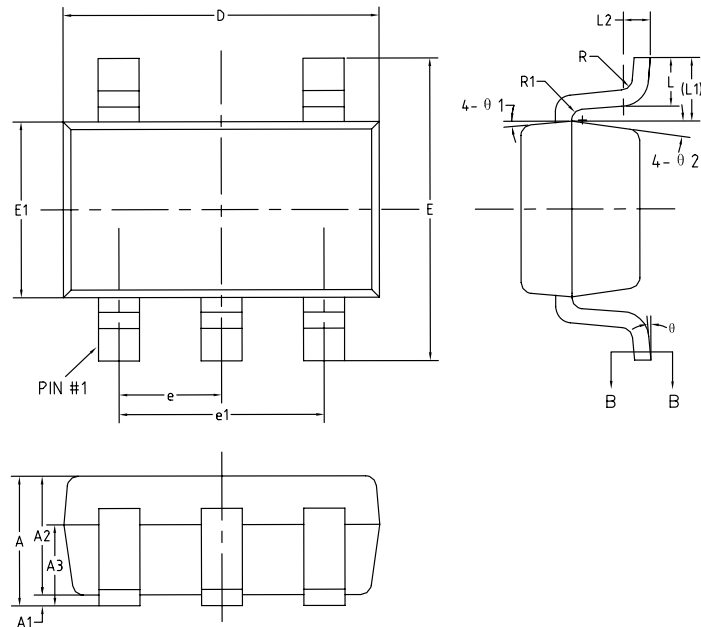
The PT5107 will remain stable and in regulation with no external load. This is especially important in CMOS RAM keep-alive applications.

ON/OFF INPUT OPERATION

The PT5107 is turned off by pulling the V_{EN} pin low, and turned on by pulling it high. If this function is not used, the V_{EN} pin should be tied to V_{IN} to keep the regulator output on at all time. To assure proper operation, the signal source used to drive the V_{EN} input must be able to swing above and below the specified turn-on/off voltage thresholds listed in the Electrical Characteristics section under V_{IL} and V_{IH}

PACKAGE INFORMATION

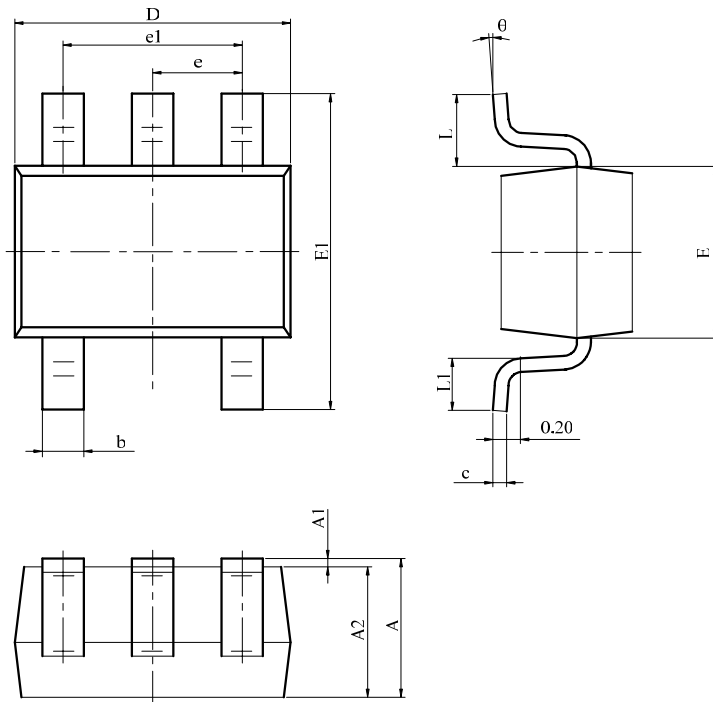
SOT23-5



SYMBOL	MILLIMETERS		
	MIN	TYP	MAX
A	-	-	1.25
A1	0	-	0.15
A2	1.00	1.10	1.20
A3	0.60	0.65	0.70
b	0.36	-	0.50
b1	0.36	0.38	0.45
c	0.14	-	0.20
c1	0.14	0.15	0.16
D	2.826	2.926	3.026
E	2.60	2.80	3.00
E1	1.526	1.626	1.726
e	0.95BSC		
e1	1.90BSC		
L	0.35	0.45	0.60
L1	0.59REF		
L2	0.25BSC		
R	0.10	-	-
R1	0.10	-	0.25
θ	0°	-	8°
θ_1	3°	5°	7°
θ_2	6°	8°	10°

PACKAGE INFORMATION

SC70-5



SYMBOL	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.150	0.350	0.006	0.014
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP		0.026 TYP	
e1	1.200	1.400	0.047	0.055
L	0.525 REF		0.021 REF	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°