

USB controller with external Amplifier and Recording function

Features

- Compliant with USB Specification v1.1, and USB 2.0 full speed
- Support I²S input and I²S output interface of master mode
 - Sampling frequencies(Fs): 48kHz
- Support recording function
- Supports Win Me//2000/XP and MacOS
- True plug-and-play application, no driver is required for basic USB speaker application
- Support volume/mute control with external button
- Built-in 5V to 3.3V regulator for internal device

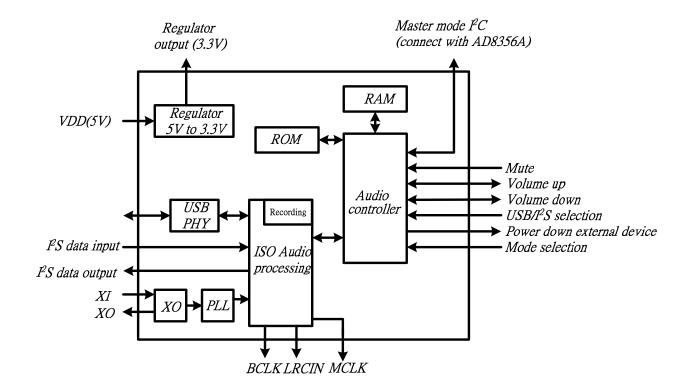
operation

- 12 MHz Crystal Input
- 32-pin LQFP(Pb free)

Description

AD62551A is a USB audio controller with I²S interface and supports recording function. The device also has an I²S input port and I²S output port. The I²S output port allows other high performance audio device (i.e. AD8356A/AD8256A) to be controlled by AD62551A.

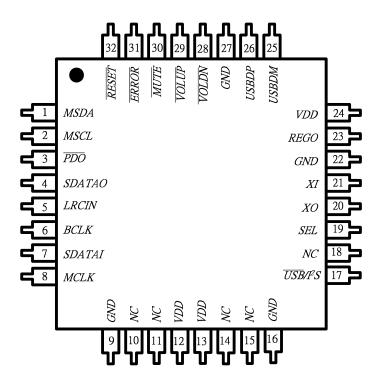
Functional Block Diagram



Publication Date: Apr. 2007 Revision: 1.2 1/10



Pin Assignment



Pin Description

| I III Description | | | | |
|-------------------|--------|------|---------------------------------------|----------------------------------|
| Pin | Name | Туре | Description | Characteristics |
| 1 | MSDA | I/O | I ² C's SDA of Master mode | Schmitt trigger TTL input buffer |
| 2 | MSCL | 0 | I ² C's SCL of master mode | |
| 3 | PDO | 0 | Power-down output (Note1) | |
| 4 | SDATAO | 0 | Serial audio output (Note1) | |
| 5 | LRCIN | 0 | L/R clock output(Fs) (Note1) | |
| 6 | BCLK | 0 | BCLK output(64xFs) (Note1) | |
| 7 | SDATAI | I | Serial audio data input | Schmitt trigger TTL input buffer |
| 8 | MCLK | 0 | Master clock(256xFs) | |
| 9 | GND | Р | Ground | |
| 10 | NC | NC | No connection | |
| 11 | NC | NC | No connection | |
| 12 | VDD | Р | 5V supply voltage | |

Elite Semiconductor Memory Technology Inc.

Publication Date: Apr. 2007 Revision: 1.2 **2/10**