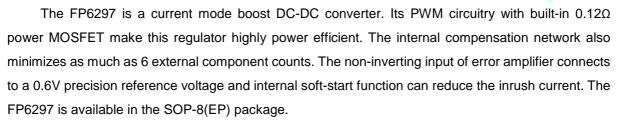


1MHz, 4A Step-Up Current Mode PWM Converter

General Description

ELING

INOLOGY



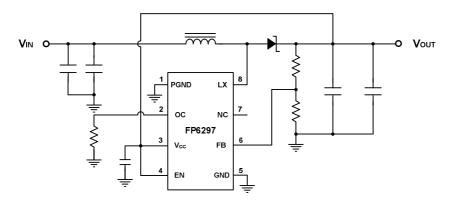
Features

- Adjustable Output up to 12V
- Internal Fixed PWM frequency: 1.0MHz
- Precision Feedback Reference Voltage: 0.6V (±2%)
- Internal 0.12Ω, 4A, 18V Power MOSFET
- Shutdown Current: 0.1µA
- Over Temperature Protection
- Over Voltage Protection
- > Adjustable Over Current Protection: 0.5A ~ 4.0A
- Package: SOP-8L

Applications

- > Chargers
- LCD Displays
- Digital Cameras
- > Handheld Devices
- Portable Products

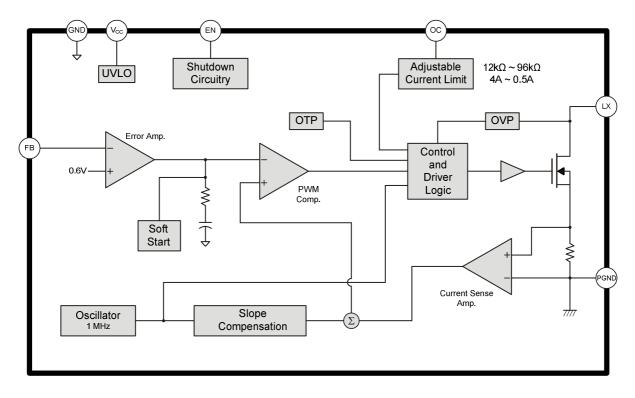
Typical Application Circuit





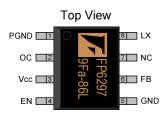


Function Block Diagram

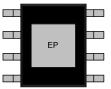


Pin Descriptions

SOP-8L(EP)



Bottom View



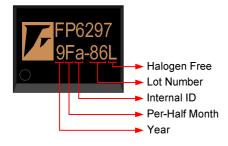
Name	No.	1/0	Description
PGND	1	Р	IC Ground
OC	2	I	Adjustable Current Limit (Floating Available)
Vcc	3	Р	IC Power Supply
EN	4	I	Enable Control (Active High)
GND	5	Р	IC Ground
FB	6	I	Error Amplifier Inverting Input
NC	7	NA	Not Connected
LX	8	0	Power Switch Output
EP	9	Р	Exposed PAD-Must connect to Ground





Marking Information

SOP-8L(EP)



Halogen Free: Halogen free product indicator.

Lot Number: Wafer lot number's last two digits.

For Example: $132386TB \rightarrow 86$

Internal ID: Internal Identification Code.

Per-Half Month: Production period indicated in half month time unit.

For Example: January→A(Front Half Month),B(Last Half Month)

February→C(Front Half Month),D(Last Half Month)

Year: Production year's last digit



Ordering Information

Part Number	Code	Operating Temperature	Package	MOQ	Description	
FP6297XR-G1		-40°C ~ 85°C	SOP-8L(EP)	2500EA	Tape & Reel	

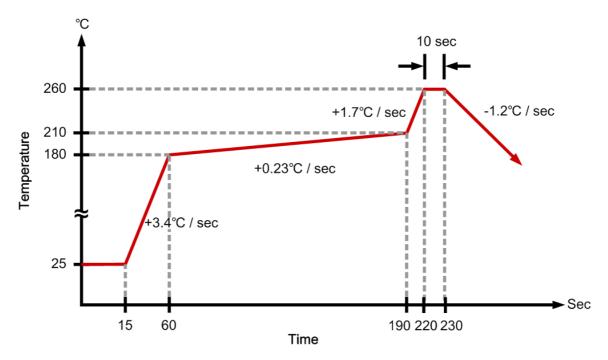
Absolute Maximum Ratings

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply Voltage	V _{cc}		0		6	V
LX Voltage	V _{LX}		0		18	V
EN,FB Voltage			0		6	V
Thermal Resistance (Note1)	θ _{JA}	SOP-8L(EP)			+60	°C / W
	θ _{JC}	SOP-8L(EP)			+4	°C / W
Junction Temperature	TJ				+150	°C
Operating Temperature	T _{OP}		-40		+85	°C
Storage Temperature	T _{ST}		-65		+150	°C
Lead Temperature		(soldering, 10 sec)			+260	°C

Note1:

 θ_{JA} is measured in the natural convection at $T_A=25^{\circ}C$ on a low effective thermal conductivity test board of JEDEC 51-3 thermal measurement standard.





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Recommended Operating Conditions

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Supply Voltage	Vin		2.6		5.5	V
Operating Temperature Range	T _A	Ambient Temperature	-40		+85	ĉ

DC Electrical Characteristics (V_{CC}=3.3V, T_A=25°C, unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
System Supply Input						
Input Supply Range	V _{cc}		2.6		5.5	V
Under Voltage Lockout	V _{UVLO}			2.2		V
UVLO Hysteresis				0.1		V
Quiescent Current	Icc	V _{FB} =0.66V, No switching		0.19		mA
Average Supply Current	I _{CC}	V _{FB} =0.55V, Switching		2.84		mA
Shutdown Supply Current	Icc	V _{EN} =GND		0.1		μA
Oscillator	·		•			
Operation Frequency	Fosc	V _{FB} =1.0V	0.8	1.0	1.2	MHz
Frequency Change with Voltage	$\triangle f / \triangle V$	V _{CC} =2.6V to 5.5V		5		%
Maximum Duty Cycle	T _{DUTY}			90		%
Reference Voltage						
Reference Voltage	V _{REF}		0.588	0.6	0.612	V
Line Regulation		V _{CC} =2.6V ~ 5.5V		0.2		% / V
Enable Control						
Enable Voltage	V _{EN}		0.96			V
Shutdown Voltage	V _{EN}				0.6	V
MOSFET						
On Resistance of Driver	R _{DS (ON)}	I _{LX} =2A		0.12		Ω
Protection	•					
OCP Current	I _{OCP}			4		А
Adjustable OCP Current	I _{OCP}	With External Resistor: 12k~96k	0.5		4	А
OTP Temperature	T _{OTP}			+150		°C



Function Description

Operation

The FP6297 is a current mode boost converter. The constant switching frequency is 1MHz and operates with pulse width modulation (PWM). Build-in 18V / 4A MOSFET provides a high output voltage. The control loop architecture is peak current mode control; therefore slope compensation circuit is added to the current signal to allow stable operation for duty cycles larger than 50%.

Soft Start Function

Soft start circuitry is integrated into FP6297 to avoid inrush current during power on. After the IC is enabled, the output of error amplifier is clamped by the internal soft-start function, which causes PWM pulse width increasing slowly and thus reducing input surge current.

Current Limit Program

A resistor between OC and GND pin programs peak switch current. The resistor value should be between 12k and 96k. The current limit will be set from 4A to 0.5A. Keep traces at this pin as short as possible. Do not put capacitance at this pin. To set the over current trip point according to the following equation:

$$I_{OCP} = \frac{48000}{R3}$$

Over Temperature Protection (OTP)

FP6297 will turn off the power MOSFET automatically when the internal junction temperature is over 150°C. The power MOSFET wake up when the junction temperature drops 30°C under the OTP threshold temperature.

Over Voltage Protection (OVP)

In some condition, the resistive divider may be unconnected, which will cause PWM signal to operate with maximum duty cycle and output voltage is boosted higher and higher. The power MOSFET will be turned off immediately, when the output voltage exceeds the OVP threshold level. The FP6297's OVP threshold is 16V.

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Application Information

Inductor Selection

Inductance value is decided based on different condition. 3.3uH to 4.7µH inductor value is recommended for general application circuit. There are three important inductor specifications, DC resistance, saturation current and core loss. Low DC resistance has better power efficiency. Also, it avoid inductor saturation which will cause circuit system unstable and lower core loss at 1 MHz.

Capacitor Selection

The output capacitor is required to maintain the DC voltage. Low ESR capacitors are preferred to reduce the output voltage ripple. Ceramic capacitor of X5R and X7R are recommended, which have low equivalent series resistance (ESR) and wider operation temperature range.

Diode Selection

Schottky diodes with fast recovery times and low forward voltages are recommended. Ensure the diode average and peak current rating exceed the average output current and peak inductor current. In addition, the diode's reverse breakdown voltage must exceed the output voltage.

Output Voltage Programming

The output voltage is set by a resistive voltage divider from the output voltage to FB. The output voltage is:

$$V_{OUT} = 0.6V \left(1 + \frac{R1}{R2}\right)$$

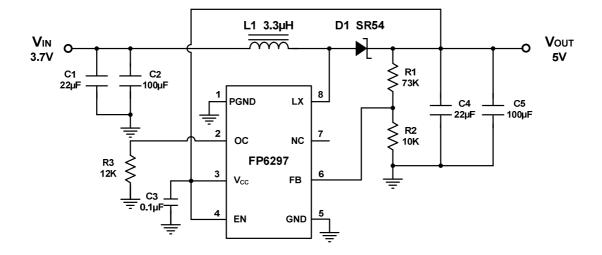
Layout Considerations

- 1. The power traces, consisting of the GND trace, the LX trace and the V_{CC} trace should be kept short, direct and wide.
- 2. LX > L and D switching node, wide and short trace to reduce EMI.
- 3. Place C_{IN} near V_{CC} pin as closely as possible to maintain input voltage steady and filter out the pulsing input current.
- 4. The resistive divider R1and R2 must be connected to FB pin directly as closely as possible.
- 5. FB is a sensitive node. Please keep it away from switching node, LX.
- 6. The GND of the IC, C_{IN} and C_{OUT} should be connected close together directly to a ground plane.

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Typical Application



General Boost Converter