



# SPN2012

## N-Channel Enhancement Mode MOSFET

### DESCRIPTION

The SPN2012 is the N-Channel enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching , low in-line power loss, and resistance to transients are needed.

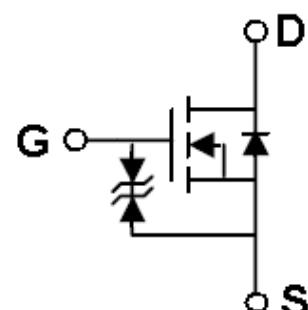
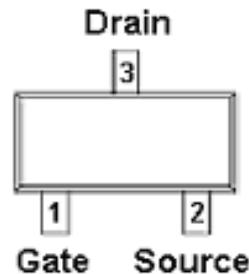
### APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

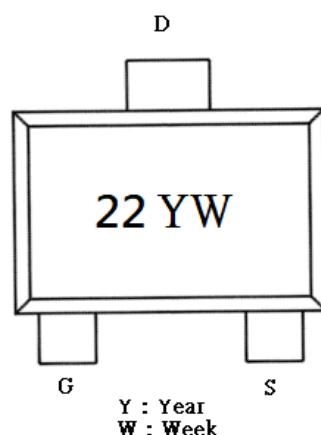
### FEATURES

- ◆ N-Channel  
20V/0.95A,R<sub>DS(ON)</sub>=310mΩ@V<sub>GS</sub>=4.5V  
20V/0.75A,R<sub>DS(ON)</sub>=360mΩ@V<sub>GS</sub>=2.5V  
20V/0.65A,R<sub>DS(ON)</sub>=460mΩ@V<sub>GS</sub>=1.8V
- ◆ Super high density cell design for extremely low R<sub>DS(ON)</sub>
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOT-23 package design

### PIN CONFIGURATION( SOT-23 )



### PART MARKING





# SPN2012

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### PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPN2012S23RGB	SOT-23	22YW

※ SPN2012S23RGB : Tape Reel ; Pb – Free ; Halogen – Free

### ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V <sub>DSS</sub>	20	V
Gate –Source Voltage	V <sub>GSS</sub>	±12	V
Continuous Drain Current(T <sub>J</sub> =150°C)	I <sub>D</sub>	0.65	A
Pulsed Drain Current	I <sub>DM</sub>	0.45	A
Continuous Source Current(Diode Conduction)	I <sub>S</sub>	0.3	A
Power Dissipation	P <sub>D</sub>	0.15	W
Operating Junction Temperature	T <sub>J</sub>	-55/150	°C
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C



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### ELECTRICAL CHARACTERISTICS

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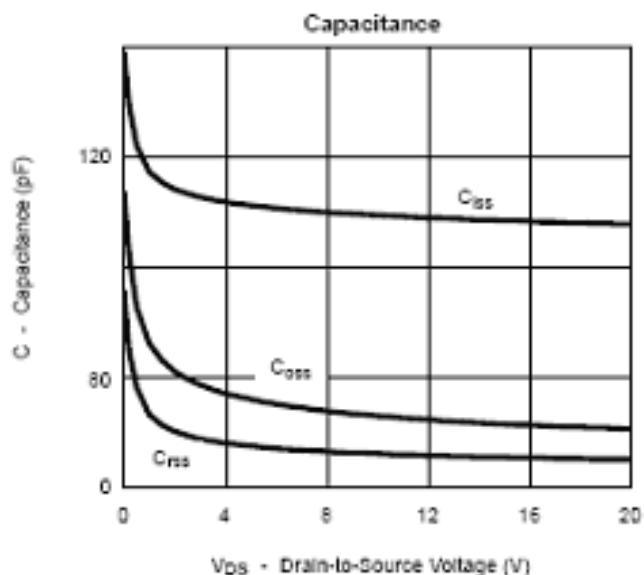
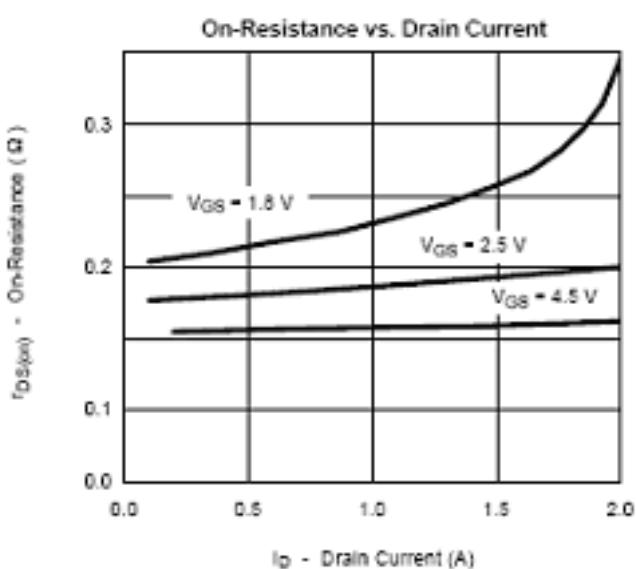
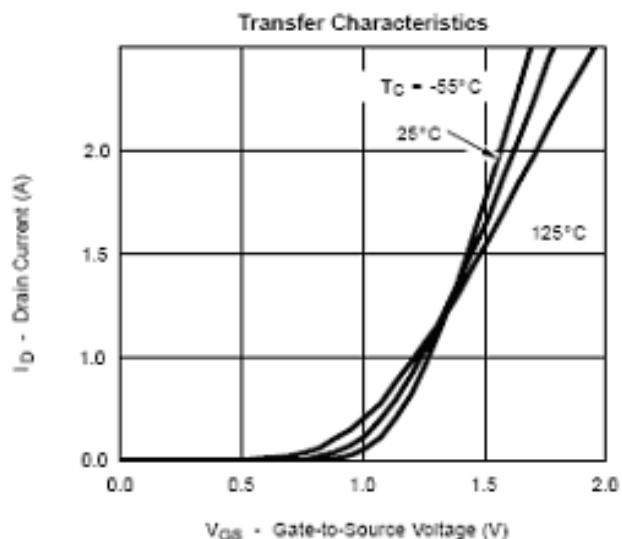
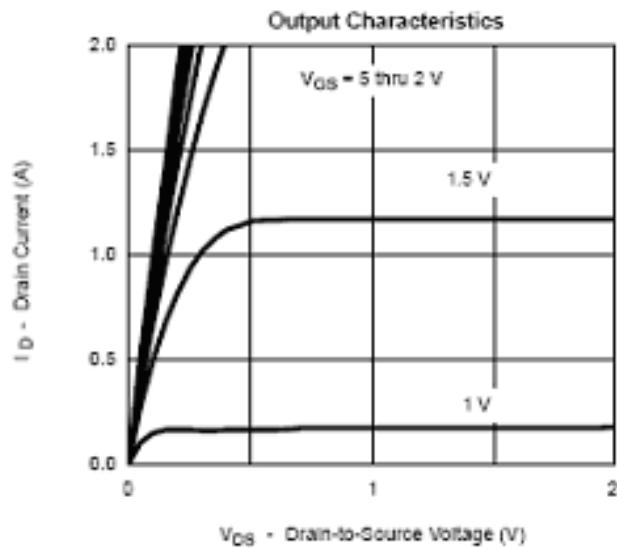
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, ID= 250uA	20			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , ID=250uA	0.35		1.0	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			30	uA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> = 20V, V <sub>GS</sub> =0V T <sub>J</sub> =55°C			5	
On-State Drain Current	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 4.5V, V <sub>GS</sub> =5V	0.7			A
Drain-Source On-Resistance	R <sub>D(on)</sub>	V <sub>GS</sub> =4.5V, ID=0.95A			0.31	Ω
		V <sub>GS</sub> =2.5V, ID=0.75A			0.36	
		V <sub>GS</sub> =1.8V, ID=0.65A			0.46	
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =10V, ID=0.4A		1.0		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =0.15A, V <sub>GS</sub> =0V		0.8	1.2	V
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =10V, V <sub>GS</sub> =4.5V, ID=0.6A		1.2	1.5	nC
Gate-Source Charge	Q <sub>gs</sub>			0.2		
Gate-Drain Charge	Q <sub>gd</sub>			0.3		
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =10V, R <sub>L</sub> =10Ω , ID=0.5A V <sub>GEN</sub> =4.5V , R <sub>G</sub> =6Ω		5	10	ns
	t <sub>r</sub>			8	15	
Turn-Off Time	t <sub>d(off)</sub>			10	18	
	t <sub>f</sub>			1.2	2.8	



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### TYPICAL CHARACTERISTICS

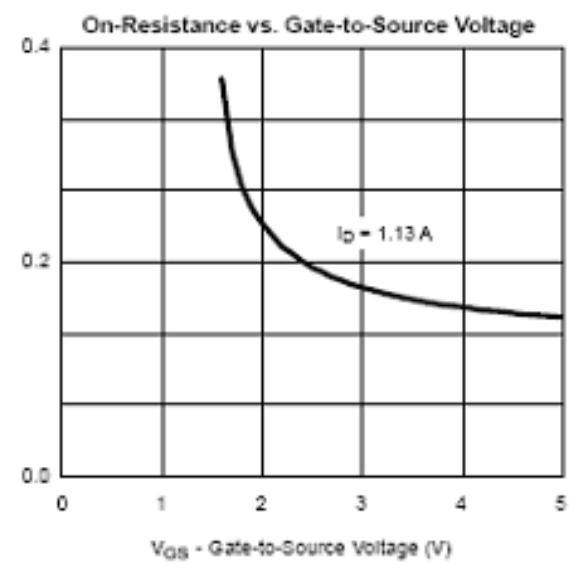
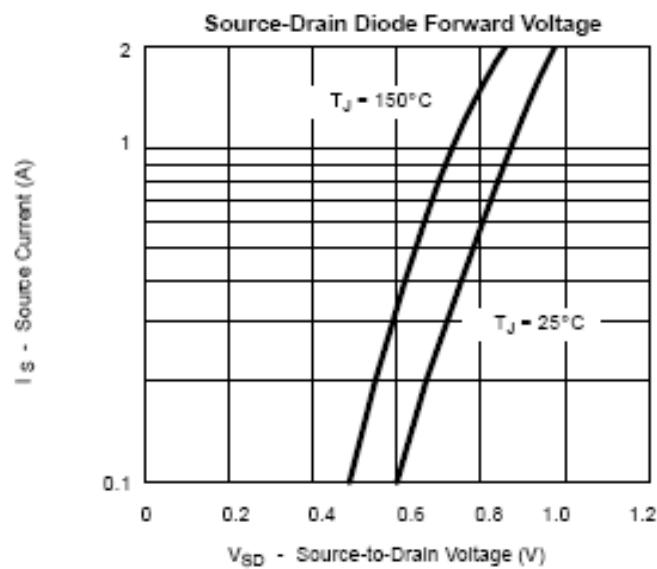
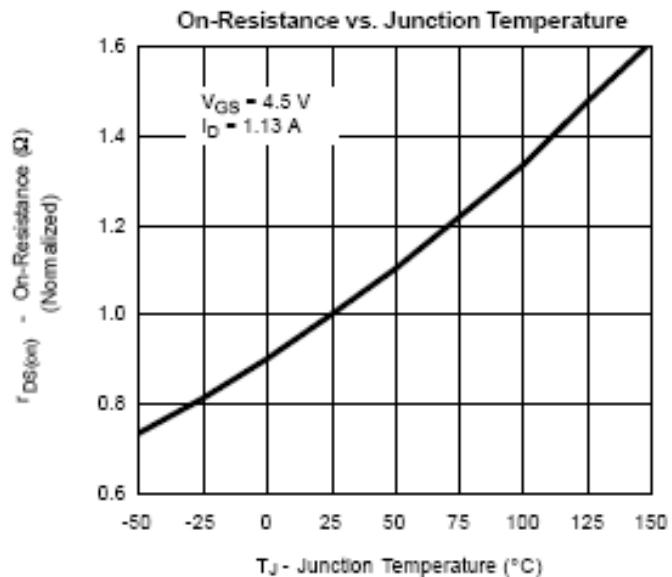
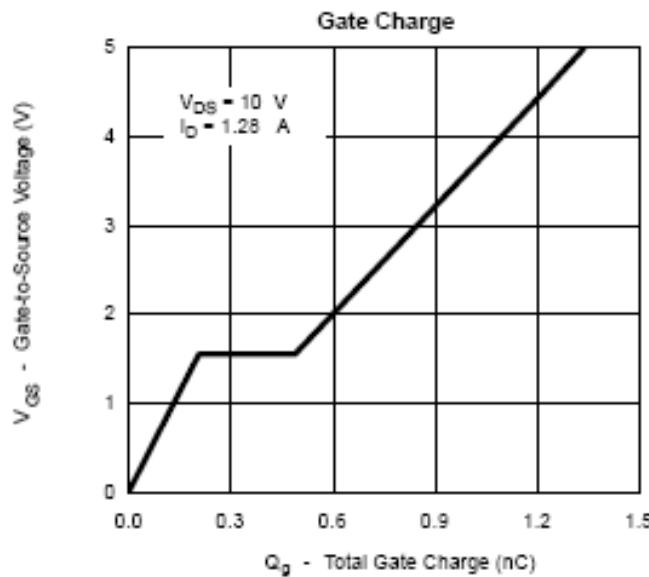




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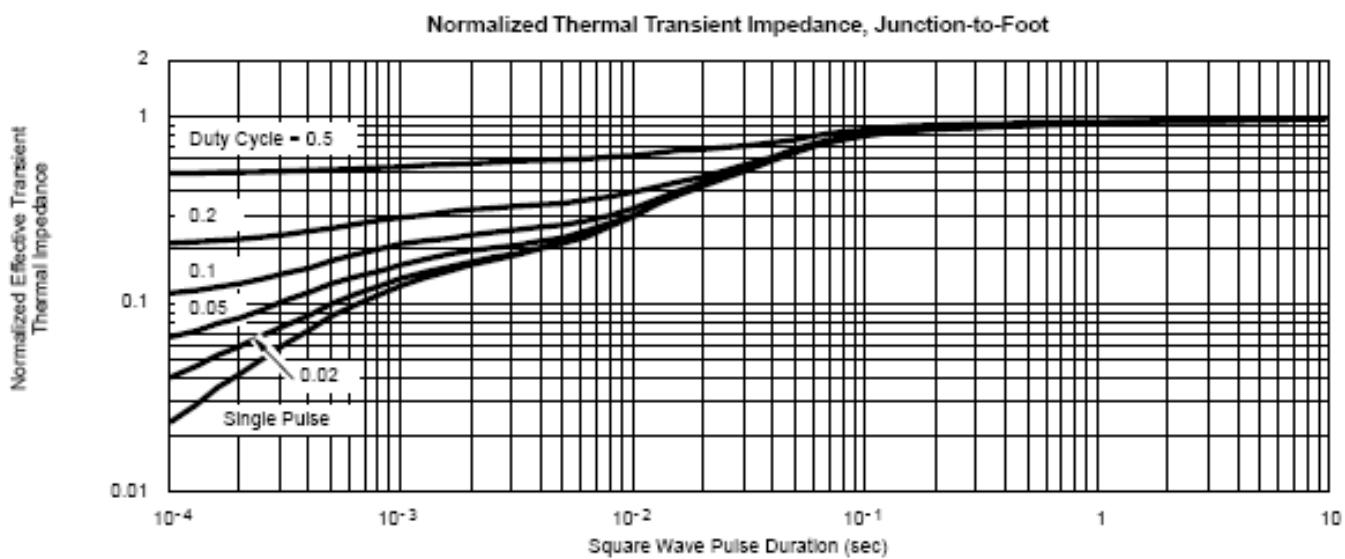
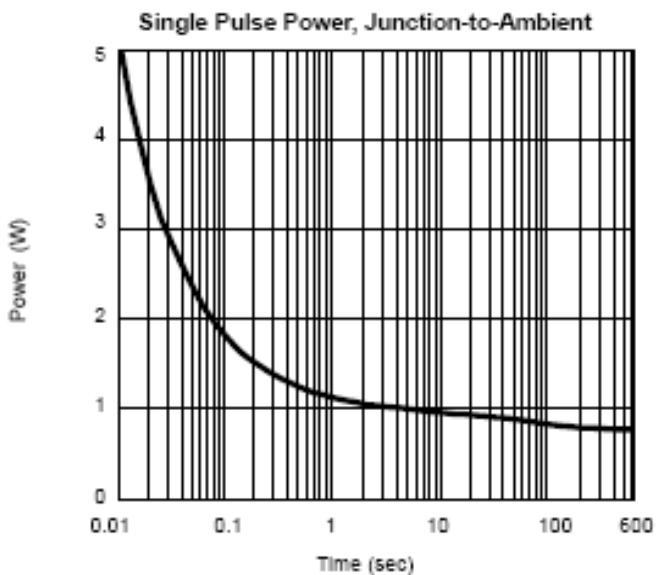
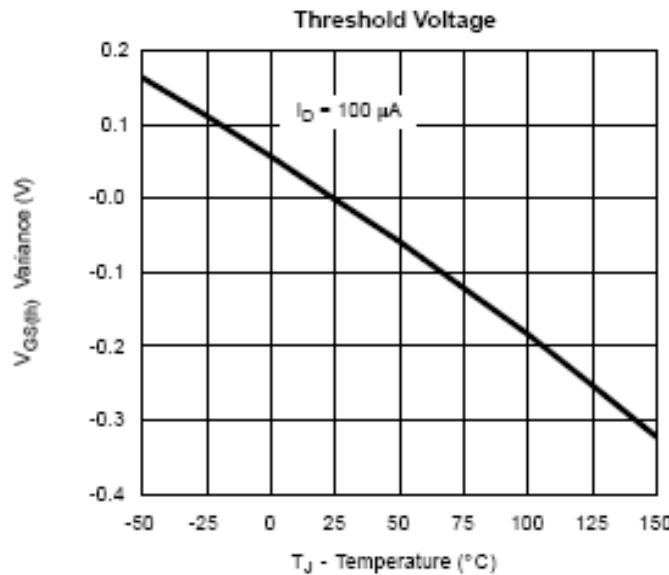




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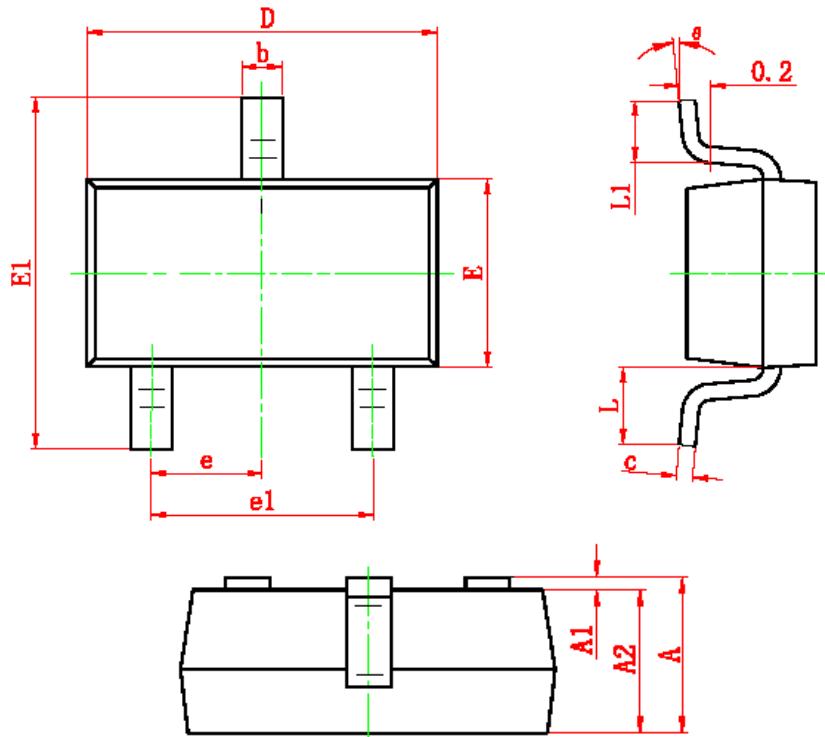




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### SOT-23 PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.200	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.100	0.035	0.039
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	6°



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