

DESCRIPTION

The SPN8460 is the N-Channel logic enhancement mode power field effect transistor which is produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance. These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits, and low in-line power loss are needed in a small outline surface mount package.

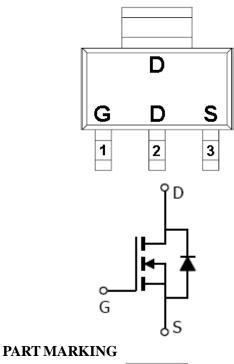
APPLICATIONS

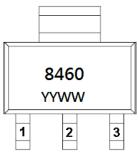
- Power Tool
- DC/DC Converter
- Load Switch

FEATURES

- 60V/2.5A, RDS(ON)= $120m\Omega$ @VGS=10V
- 60V/2.0A, RDS(ON)= $130m\Omega$ @VGS=4.5V
- Super high density cell design for extremely low RDS (ON)
- Exceptional on-resistance and maximum DC current capability
- SOT-223 package design

PIN CONFIGURATION(SOT-223)





Y: Year Code W: Week Code

PINDESCRIPTION				
Pin	Symbol	Description		
1	G	Gate		
2	D	Drain		
3	S	Source		

ORDERINGINFORMATION

Part Number	Package	Part Marking
SPN8460S22RGB	SOT-223	8460

SPN8460S22RGB: Tape Reel; Pb – Free; Halogen – Free

ABSOULTE MAXIMUM RATINGS

(Ta=25°C Unless otherwise noted)

Parameter		Symbol	Typical	Unit
Drain-Source Voltage		Vdss	60	V
Gate –Source Voltage		VGSS	±20	V
Continuous Drain Current(TJ=150°C)	Ta=25°C	ID	4	A
, , ,	Ta=70°C		2.8	A
Pulsed Drain Current		Ірм	25	A
Continuous Source Current(Diode Conduction)		Is	2.5	A
Down Dissination	TA=25°C	PD	3	W
Power Dissipation	Ta=70°C		1.1	vv
Operating Junction Temperature		TJ	150	$^{\circ}\!\mathbb{C}$
Storage Temperature Range		Tstg	-55/150	$^{\circ}\!\mathbb{C}$
Thermal Resistance-Junction to Ambient		RθJA	42	°C/W

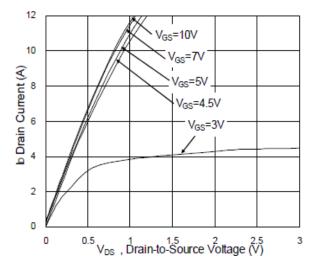
ELECTRICAL CHARACTERISTICS

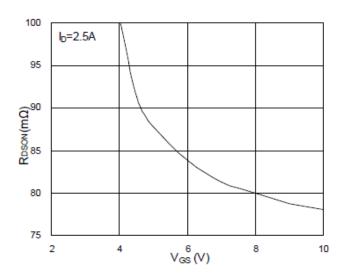
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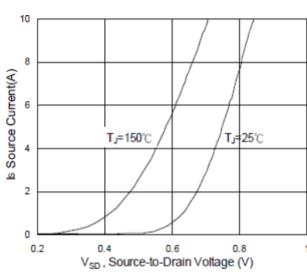
Parameter	Symbol	Conditions	Min.	Тур	Max.	Unit	
Static	·						
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V,ID=250uA	60			V	
Gate Threshold Voltage	VGS(th)	VDS=VGS,ID=250uA	0.5		1.5] v	
Gate Leakage Current	Igss	V _{DS} =0V,V _{GS} =±20V			±100	nA	
		VDS=48V,VGS=0V			1		
Zero Gate Voltage Drain Current	IDSS	VDS=48V,VGS=0V TJ=55°C			5	uA	
On-State Drain Current	ID(on)	$V_{DS} \ge 10V, V_{GS} = 4.5V$	4			A	
Drain-Source On-Resistance	Dr.g()	VGS = 10V,ID=2.5A			120	m0	
Drain-Source On-Resistance	RDS(on)	VGS =4.5V,ID=2A			130	mΩ	
Forward Transconductance	gfs	Vds=5V,Id=2A		7		S	
Diode Forward Voltage	Vsd	Is=2.5A,VGS=0V			1.2	V	
Dynamic							
Total Gate Charge	Qg			5	7	nC	
Gate-Source Charge	Qgs	V _{DS} =48V, V _{GS} =4.5V I _D =2A		1.68	2.4		
Gate-Drain Charge	Qgd	10-211		1.9	2.7	1	
Input Capacitance	Ciss			511			
Output Capacitance	Coss	VDS=15V, VGS=0V f=1MHz		38		pF	
Reverse Transfer Capacitance	Crss			25			
Turn-On Time	td(on)			1.6	3.2	ne	
	tr	VDS=30V, ID=2.0A,		7.2	13		
Turn-Off Time	td(off)	$V_{GS}=10V, R_{G}=3.3\Omega$		25	50	ns	
	tf			14.5	29		

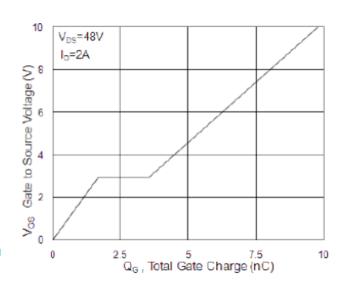


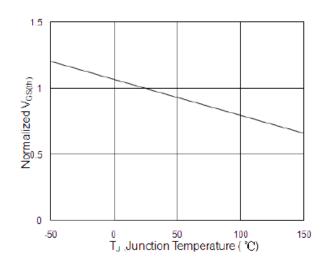
TYPICAL CHARACTERISTICS

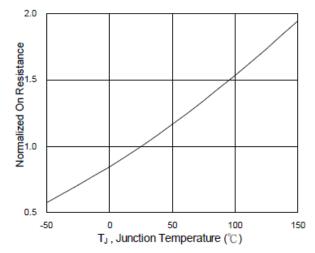




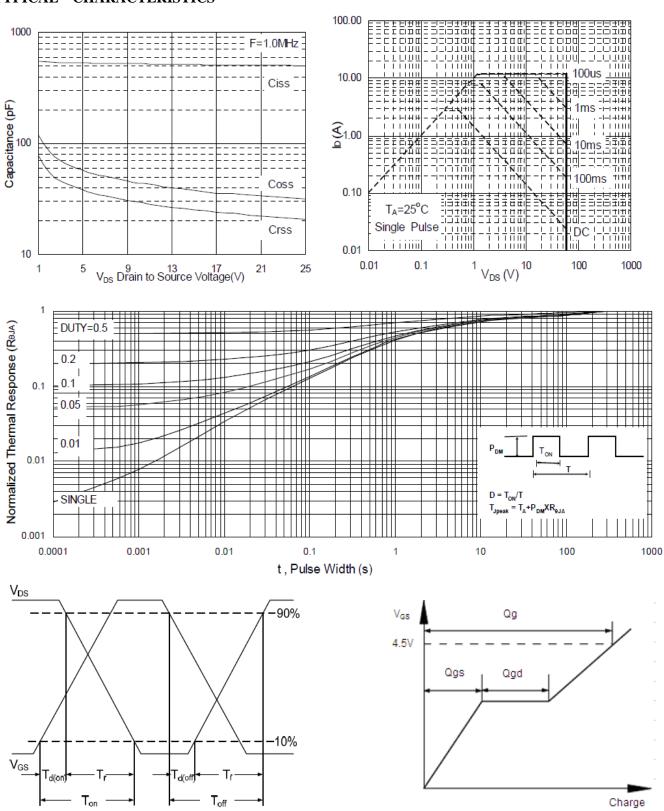








CHARACTERISTICS TYPICAL

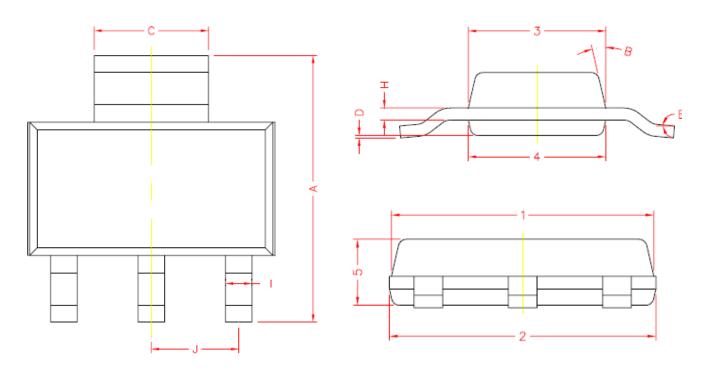


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Charge



SOT-233 PACKAGE OUTLINE



	DIMENSIONS		
REF.	Millimeters		
	Min.	Max.	
Α	6.70	7.30	
С	2.90	3.10	
D	0.02	0.10	
Ε	0*	10°	
- 1	0.60	0.80	
Н	0.25	0.35	
В	13' TYP.		
J	2.30 REF.		
1	6.30	6.70	
2	6.30	6.70	
3	3.30	3.70	
4	3.30	3.70	
5	1.40	1.80	

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