

#### DESCRIPTION

The SPN1072 is the N-Channel enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching, low in-line power loss, and resistance to transients are needed.

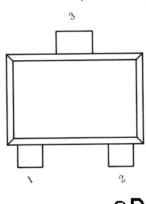
#### **APPLICATIONS**

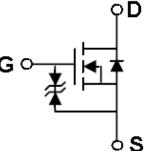
- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

#### **FEATURES**

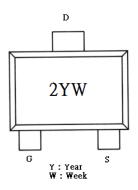
- N-Channel 20V/0.95A, RDS(ON)= $380m\Omega@VGS$ =4.5V 20V/0.75A, RDS(ON)= $450m\Omega@VGS$ =2.5V 20V/0.65A, RDS(ON)= $800m\Omega@VGS$ =1.8V
- Super high density cell design for extremely low RDS (ON)
- Exceptional on-resistance and maximum DC current capability
- ◆ SOT-723 package design

## PIN CONFIGURATION(SOT-723)





### **PART MARKING**



#### PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

## ORDERING INFORMATION

Part Number	Package	Part Marking
SPN1072S72RGB	SOT-723	2

<sup>\*</sup> SPN1072S72RGB: Tape Reel; Pb – Free; Halogen – Free

## ABSOULTE MAXIMUM RATINGS

(Ta=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit	
Drain-Source Voltage	VDSS	20	V	
Gate –Source Voltage		VGSS	±12	V
Continuous Drain Current(TJ=150°C) TA=25°C		ID	0.65	Α
Pulsed Drain Current		Idm	0.45	Α
Continuous Source Current(Diode Conduction)		Is	0.3	Α
Power Dissipation	Ta=25°C	PD	0.15	W
Operating Junction Temperature		Тл	-55/150	$^{\circ}\!\mathbb{C}$
Storage Temperature Range		Tstg	-55/150	$^{\circ}\!\mathbb{C}$

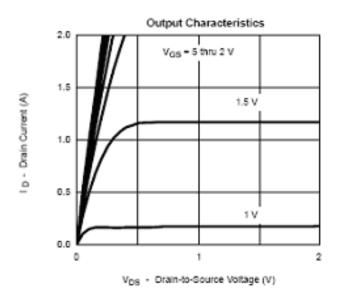
## **ELECTRICAL CHARACTERISTICS**

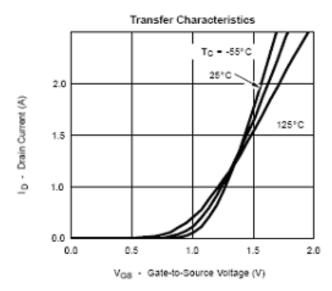
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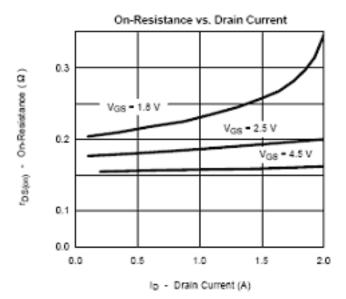
Parameter	Symbol	Conditions	Min.	Тур	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V(BR)DSS	V <sub>G</sub> S=0V,I <sub>D</sub> = 250uA	20			V
Gate Threshold Voltage	VGS(th)	VDS=VGS,ID=250uA	0.35		1.0	\ \ \ \
Gate Leakage Current	Igss	V <sub>DS</sub> =0V,V <sub>GS</sub> =±12V			30	uA
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = 20V,V <sub>GS</sub> =0V V <sub>DS</sub> = 20V,V <sub>GS</sub> =0V T <sub>J</sub> =55°C			5	uA
On-State Drain Current	ID(on)	V <sub>DS</sub> ≥ 4.5V,V <sub>GS</sub> =5V	0.7			A
Drain-Source On-Resistance	RDS(on)	V <sub>GS</sub> =4.5V,I <sub>D</sub> =0.95A V <sub>GS</sub> =2.5V,I <sub>D</sub> =0.75A V <sub>GS</sub> =1.8V,I <sub>D</sub> =0.65A		0.26 0.32 0.42	0.38 0.45 0.80	Ω
Forward Transconductance	gfs	V <sub>DS</sub> =10V,I <sub>D</sub> =0.4A		1.0		S
Diode Forward Voltage	Vsd	Is=0.15A,VGS=0V		0.8	1.2	V
Dynamic	•					
Total Gate Charge	Qg	V <sub>DS</sub> =10V,V <sub>GS</sub> =4.5V,		1.2	1.5	nC
Gate-Source Charge	Qgs	ID≡0.6A		0.2		
Gate-Drain Charge	Qgd			0.3		
Turn-On Time	td(on)	$V_{DD}=10V_{,RL}=10\Omega$ ,		5	10	ns
	tr	ID=0.5A		8	15	
Turn Off Times	td(off)	VGEN= $4.5$ V ,RG= $6\Omega$		10	18	
Turn-Off Time	tf			1.2	2.8	

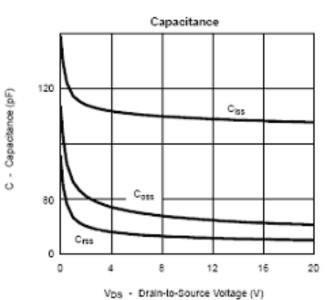
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## TYPICAL CHARACTERISTICS

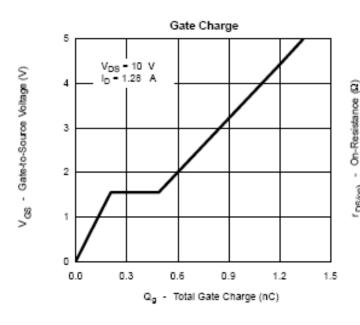


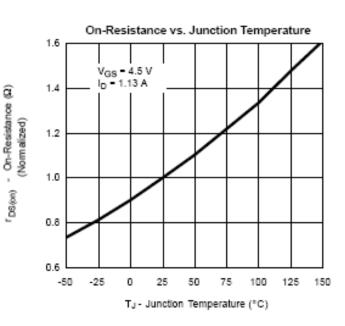


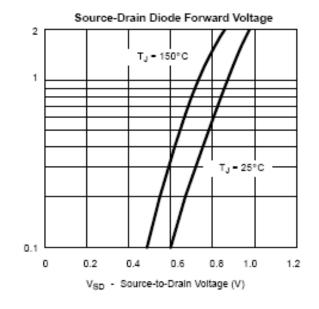


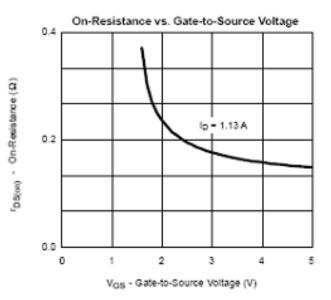


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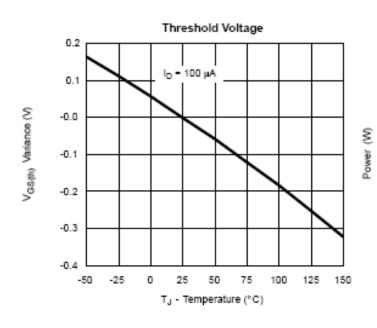


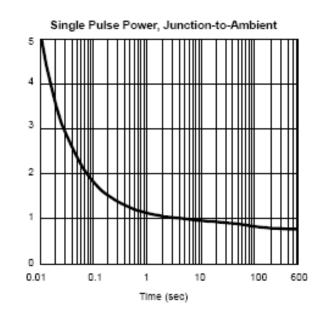




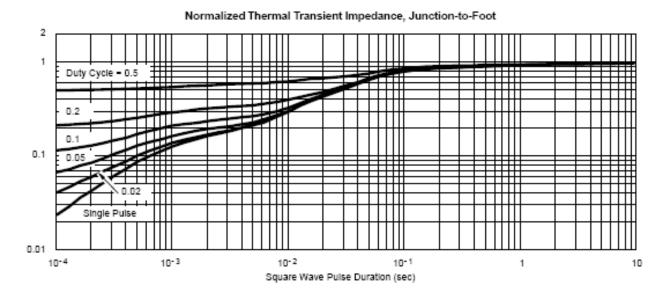
Is - Source Current (A)

## TYPICAL CHARACTERISTICS



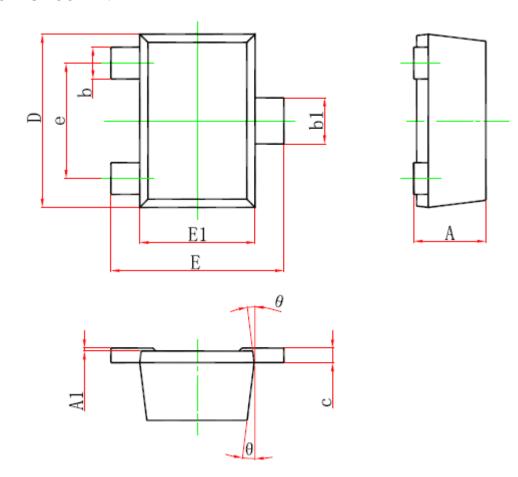








# **SOT-723 PACKAGE OUTLINE**



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α		0.500		0.020	
A1	0.000	0.050	0.000	0.002	
b	0.170	0.270	0.007	0.011	
b1	0.270	0.370	0.011	0.015	
С		0.150		0.006	
D	1.150	1.250	0.045	0.049	
E	1.150	1.250	0.045	0.049	
E1	0.750	0.850	0.030	0.033	
e	0.800TYP.		0.031TYP.		
θ	7° REF.		7° REF.		

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SYNC Power Corporation
7F-2, No.3-1, Park Street
NanKang District (NKSP), Taipei, Taiwan 115
Phone: 886-2-2655-8178

Fax: 886-2-2655-8468

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