



SPC4516B

N & P Pair Enhancement Mode MOSFET

DESCRIPTION

The SPC4516B is the N- and P-Channel enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching , low in-line power loss, and resistance to transients are needed.

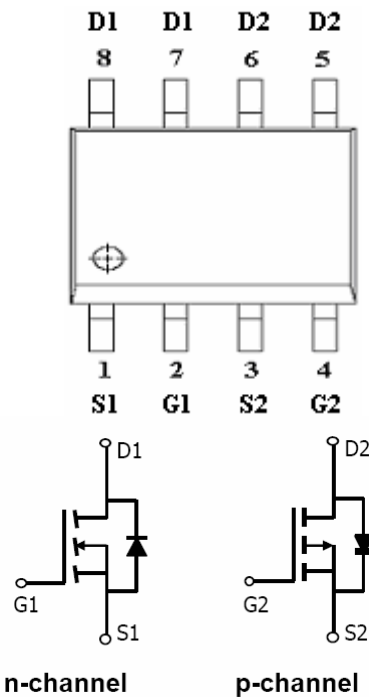
FEATURES

- ◆ N-Channel
30V/8.5A, $R_{DS(ON)} = 17m\Omega @ V_{GS} = 10V$
30V/7.8A, $R_{DS(ON)} = 20m\Omega @ V_{GS} = 4.5V$
- ◆ P-Channel
-30V/-8.2A, $R_{DS(ON)} = 24m\Omega @ V_{GS} = -10V$
-30V/-7.26A, $R_{DS(ON)} = 30m\Omega @ V_{GS} = -4.5V$
- ◆ Super high density cell design for extremely low $R_{DS(ON)}$
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOP – 8P package design

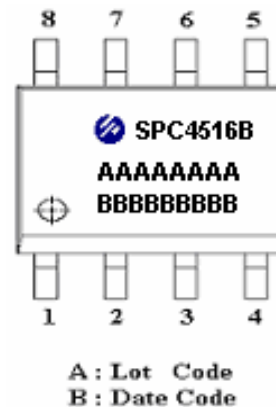
APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

PIN CONFIGURATION(SOP – 8P)



PART MARKING





SPC4516B

N & P Pair Enhancement Mode MOSFET

PIN DESCRIPTION

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

ORDERING INFORMATION

Part Number	Package	Part Marking
SPC4516BS8RGB	SOP- 8P	SPC4516B

※ SPC4516BS8RGB : 13" Tape Reel ; Pb – Free ; Halogen - Free

ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical		Unit	
		N-Channel	P-Channel		
Drain-Source Voltage	V _{DSS}	30	-30	V	
Gate –Source Voltage	V _{GSS}	±20	±20	V	
Continuous Drain Current(T _J =150°C)	I _D	TA=25°C	8.5	-7.2	A
		TA=70°C	7.5	-5.6	
Pulsed Drain Current	I _{DM}	20	-20	A	
Continuous Source Current(Diode Conduction)	I _S	2.3	-2.3	A	
Power Dissipation	P _D	TA=25°C	2.5	2.8	W
		TA=70°C	1.6	1.8	
Operating Junction Temperature	T _J	-55/150		°C	
Storage Temperature Range	T _{STG}	-55/150		°C	
Thermal Resistance-Junction to Ambient	R _{θJA}	T ≤ 10sec	50	52	°C/W
		Steady State	80	80	



SPC4516B

N & P Pair Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (NMOS)

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.6		1.8	
Gate Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=24V, V_{GS}=0V$			1	uA
		$V_{DS}=24V, V_{GS}=0V$ $T_J=85^\circ C$			5	
On-State Drain Current	$I_{D(on)}$	$V_{DS}\geq 5V, V_{GS}=10V$	25			A
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=8.5A$		0.014	0.017	Ω
		$V_{GS}=4.5V, I_D=7.8A$		0.017	0.020	
Forward Transconductance	g_{fs}	$V_{DS}=15V, I_D=6.2A$		13		S
Diode Forward Voltage	V_{SD}	$I_S=2.3A, V_{GS}=0V$		0.5	1.0	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=15V, V_{GS}=10V$ $I_D=2A$		10	18	nC
Gate-Source Charge	Q_{gs}			2.8		
Gate-Drain Charge	Q_{gd}			2.0		
Input Capacitance	C_{iss}	$V_{DS}=15V, V_{GS}=0V$ $f=1MHz$		850		pF
Output Capacitance	C_{oss}			158		
Reverse Transfer Capacitance	C_{rss}			120		
Turn-On Time	$t_{d(on)}$	$V_{DD}=15V, R_L=15\Omega$ $I_D=5.0A, V_{GEN}=10V$ $R_G=1\Omega$		10	15	nS
	t_r			4	12	
Turn-Off Time	$t_{d(off)}$			15	30	
	t_f			10	15	



SPC4516B

N & P Pair Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS (PMOS)

(TA=25°C Unless otherwise noted)

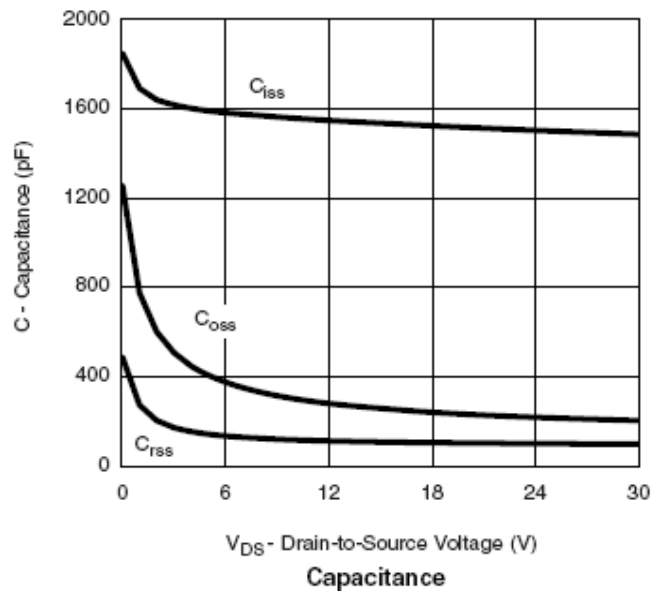
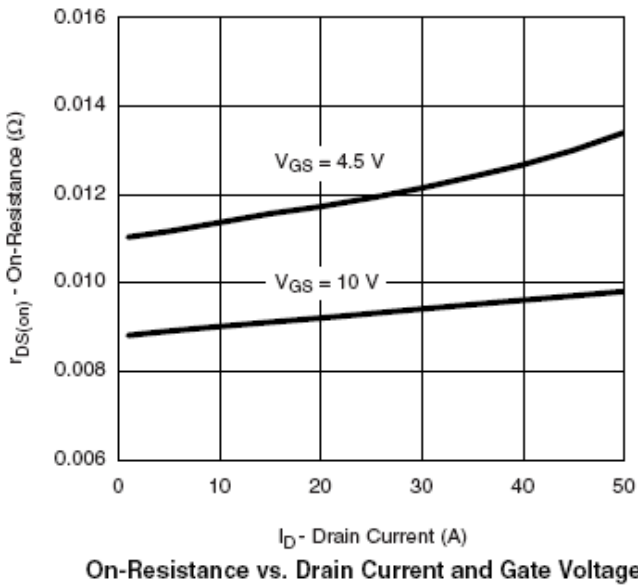
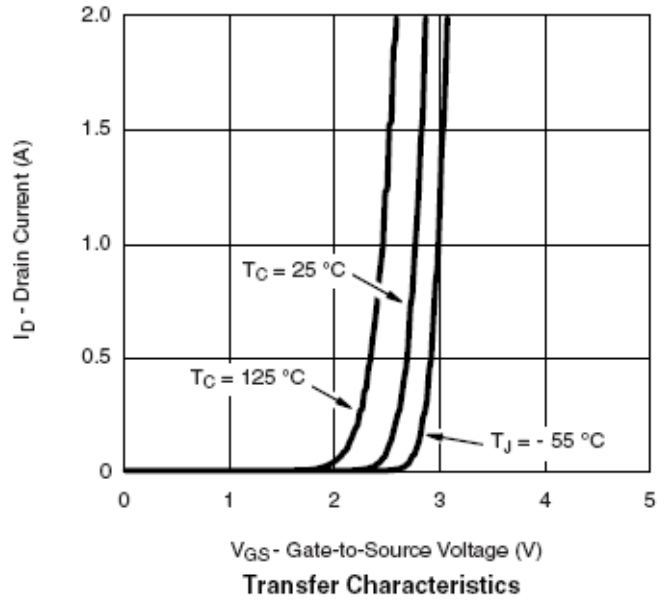
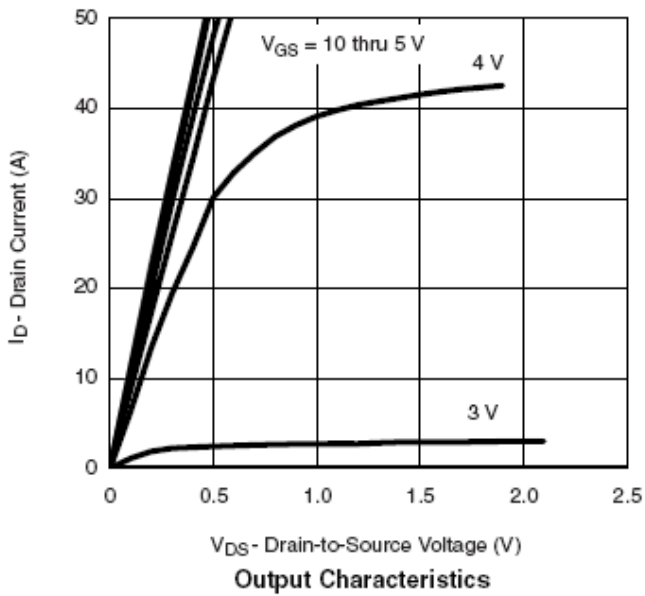
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.7		-1.6	
Gate Leakage Current	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-24V, V_{GS}=0V$			-1	uA
		$V_{DS}=-24V, V_{GS}=0V$ $T_J=85^\circ C$			-5	
On-State Drain Current	$I_{D(on)}$	$V_{DS}=-5V, V_{GS}=-4.5V$	-40			A
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-8.2A$		0.020	0.024	Ω
		$V_{GS}=-4.5V, I_D=-7.2A$		0.025	0.030	
Forward Transconductance	g_{fs}	$V_{DS}=-10V, I_D=-9.0A$		24		S
Diode Forward Voltage	V_{SD}	$I_S=-2.3A, V_{GS}=0V$		-0.8	-1.2	V
Dynamic						
Total Gate Charge	Q_g	$V_{DS}=-15V, V_{GS}=-10V$ $I_D=-9.0A$		20	30	nC
Gate-Source Charge	Q_{gs}			3.5		
Gate-Drain Charge	Q_{gd}			4.8		
Input Capacitance	C_{iss}	$V_{DS}=-15V, V_{GS}=0V$ $f=1MHz$		1850		pF
Output Capacitance	C_{oss}			450		
Reverse Transfer Capacitance	C_{rss}			335		
Turn-On Time	$t_{d(on)}$	$V_{DD}=-15V, R_L=15\Omega$ $I_D=-1.0A, V_{GEN}=-10V$ $R_G=6\Omega$		20	30	nS
	t_r			20	30	
Turn-Off Time	$t_{d(off)}$			75	110	
	t_f			40	80	



SPC4516B

N & P Pair Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS (NMOS)

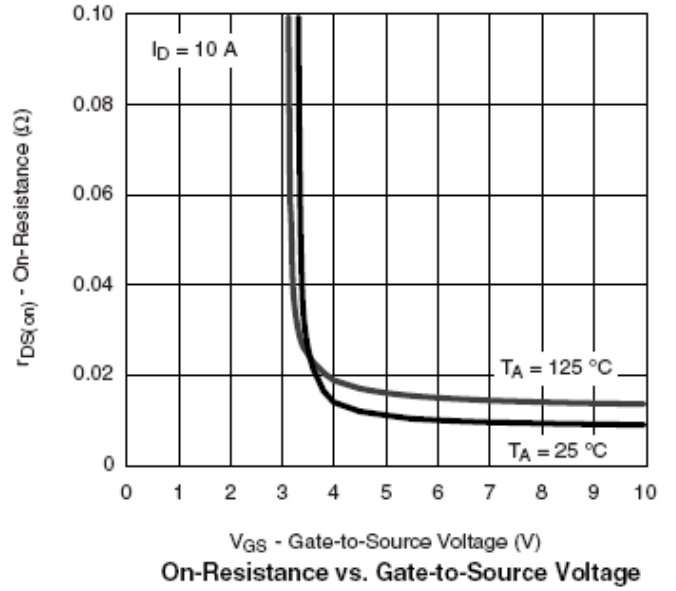
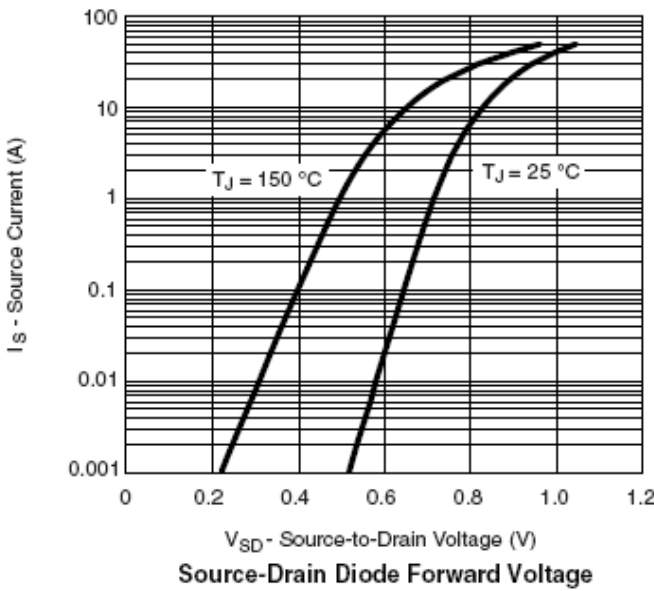
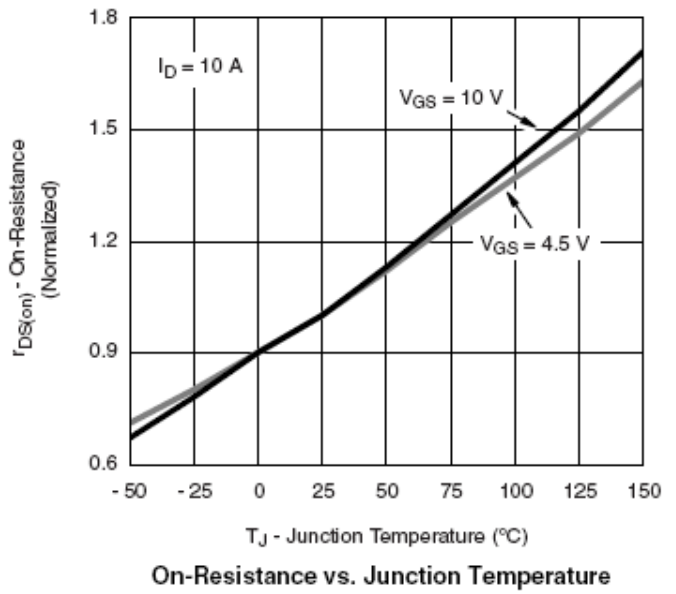
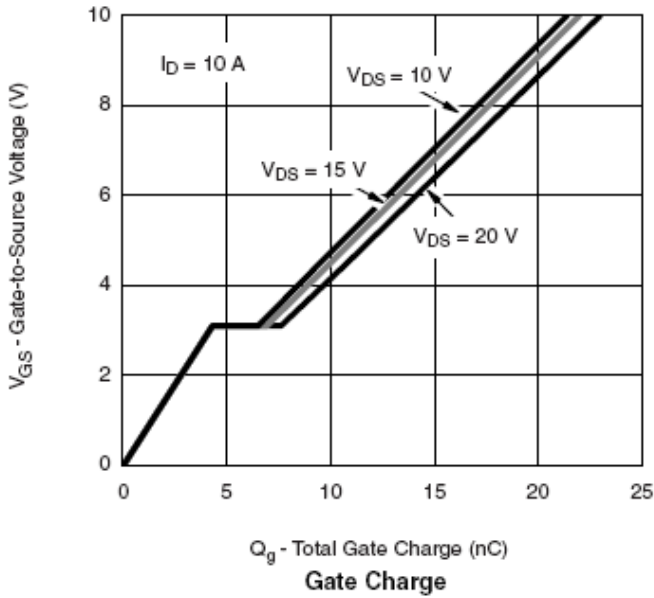




SPC4516B

N & P Pair Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS (NMOS)

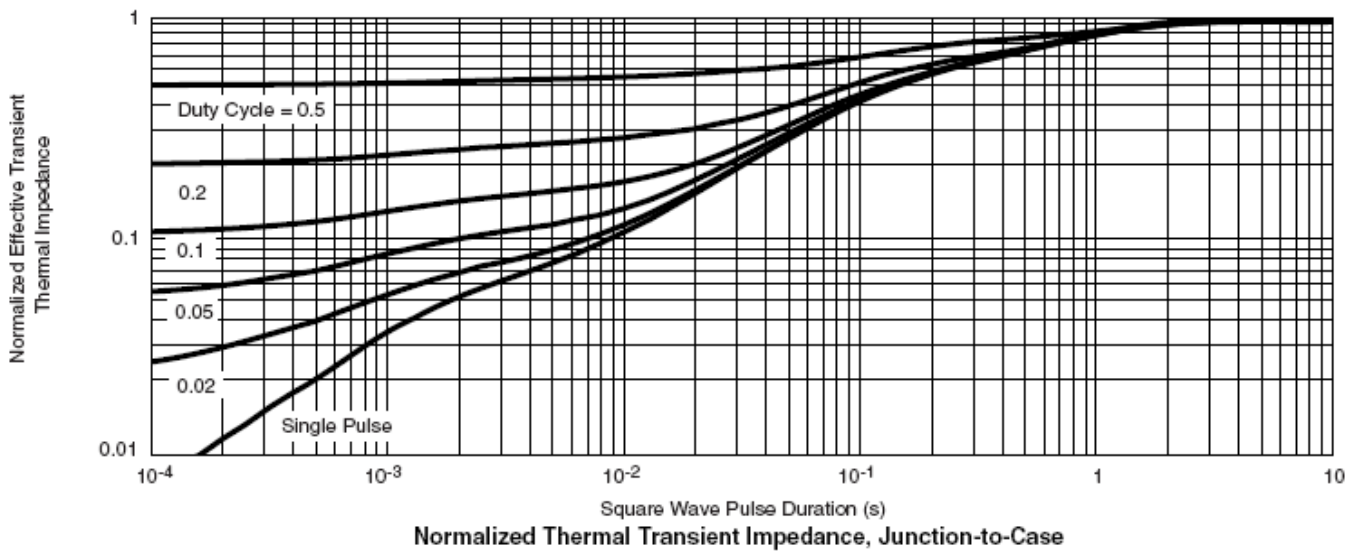
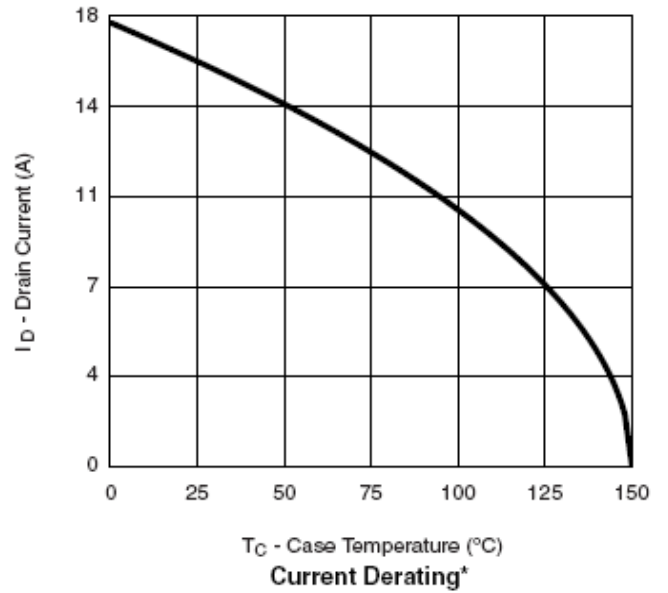
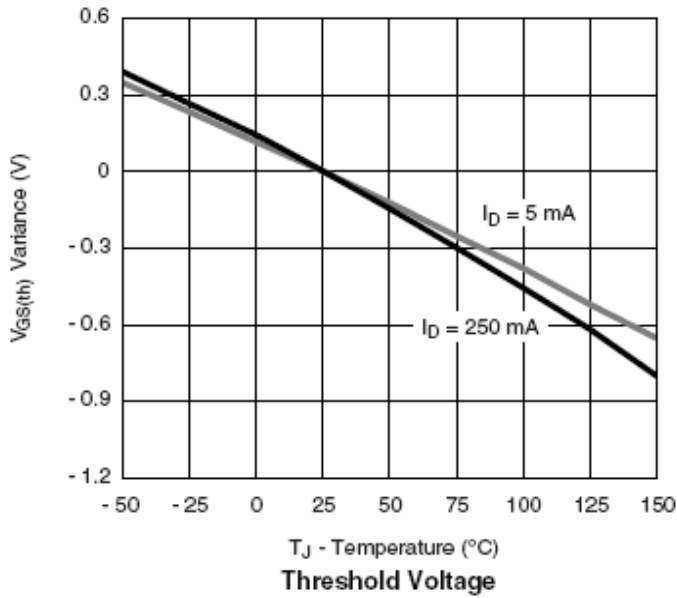




SPC4516B

N & P Pair Enhancement Mode MOSFET

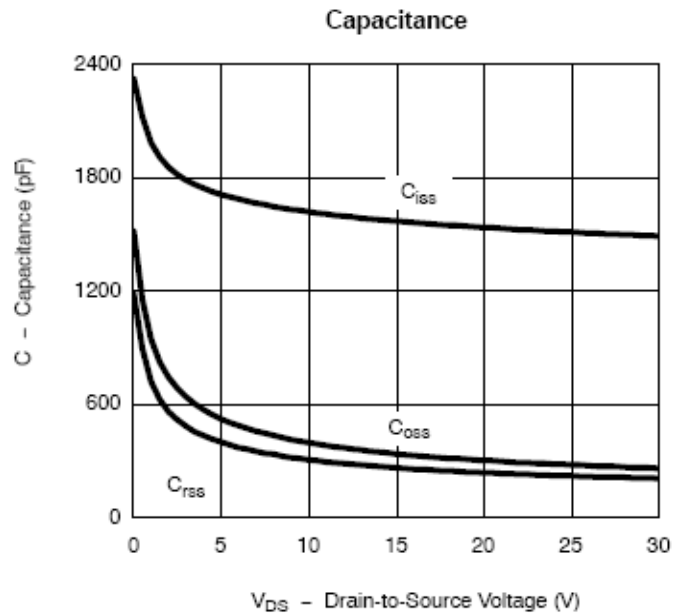
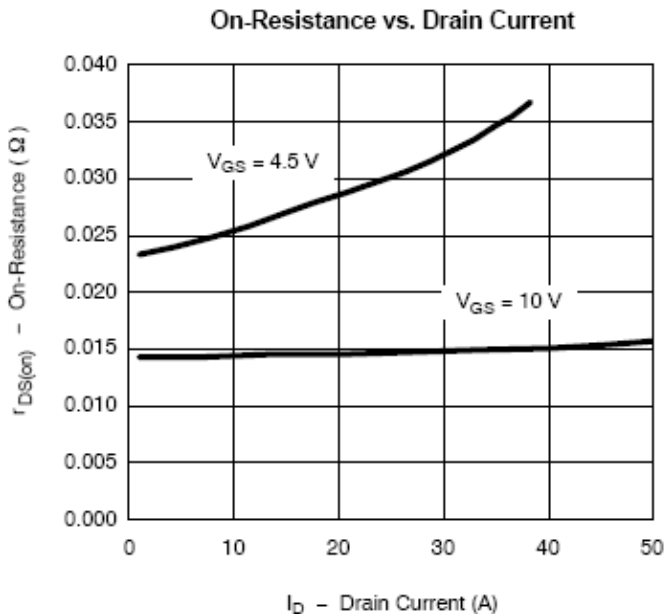
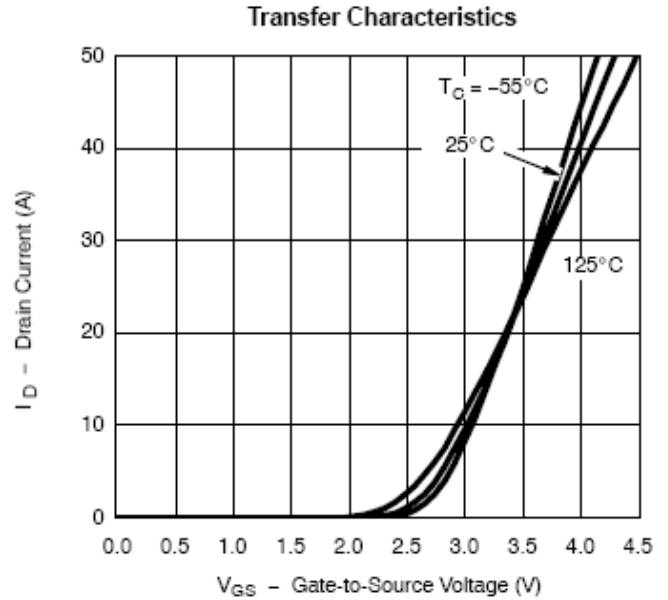
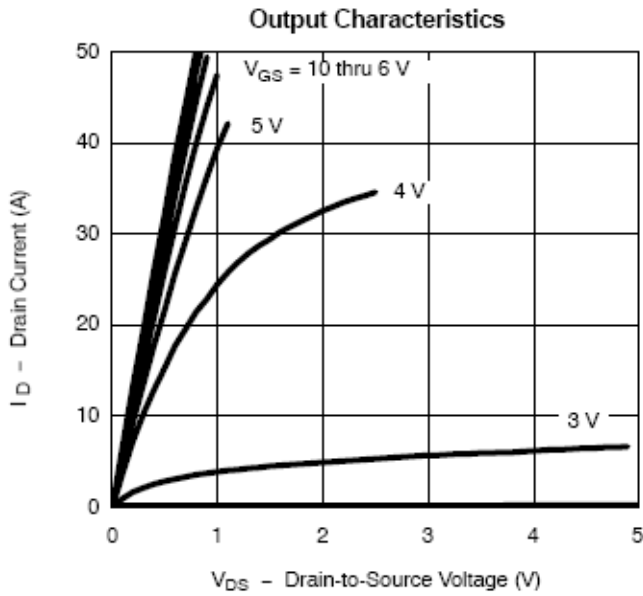
TYPICAL CHARACTERISTICS (NMOS)





SPC4516B N & P Pair Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS (PMOS)

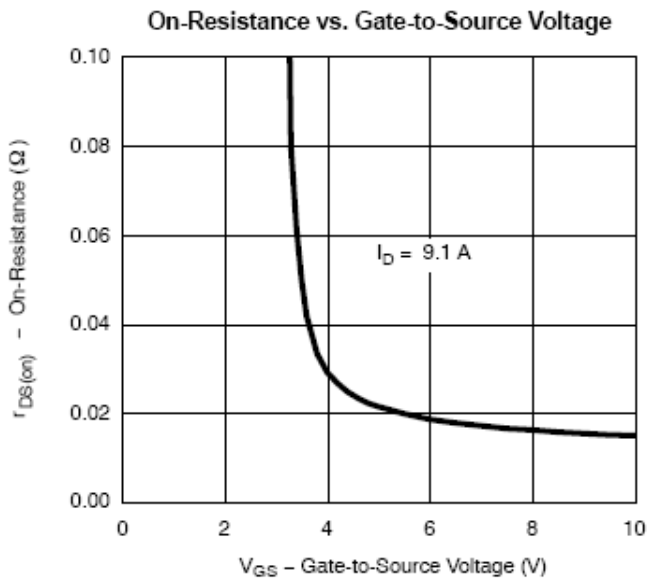
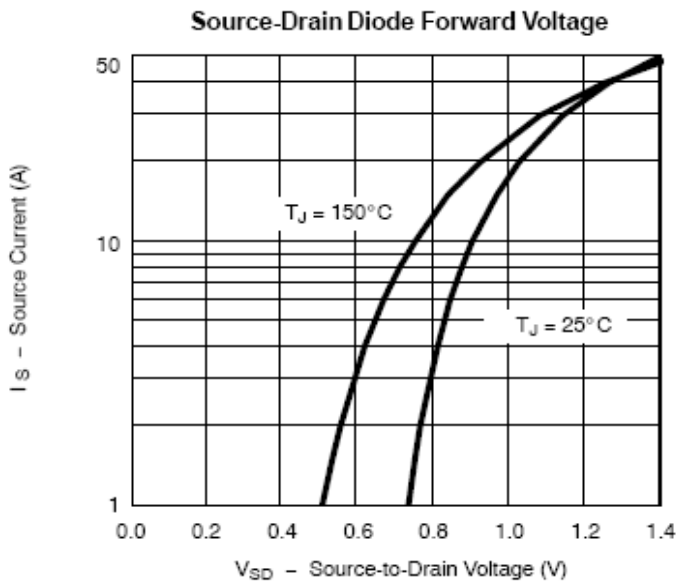
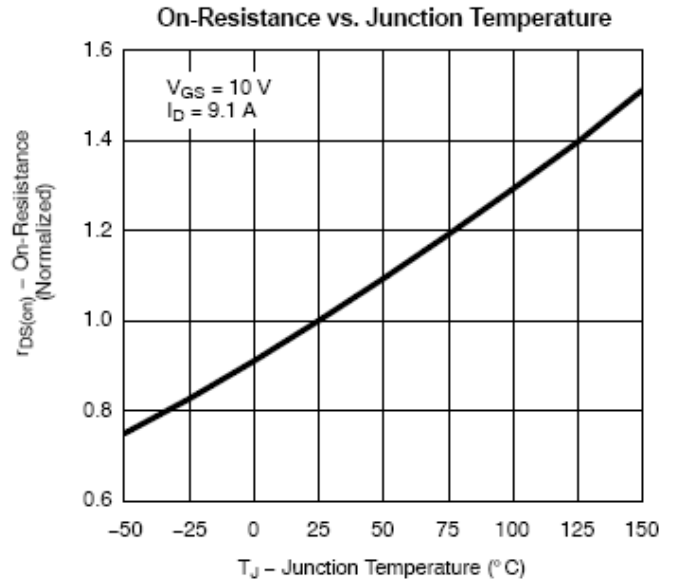
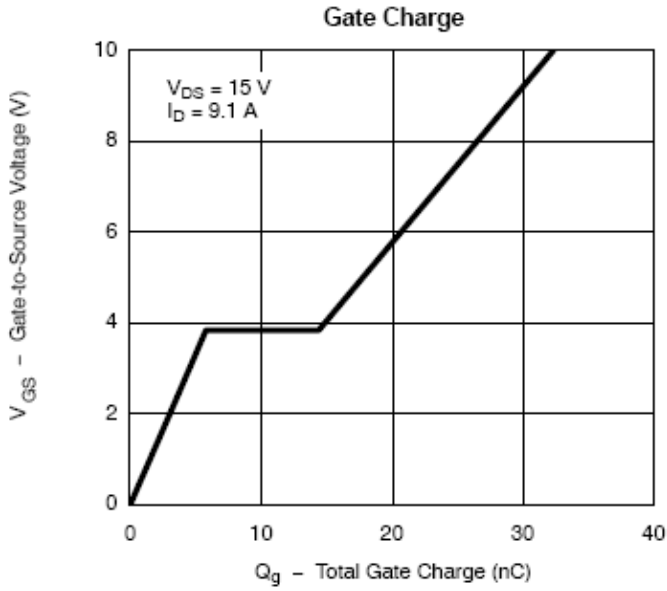




SPC4516B

N & P Pair Enhancement Mode MOSFET

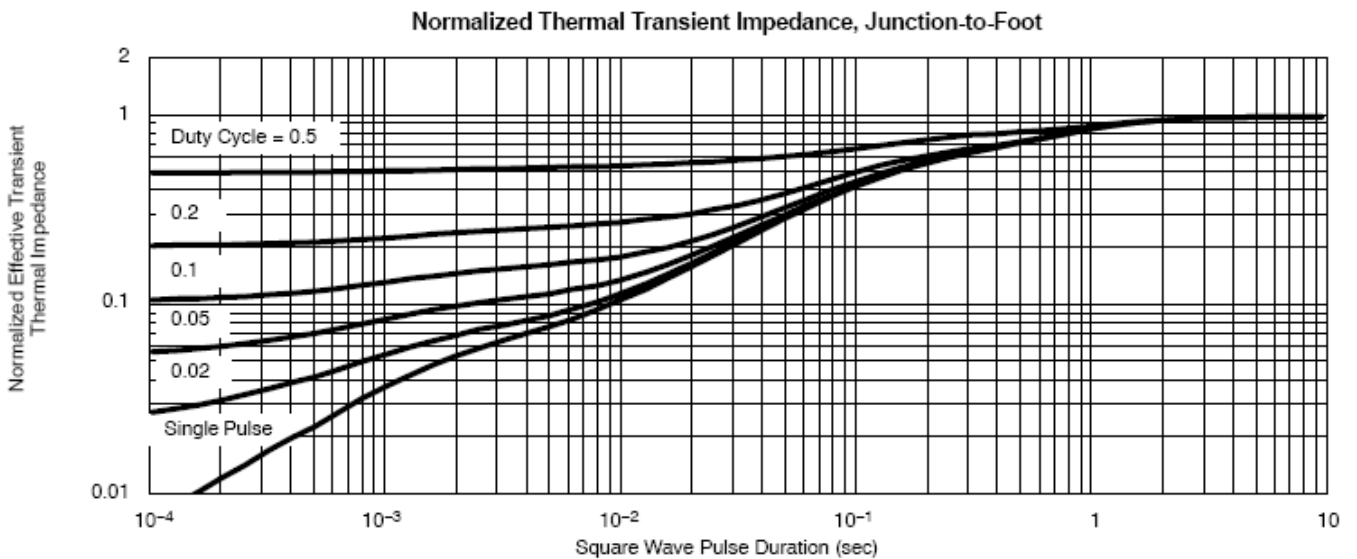
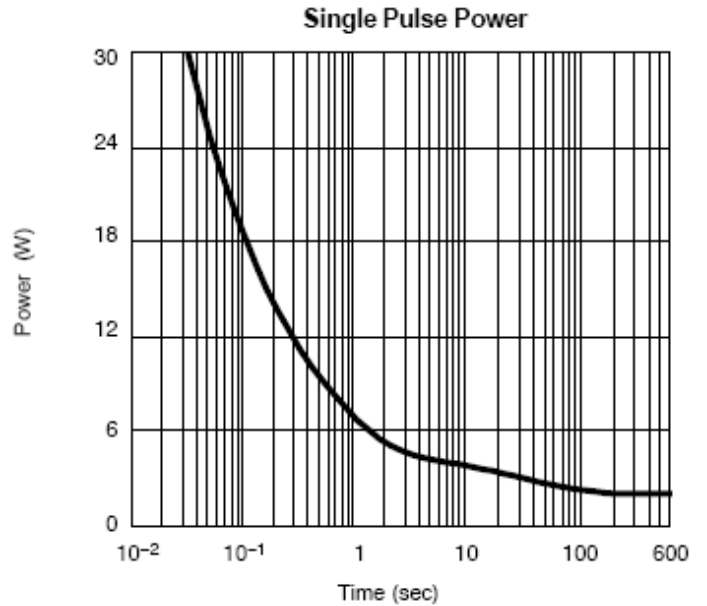
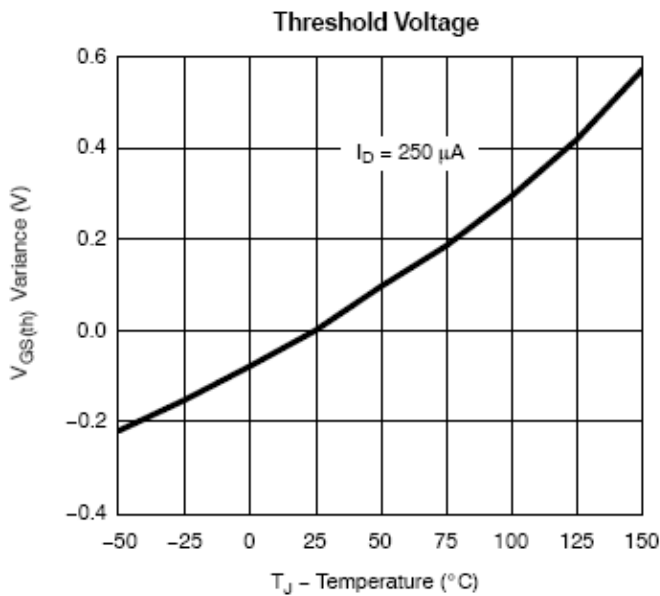
TYPICAL CHARACTERISTICS (PMOS)





SPC4516B N & P Pair Enhancement Mode MOSFET

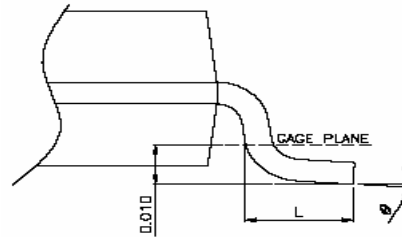
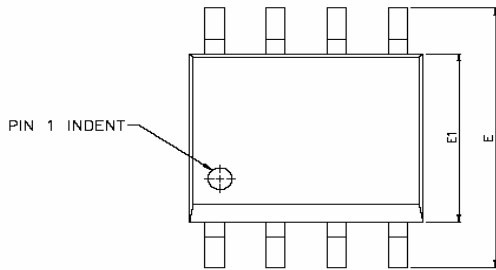
TYPICAL CHARACTERISTICS (PMOS)



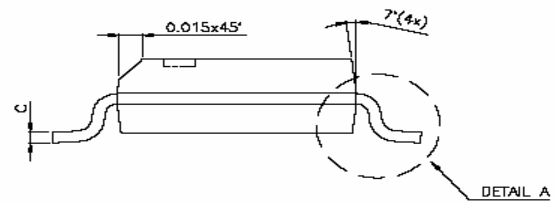
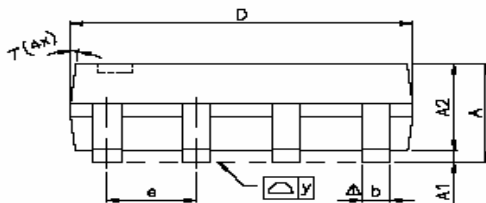


SPC4516B N & P Pair Enhancement Mode MOSFET

SOP- 8 PACKAGE OUTLINE



DETAIL A



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10	—	0.25	0.004	—	0.010
A2	—	1.45	—	—	0.057	—
b	0.33	0.41	0.51	0.013	0.016	0.020
C	0.19	0.20	0.25	0.0075	0.008	0.0098
D	4.80	4.85	4.95	0.189	0.191	0.195
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
e	—	1.27	—	—	0.050	—
L	0.38	0.71	1.27	0.015	0.028	0.050
Δ y	—	—	0.076	—	—	0.003
θ	0°	—	8°	0°	—	8°



SPC4516B

N & P Pair Enhancement Mode MOSFET

Information provided is alleged to be exact and consistent. SYNC Power Corporation presumes no responsibility for the penalties of use of such information or for any violation of patents or other rights of third parties which may result from its use. No license is granted by allegation or otherwise under any patent or patent rights of SYNC Power Corporation. Conditions mentioned in this publication are subject to change without notice. This publication surpasses and replaces all information previously supplied. SYNC Power Corporation products are not authorized for use as critical components in life support devices or systems without express written approval of SYNC Power Corporation.

©The SYNC Power logo is a registered trademark of SYNC Power Corporation

©2004 SYNC Power Corporation – Printed in Taiwan – All Rights Reserved

SYNC Power Corporation

9F-5, No.3-2, Park Street

NanKang District (NKSP), Taipei, Taiwan 115

Phone: 886-2-2655-8178

Fax: 886-2-2655-8468

©<http://www.syncpower.com>