



SPC4533W

N & P Pair Enhancement Mode MOSFET

DESCRIPTION

The SPC4533W is the N- and P-Channel enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching, low in-line power loss, and resistance to transients are needed.

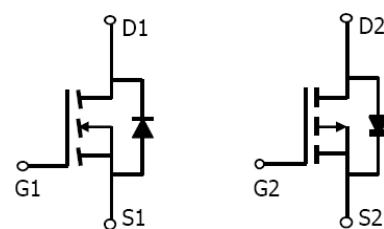
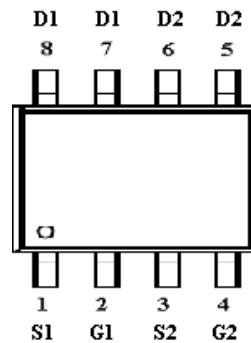
FEATURES

- ◆ N-Channel
 - 30V/10A,R_{DS(ON)}= 25mΩ@V_{GS}= 10V
 - 30V/8.0A,R_{DS(ON)}= 36mΩ@V_{GS}= 4.5V
- ◆ P-Channel
 - 30V/-6.0A,R_{DS(ON)}= 42mΩ@V_{GS}= -10V
 - 30V/-3.0A,R_{DS(ON)}= 78mΩ@V_{GS}= -4.5V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOP – 8P package design

APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- LCD Display inverter

PIN CONFIGURATION(SOP – 8P)



PART MARKING



A : Lot Code
B : Date Code



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PIN DESCRIPTION

Pin	Symbol	Description
1	S1	Source 1
2	G1	Gate 1
3	S2	Source 2
4	G2	Gate 2
5	D2	Drain 2
6	D2	Drain 2
7	D1	Drain 1
8	D1	Drain 1

ORDERING INFORMATION

Part Number	Package	Part Marking
SPC4533WS8RGB	SOP- 8P	SPC4533W

※ SPC4533WS8RGB : 13" Tape Reel ; Pb – Free ; Halogen - Free

ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical		Unit
		N-Channel	P-Channel	
Drain-Source Voltage	V _{DSS}	30	-30	V
Gate –Source Voltage	V _{GSS}	±20	±20	V
Continuous Drain Current(T _J =150°C)	T _A =25°C	ID	10	A
	T _A =70°C		6	
Pulsed Drain Current	I _{DM}	20	-12	A
Power Dissipation	T _A =25°C	P _D	2.0	W
Operating Junction Temperature	T _J	-55/150		°C
Storage Temperature Range	T _{STG}	-55/150		°C
Thermal Resistance-Junction to Ambient	R _{θJA}	80	80	°C/W



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N CHANNEL ELECTRICAL CHARACTERISTICS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V, ID=250uA	30			V
Gate Threshold Voltage	VGS(th)	VDS=VGS, ID=250uA	1.0		2.5	
Gate Leakage Current	IGSS	VDS=0V, VGS=±20V			±100	nA
Zero Gate Voltage Drain Current	IDSS	VDS=24V, VGS=0V			1	uA
		VDS=24V, VGS=0V TJ=55°C			5	
On-State Drain Current	ID(on)	VDS≥5V, VGS =10V	25			A
Drain-Source On-Resistance	RDS(on)	VGS= 10V, ID=10A		18	25	mΩ
		VGS=4.5V, ID=5.6A		25	36	
Forward Transconductance	gfs	VDS=15V, ID=10A		10		S
Diode Forward Voltage	VSD	IS=1A, VGS =0V			1.2	V
Dynamic						
Total Gate Charge	Qg	VDS=20V, VGS=4.5V ID= 10A		7.2		nC
Gate-Source Charge	Qgs			1.4		
Gate-Drain Charge	Qgd			2.2		
Input Capacitance	Ciss	VDS=15V GS=0V f=1MHz		570		pF
Output Capacitance	Coss			81		
Reverse Transfer Capacitance	Crss			65		
Turn-On Time	td(on)	VDD=12V, ID=5.0A, VGEN=10V, RG=3.3Ω		4.1		nS
	tr			9.8		
Turn-Off Time	td(off)			15.5		
	tf			6.1		



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P CHANNEL ELECTRICAL CHARACTERISTICS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, ID=-250uA	-30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , ID=-250uA	-1.0		-2.5	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V, V _{GS} =0V			-1	uA
		V _{DS} =-24V, V _{GS} =0V T _J =55°C			-5	
On-State Drain Current	I _{D(on)}	V _{DS} ≤-5V, V _{GS} =-10V	-6			A
Drain-Source On-Resistance	R _{DSS(on)}	V _{GS} =-10V, ID=-6A		0.035	0.042	Ω
		V _{GS} =-4.5V, ID=-3A		0.065	0.078	
Forward Transconductance	g _{fs}	V _{DS} =-10.0V, ID=-6A		6		S
Diode Forward Voltage	V _{SD}	I _S =-6A, V _{GS} =0V			-1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =-20V, V _{GS} =-4.5V ID=-6A		6.4		nC
Gate-Source Charge	Q _{gs}			2.7		
Gate-Drain Charge	Q _{gd}			3.1		
Input Capacitance	C _{iss}	V _{DS} =-24V, V _{GS} =0V f=1MHz		650		pF
Output Capacitance	C _{oss}			270		
Reverse Transfer Capacitance	C _{rss}			104		
Turn-On Time	t _{d(on)}	V _{DD} =-12V, ID=-5.0A, V _{GEN} =-10V R _G =3.3Ω		9		ns
	t _r			16		
Turn-Off Time	t _{d(off)}			21		
	t _f			22		



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N CHANNEL TYPICAL CHARACTERISTICS

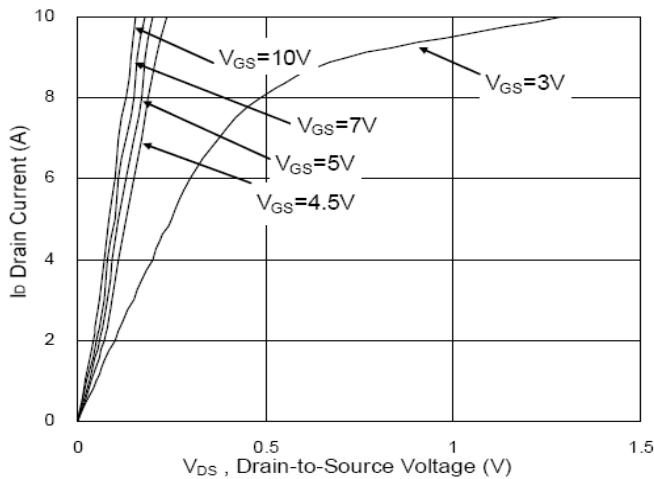


Fig. 1 Typical Output Characteristics

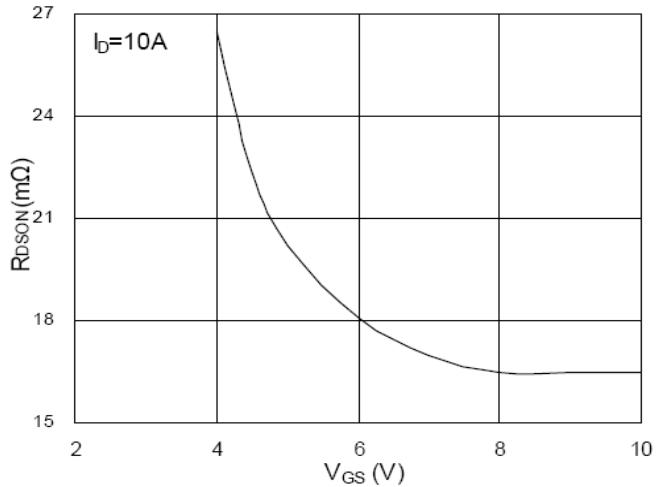


Fig. 2 On-Resistance vs. Gate Voltage

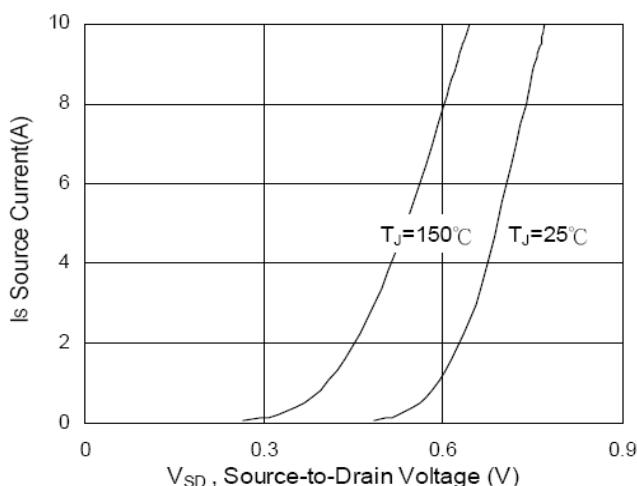


Fig. 3 Forward Characteristics of Diode

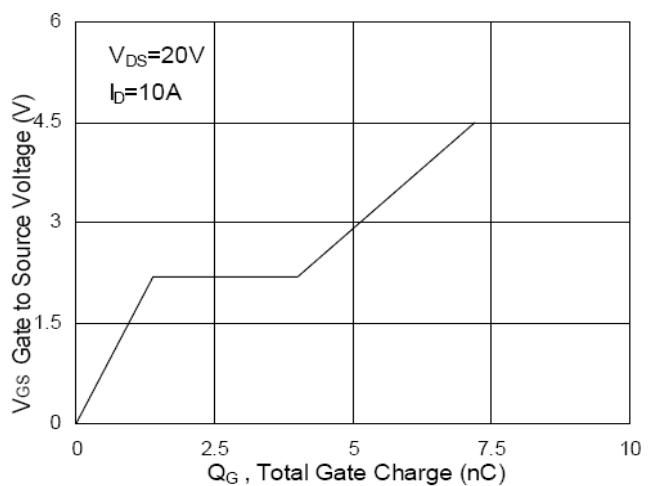


Fig. 4 Gate Charge Characteristics

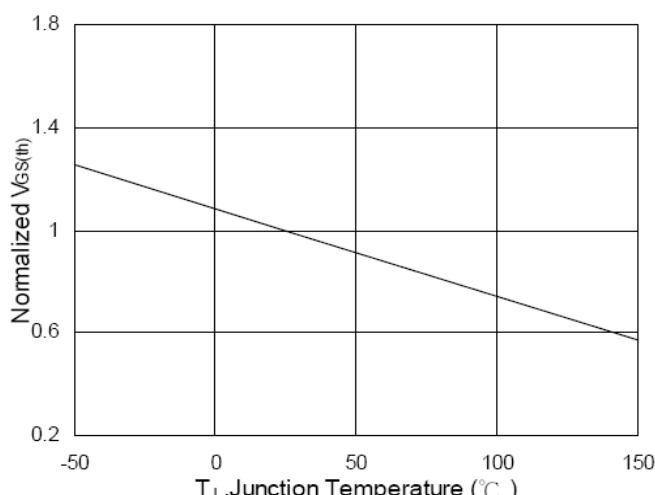


Fig. 5 V_{GS} vs. Junction Temperature

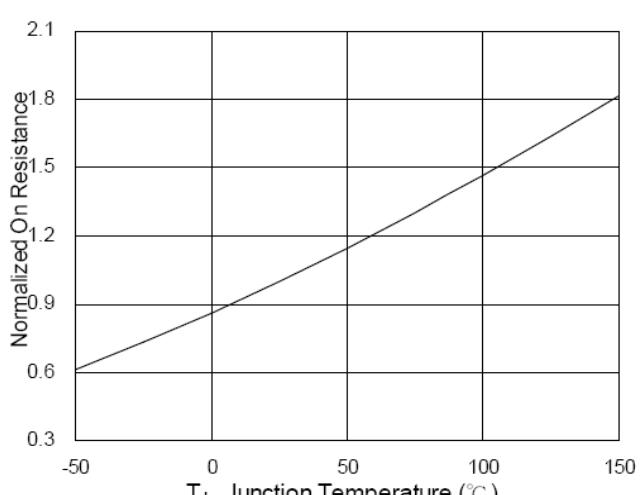


Fig. 6 On-Resistance vs. Junction Temperature



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N CHANNEL TYPICAL CHARACTERISTICS

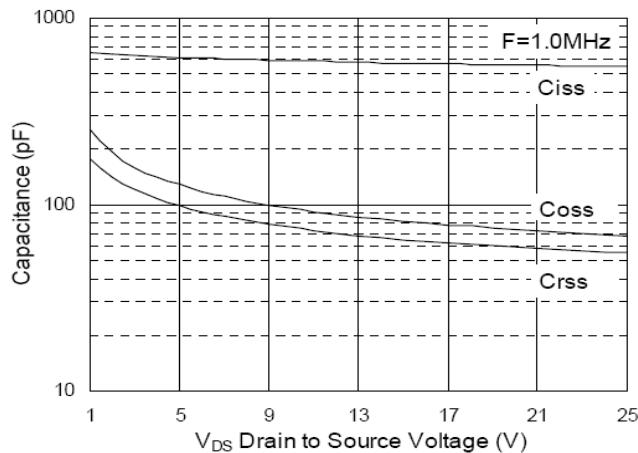


Fig. 7 Typical Capacitance Characteristics

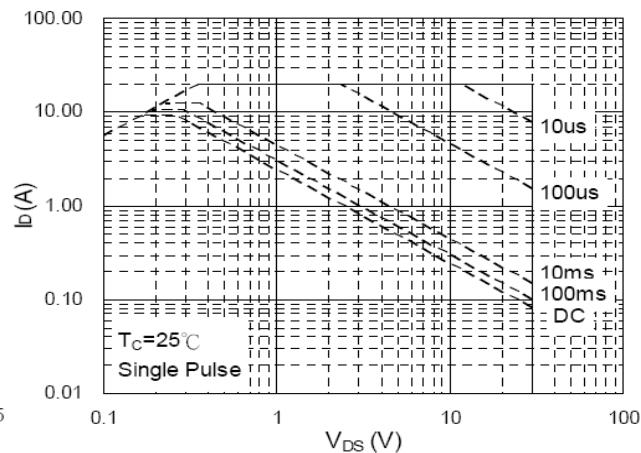


Fig. 8 Maximum Safe Operation Area

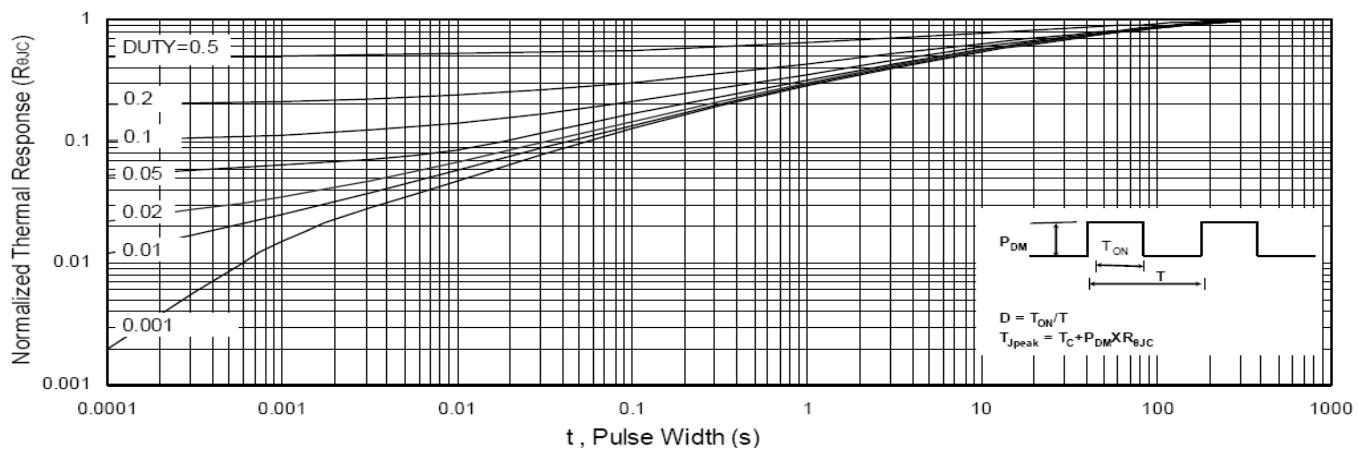


Fig. 9 Effective Transient Thermal Impedance

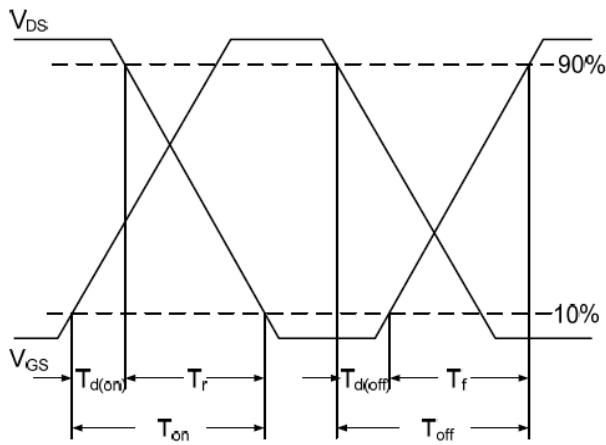


Fig. 10 Switching Time Waveform

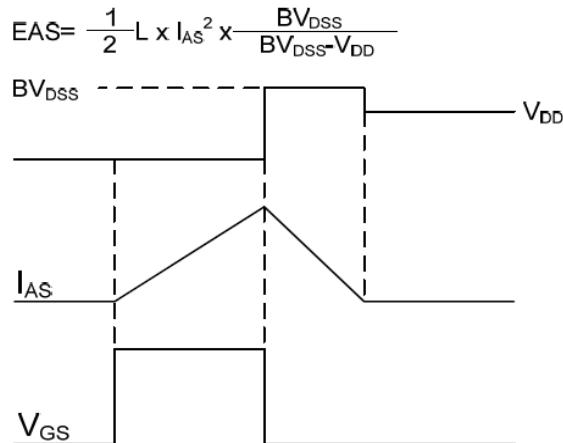


Fig. 11 Unclamped Inductive Waveform



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P CHAENL TYPICAL CHARACTERISTICS

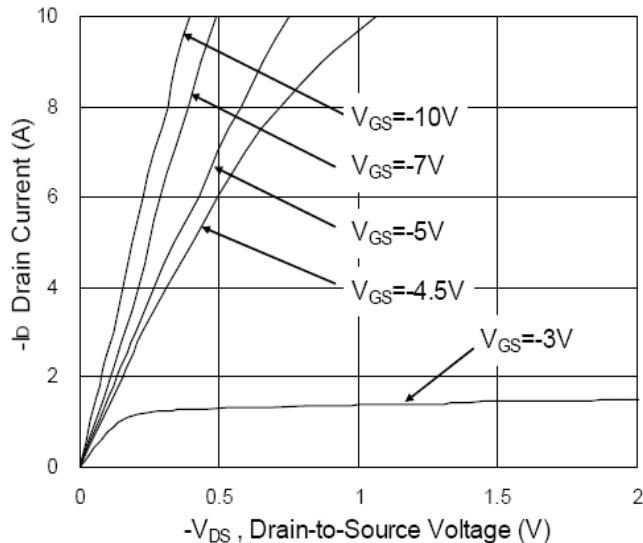


Fig. 1 Typical Output Characteristics

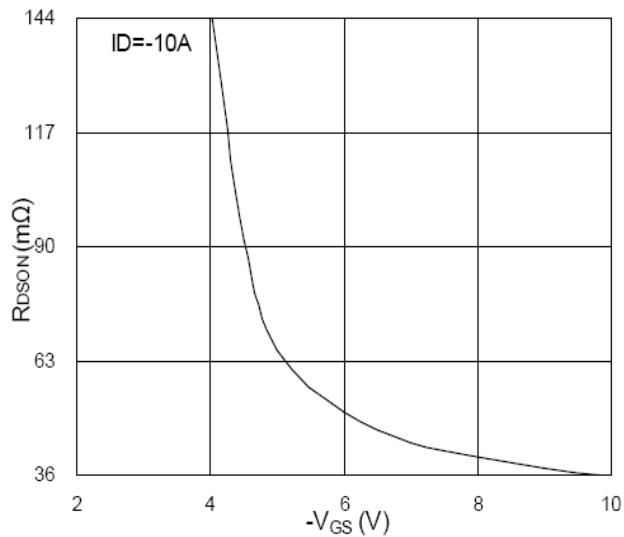


Fig. 2 On-Resistance vs. Gate Voltage

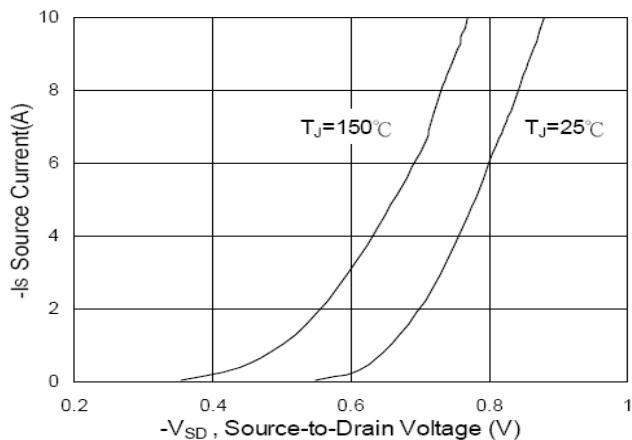


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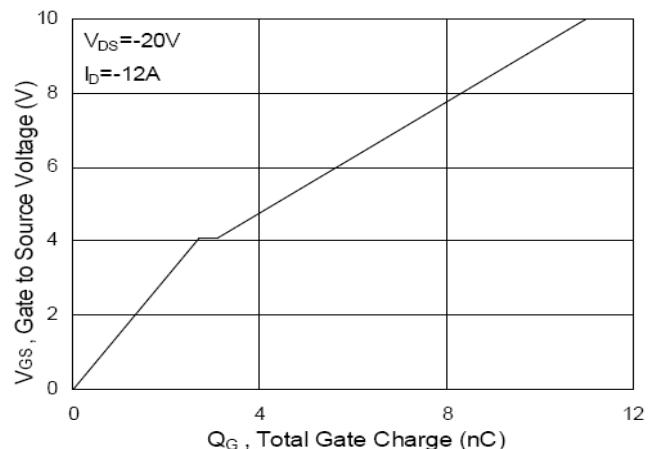


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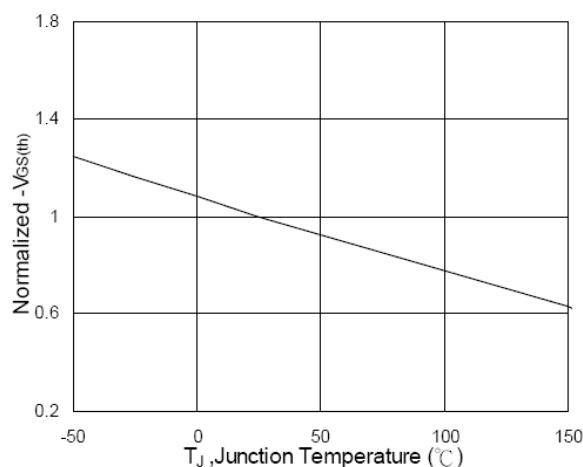


Fig. 5 Vgs vs. Junction Temperature

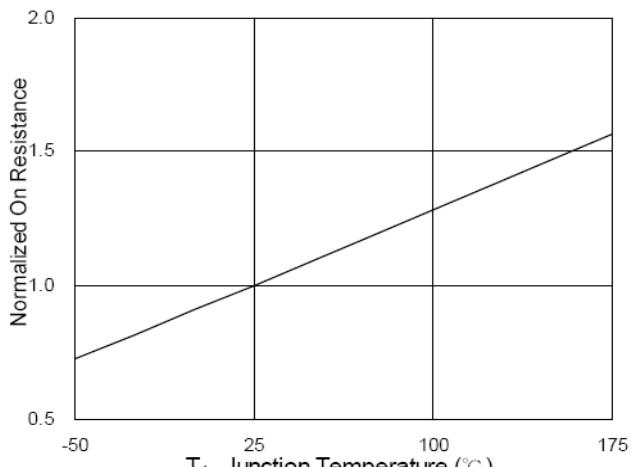


Fig. 6 On-Resistance vs Junction Temp



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P CHANNEL TYPICAL CHARACTERISTICS

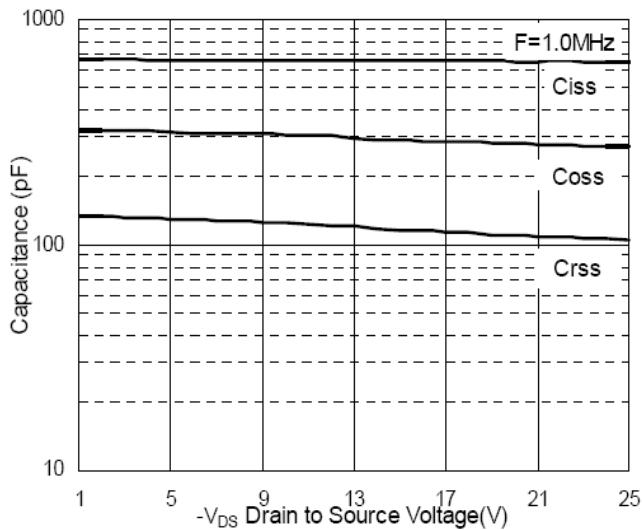


Fig. 7 Typical Capacitance Characteristics

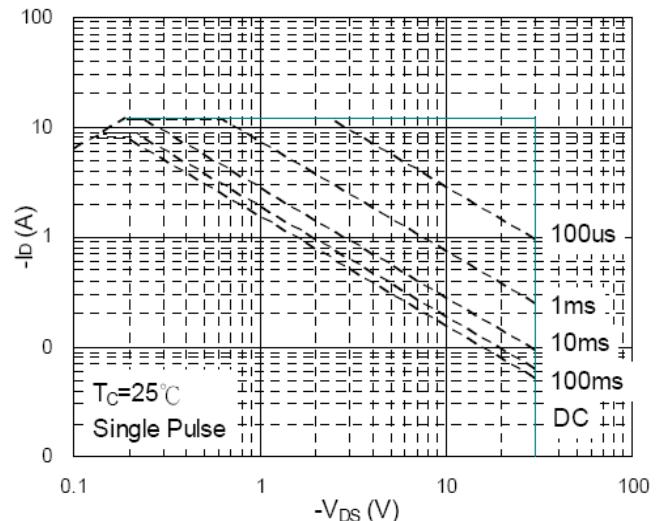


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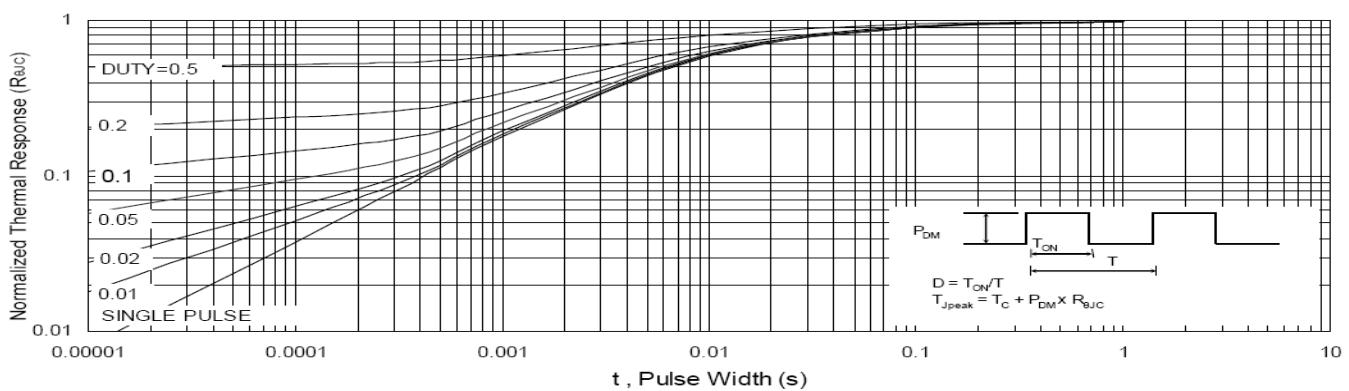


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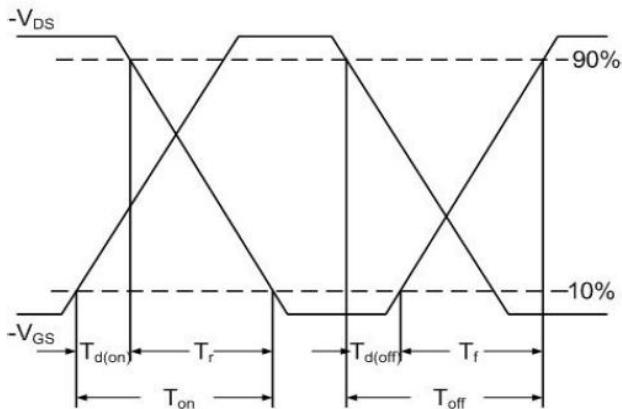


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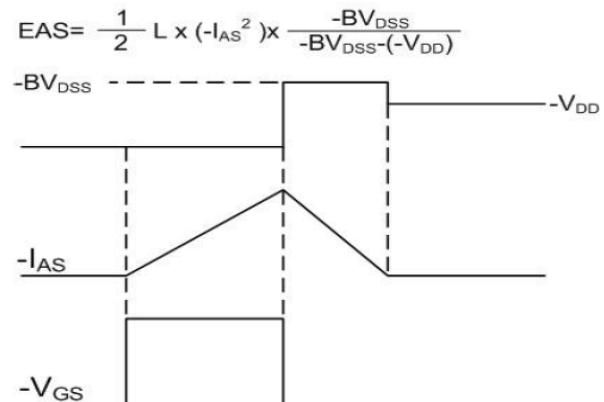


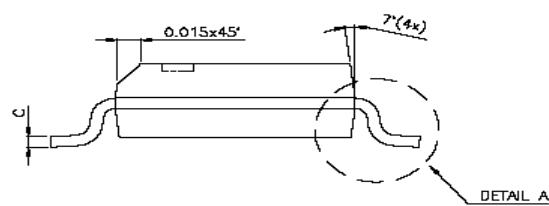
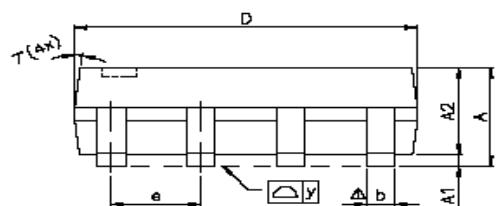
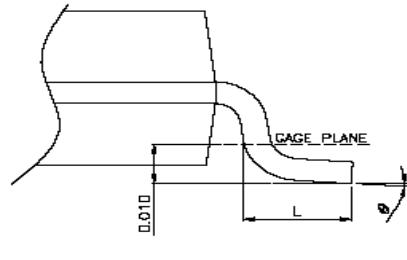
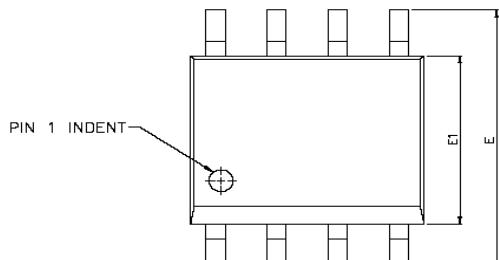
Fig. 11 Unclamped Inductive Waveform



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SOP- 8 PACKAGE OUTLINE



SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10	—	0.25	0.004	—	0.010
A2	—	1.45	—	—	0.057	—
b	0.33	0.41	0.51	0.013	0.016	0.020
C	0.19	0.20	0.25	0.0075	0.008	0.0098
D	4.80	4.85	4.95	0.189	0.191	0.195
E	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
e	—	1.27	—	—	0.050	—
L	0.38	0.71	1.27	0.015	0.028	0.050
$\triangle y$	—	—	0.076	—	—	0.003
θ	0°	—	8°	0°	—	8°



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