



SPP3467

P-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPP3467 is the P-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application such as cellular phone and notebook computer power management and other battery powered circuits, and low in-line power loss are needed in a very small outline surface mount package.

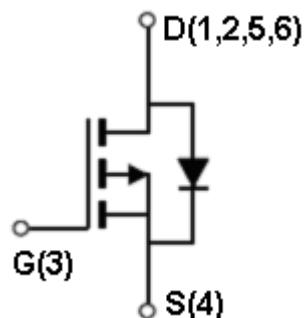
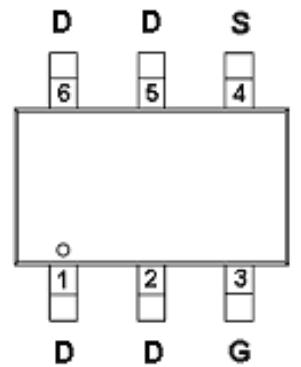
APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

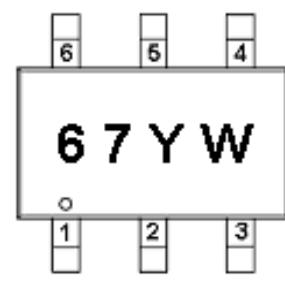
FEATURES

- ◆ -20V/-5.0A,R_{DS(ON)}= 90mΩ@V_{GS}=-4.5V
- ◆ -20V/-3.5A,R_{DS(ON)}=110mΩ@V_{GS}=-2.5V
- ◆ -20V/-1.7A,R_{DS(ON)}=140mΩ@V_{GS}=-1.8V
- ◆ Super high density cell design for extremely low R_{DS (ON)}
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOP-23-6P package design

PIN CONFIGURATION(SOT-23-6P)



PART MARKING





SPP3467

P-Channel Enhancement Mode MOSFET

PIN DESCRIPTION

Pin	Symbol	Description
1	D	Drain
2	D	Drain
3	G	Gate
4	S	Source
5	D	Drain
6	D	Drain

ORDERING INFORMATION

Part Number	Package	Part Marking
SPP3467S26RGB	SOT-23-6P	67YW

※ Week Code : A ~ Z(1 ~ 26) ; a ~ z(27 ~ 52)

※ SPP3467S26RGB : Tape Reel ; Pb – Free ; Halogen - Free

ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	-20	V
Gate –Source Voltage	V _{GSS}	±12	V
Continuous Drain Current(T _J =150°C)	T _A =25°C	ID	-5.2
	T _A =70°C		-4.2
Pulsed Drain Current	I _{DM}	-20	A
Continuous Source Current(Diode Conduction)	I _S	-1.7	A
Power Dissipation	T _A =25°C	P _D	2.0
	T _A =70°C		1.3
Operating Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient	R _{θJA}	90	°C/W



SPP3467

P-Channel Enhancement Mode MOSFET

ELECTRICAL CHARACTERISTICS

(TA=25°C Unless otherwise noted)

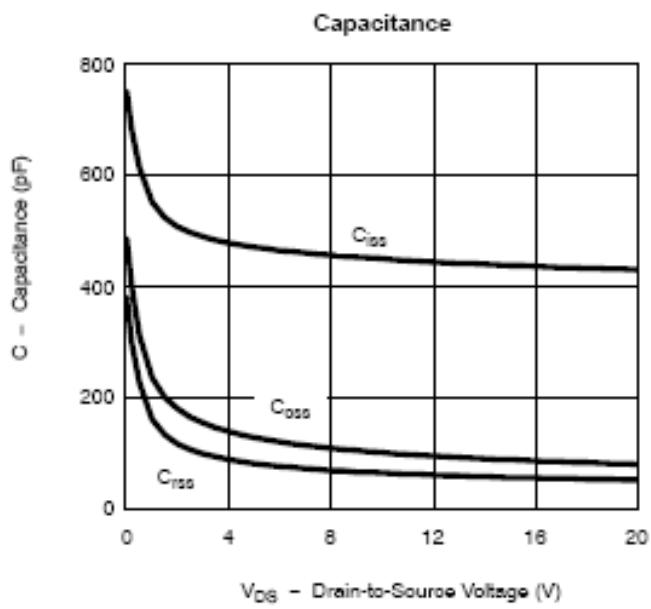
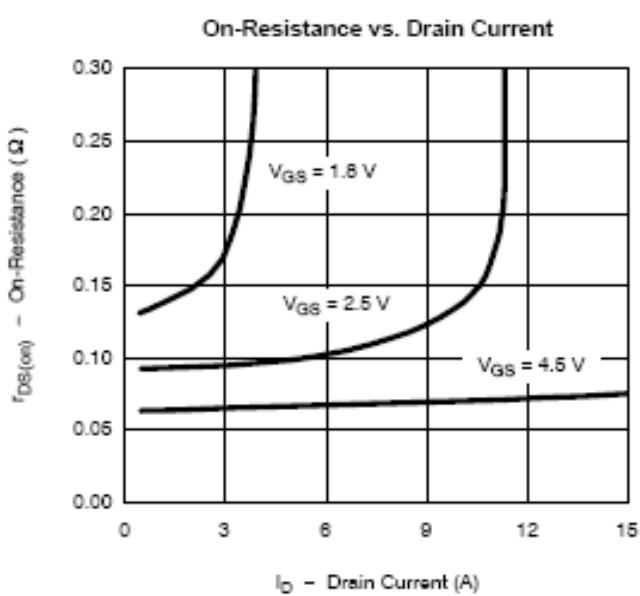
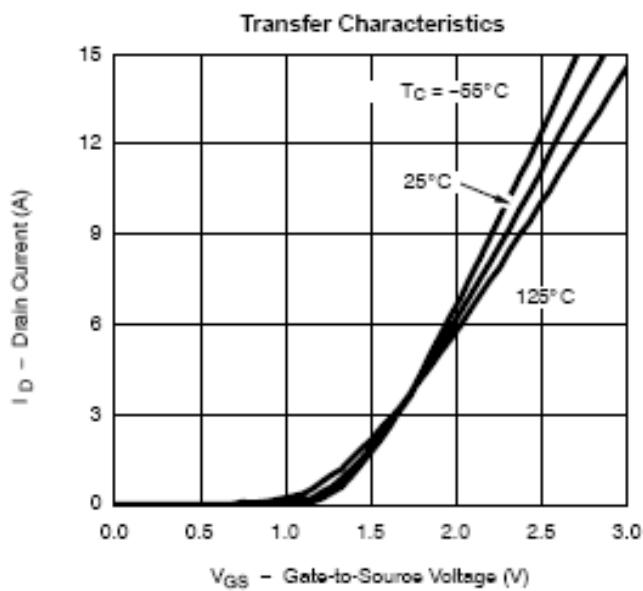
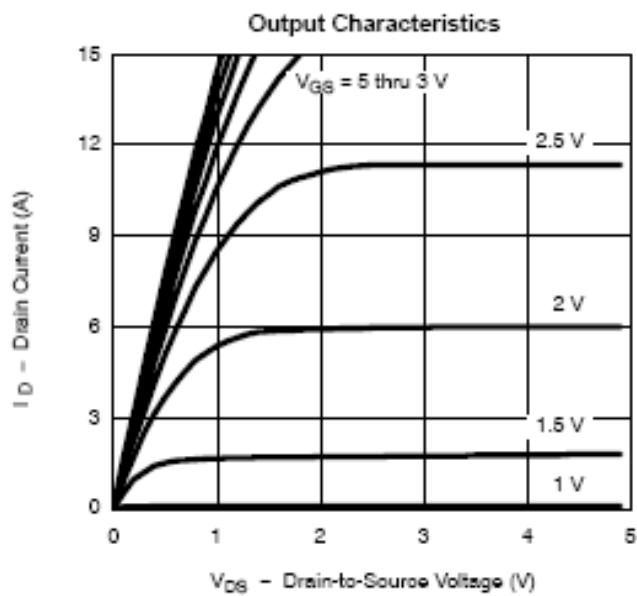
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, ID=-250uA	-20			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , ID=-250uA	-0.35		-0.8	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V, V _{GS} =0V			-1	uA
		V _{DS} =-20V, V _{GS} =0V T _J =55°C			-5	
On-State Drain Current	I _{D(on)}	V _{DS} ≤-5V, V _{GS} =-4.5V	-6			A
Drain-Source On-Resistance	R _{DSS(on)}	V _{GS} =-4.5V, ID=-5.0A		0.075	0.090	Ω
		V _{GS} =-2.5V, ID=-3.5A		0.090	0.110	
		V _{GS} =-1.8V, ID=-1.7A		0.120	0.140	
Forward Transconductance	g _{fs}	V _{DS} =-5V, ID=-2.8A		6		S
Diode Forward Voltage	V _{SD}	I _S =-1.5A, V _{GS} =0V		-0.8	-1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =-6V, V _{GS} =-4.5V ID=-2.8A		4.8	8	nC
Gate-Source Charge	Q _{gs}			1.0		
Gate-Drain Charge	Q _{gd}			1.0		
Input Capacitance	C _{iss}	V _{DS} =-6V, V _{GS} =0V f=1MHz		485		pF
Output Capacitance	C _{oss}			85		
Reverse Transfer Capacitance	C _{rss}			40		
Turn-On Time	t _{d(on)}	V _{DD} =-6V, R _L =6Ω ID=-1.0A, V _{GEN} =-4.5V R _G =6Ω		10	16	ns
	t _r			13	23	
Turn-Off Time	t _{d(off)}			18	25	
	t _f			15	20	



SPP3467

P-Channel Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS

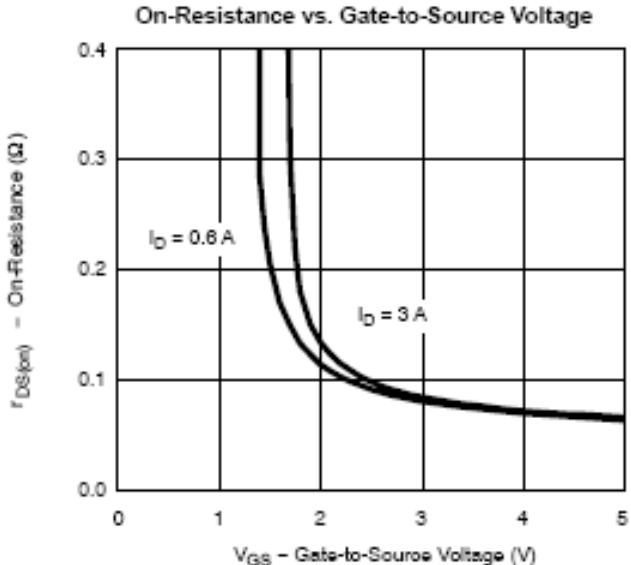
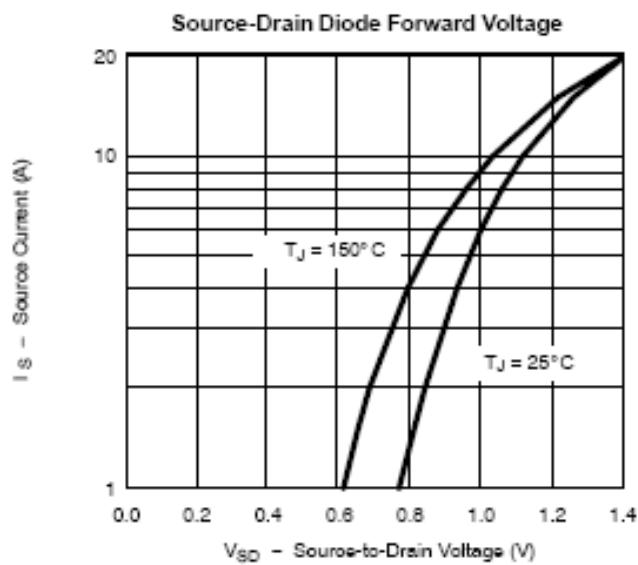
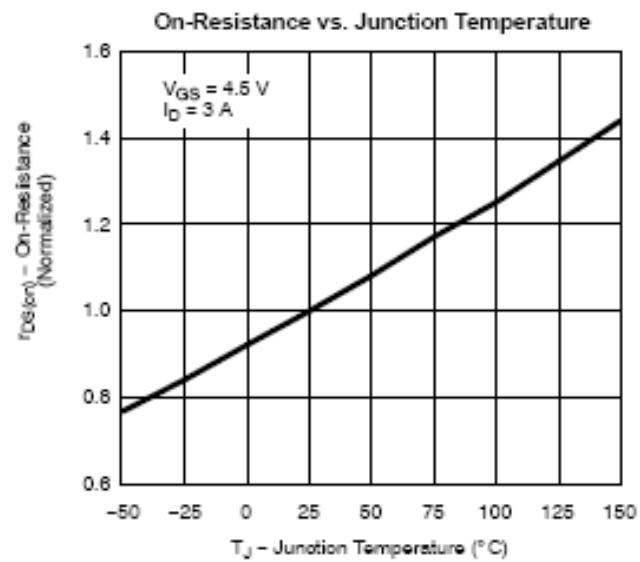
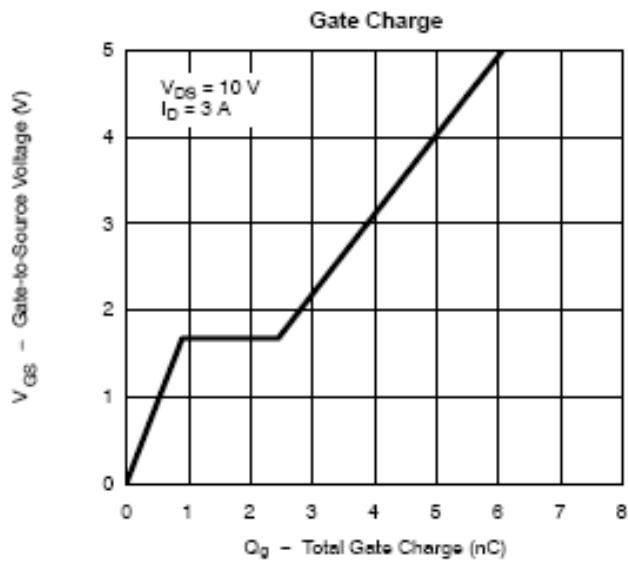




SPP3467

P-Channel Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS

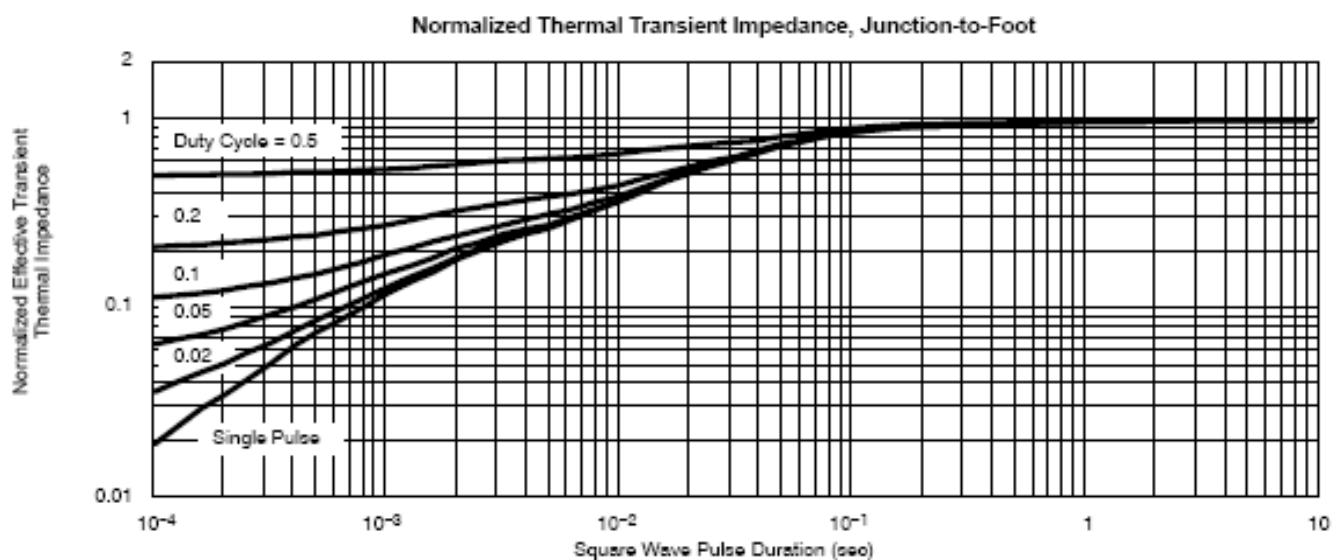
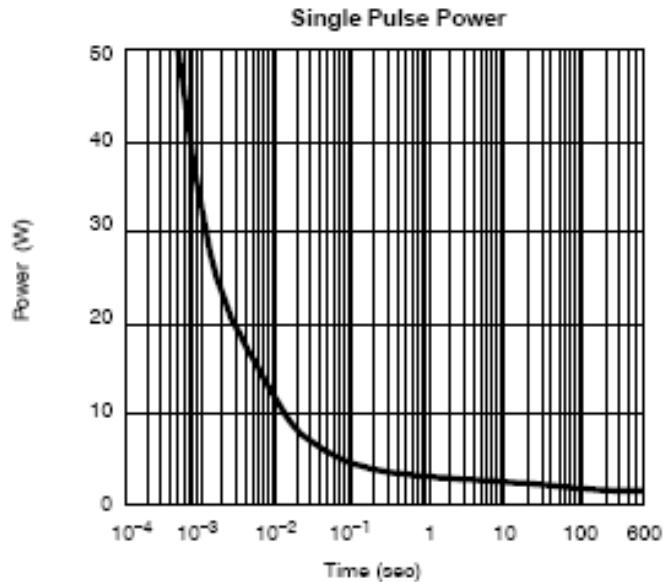
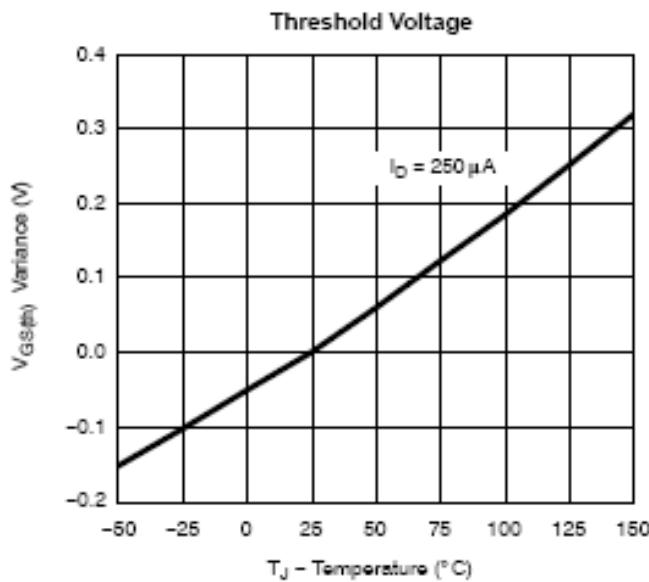




SPP3467

P-Channel Enhancement Mode MOSFET

TYPICAL CHARACTERISTICS

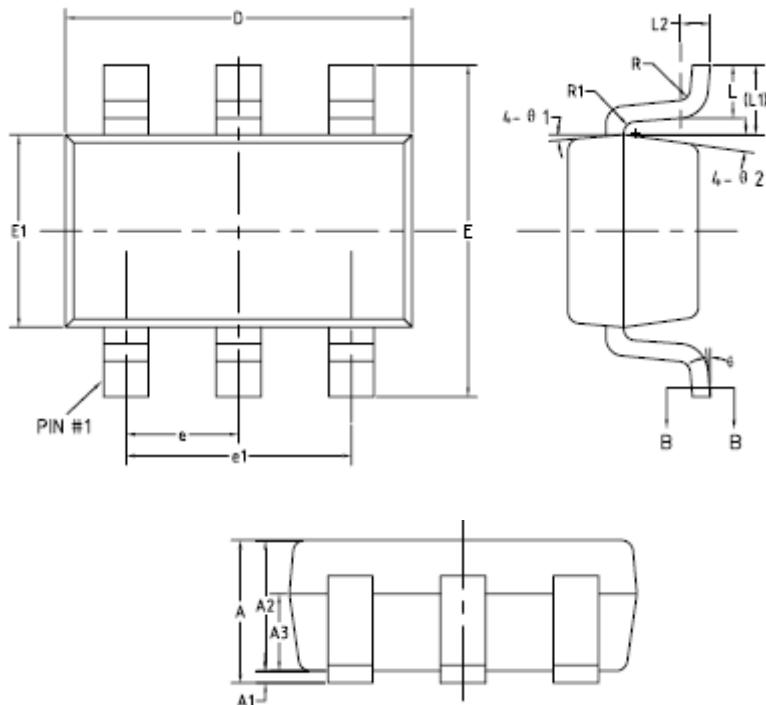




SPP3467

P-Channel Enhancement Mode MOSFET

SOT-23- 6P PACKAGE OUTLINE



COMMON DIMENSIONS
(UNITS OF MEASURE—MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	—	—	1.45
A1	0	—	0.15
A2	0.90	1.10	1.30
A3	0.60	0.65	0.70
b	0.39	—	0.49
b1	0.38	0.40	0.45
c	0.12	—	0.19
c1	0.11	0.13	0.15
D	2.85	2.95	3.05
E	2.60	2.80	3.00
E1	1.55	1.65	1.75
e	0.85	0.95	1.05
e1	1.80	1.90	2.00
L	0.35	0.45	0.60
L1	0.59REF		
L2	0.25BSC		
R	0.05	—	—
R1	0.05	—	0.20
θ	0°	—	8°
θ 1	8°	10°	12°
θ 2	8°	10°	12°



SPP3467

P-Channel Enhancement Mode MOSFET

Information provided is alleged to be exact and consistent. SYNC Power Corporation presumes no responsibility for the penalties of use of such information or for any violation of patents or other rights of third parties which may result from its use. No license is granted by allegation or otherwise under any patent or patent rights of SYNC Power Corporation. Conditions mentioned in this publication are subject to change without notice. This publication surpasses and replaces all information previously supplied. SYNC Power Corporation products are not authorized for use as critical components in life support devices or systems without express written approval of SYNC Power Corporation.

© The SYNC Power logo is a registered trademark of SYNC Power Corporation
© 2004 SYNC Power Corporation – Printed in Taiwan – All Rights Reserved
SYNC Power Corporation
7F-2, No.3-1, Park Street
NanKang District (NKSP), Taipei, Taiwan 115
Phone: 886-2-2655-8178
Fax: 886-2-2655-8468
© <http://www.syncpower.com>
